

LME78_2.0 series

Wide Input Non-Isolated & Regulated, Single Output



Switching Regulator

- High efficiency up to 96%
 Operating temperature range:
- -40°C ~ +100°C
- Short circuit protection (SCP)
 High voltage input range, up to 36V
- 🕂 3PIN SIP package
- 🕂 Non isolated
- Very low standby current
- 🕂 UL94V-0 package material

SHORT CIRCUIT PROTECTED Campliant 66

Common specifications	
Short circuit protection:	Hiccup, automatic recovery
Thermal impedance:	34°C/W, MIN Mounting at FR4 (1.18*1.18inch) PCB
Cooling:	Nature convection
Operation temperature range:	-40°C~+100°C (see temperature derating curve)
Storage temperature range:	-55°C ~+125°C
Soldering temperature:	260°C MAX, 1.5mm from case for 10 sec
Maximum case temperature:	105°C
Storage humidity range:	< 95%RH
Package material:	Plastic [UL94-V0]
MTBF (MIL-HDBK-217F @25°C):	5VDC input: 16Mhrs, min24VDC input: 2.6Mhrs, min
Safety standard (design to meet):	IEC/EN 60950-1, IEC/EN 62368-1
Weight:	2.4g
Dimensions:	14 x 7.5 x 10.1mm

Input specifications					
Item	Test conditions	Fest conditions Min Typ Ma		Max	Units
Input surge voltage	 5VDC input 24VDC input		6 40		VDC VDC
Start up time	nominal Vin, constant resistive load		5		ms
Input reflected ripple current*			35		mA pk-pk
Input Filter	Capacitor Filter				

* Measured through a source indicator L1 (12 $\mu H)$ and a source capacitor C1 (10 $\mu H)$ at nominal input and full load.

Example: LME78_05-2.0

LM = Series; E = cost effective, 05 = 5Vout; 2.0 = 2.0A

Note:

- 1. Do not operate exceeding the absolut maximum rating, it will cause damage;
- 2. Operation unter no-load conditions will not damage these devices, however they may not meet all listed specifications;
- 3. Unless otherwise specified, data in this datasheet should be tested under the conditions of Ta = 25° C, humidity <75% when inputting nominal voltage and outputting rated load;
- All index testing methods in this datasheet are based on our company's corporate standards;
- 5. Specifications subject to change without prior notice.

Introducing our high-efficiency LME78_2.0 series, designed to deliver up to 96% efficiency and operate within a broad temperature range of -40°C
to +100°C. Housed in a compact 3PIN SIP package, this non-isolated con-
verter is ideal for applications requiring a high voltage input range, ac-
commodating up to 36V. It features robust short circuit protection (SCP)
and boasts very low standby current, ensuring energy efficiency even
when not in active use. Constructed from UL94V-0 package material, it
meets stringent safety standards, making it a reliable choice for a wide
range of applications.

Output specifications					
ltem	Test conditions Min Typ			Max	Units
Voltage accuracy				±2	%
Line regulation				±0.5	%
Load regulation	0% to 100% load • 5VDC input • 24VDC input - for Vo ≥5.0VDC - for Vo ≥3.3VDC			±1.0 ±1.0 ±1.5	% % %
	10% to 100% load • 24VDC input			±1.0	%
Ripple + Noise*	20MHz bandwidth - for Vo ≤6.5VDC - for Vo ≥9VDC		50 75		mVpk-pk mVpk-pk
Switching frequency	5VDC input24VDC input		1200 410		KHz KHz
Temperature Drift Coefficient			±0.02		%/°C
Transient response deviation	Nominal input, 25% load step change (75%-50%-25% of Io)			±3	%
Transient recovery time	Nominal input, 25% load step change (75%-50%-25% of Io)		150		μs
Over load protection	 5VDC input 24VDC input		8.5 3.5		A A

* Ripple and noise measured with a 0.1µF ceramic capacitor.

EMC specifications					
EMI	CE	EN55032	CLASS B		
EMI	RE	EN55032	CLASS B		
EMS	ESD	IEC61000-4-2	perf. Criteria A		
EMS	RS	IEC61000-4-3	perf. Criteria A		
EMS	EFT*	IEC61000-4-4	perf. Criteria A		
EMS	Surge*	IEC61000-4-5	perf. Criteria A		
EMS	CS	IEC61000-4-6	perf. Criteria A		
EMS	PFMF	IEC61000-4-8	perf. Criteria A		

 An external filter capacitor and TVS is required if the module has to meet IEC61000-4-4 and IEC61000-4-5.

LME78_2.0 series

Wide Input Non-Isolated & Regulated, Single Output

Product Selection Guide

Part Number	Input Voltage [VDC] Nominal	Input Current [@full load, mA typ] min/max Vin	Output Voltage [VDC]	Output Current [mA]	Capacitive load [μF, max.]	Efficiency [%, max]
LME78_051.2-2.0	5 (3-5.5)	889/507	1.2	2000	4200	90
LME78_051.5-2.0	5 (3-5.5)	1099/620	1.5	2000	3700	91
LME78_051.8-2.0	5 (3-5.5)	1304/727	1.8	2000	3300	92
LME78_052.5-2.0	5 (3.8-5.5)	1385/988	2.5	2000	1800	95
LME78_1.2-2.0	24 (4.6-36)	621/89	1.2	2000	2500	84
LME78_1.5-2.0	24 (4.6-36)	758/108	1.5	2000	2000	86
LME78_1.8-2.0	24 (4.6-36)	900/127	1.8	2000	1600	87
LME78_2.5-2.0	24 (4.6-36)	1221/167	2.5	2000	1200	89
LME78_03-2.0	24 (4.75-36)	1527/213	3.3	2000	900	91
LME78_05-2.0	24 (6.5-36)	1637/312	5	2000	600	94
LME78_6.5-2.0	24 (9-36)	1537/397	6.5	2000	470	94
LME78_09-2.0	24 (12-36)	1579/544	9	2000	330	95
LME78_12-2.0	24 (15-36)	1684/717	12	2000	270	95
LME78_15-2.0	24 (18-36)	1736/887	15	2000	200	96

Typical characteristics



24 Series Derating Curve

Blue Zone Vout : 1.2-1.8Vdc models Gray Zone Vout : 2.5 · 3.3Vdc models Yellow Zone Vout : 5 · 6.5Vdc models Orange Zone Vout : 9Vdc model Green Zone Vout : 12 · 15Vdc models

Test configurations

Input reflected ripple current test step:

Input reflected ripple current ist measured through a source indicator Lin (12 μ H) and a source capacitor Cin (10 μ F, ESR<1.0 Ω at 100KHz) at nominal input and full load.



IOutput ripple & noise measurement test: Measured with a 0.1µF ceramic capacitor.

The scope measurement bandwidth is 0-20MHz.



LME78 2.0 series

Wide Input Non-Isolated & Regulated, Single Output

EMC countermeasures

EMI countermeasures

Input filter components (C1, C2, C3, L1) are used to help meet EMI requirement for the module. These components should be mounted as close as possible to the module; and all leads should be minimized to decrease radiated noise.



EFT / Surge test countermeasures

Filter suggestion:

24Vin models: Nippon - chemi - con KY series, 220µF/10V and a TVS, 3KW, 36V



Mechanical dimensions



PINCONN	PIN CONNECTIONS			
PINNUMBER	SINGLE			
1	+V Input			
2	GND			
3	+V Output			

Notes : All dimensions are typical in millimeters (inches). 1. Pin diameter: 0.65±0.15 (0.03±0.006) 2. Pin pitch and length tolerance: ±0.35 (±0.014)

10.10 (0.40)

- 3. Pin to case tolerance: $\pm 0.5 (\pm 0.02)$ 4. Case Tolerance: $\pm 0.5 (\pm 0.02)$