# E3M0160120D

## Silicon Carbide Power MOSFET
## E-Series Automotive
## N-Channel Enhancement Mode

## Features

- 3rd generation SiC MOSFET technology
- High blocking voltage with low on-resistance
- High-speed switching with low capacitances
- Fast intrinsic diode with low reverse recovery ($Q_{rr}$)
- Halogen free, RoHS compliant
- Automotive Qualified (AEC-Q101) and PPAP Capable

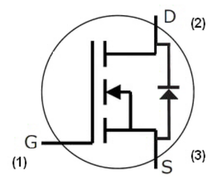
## Benefits

- Higher system efficiency
- Reduce cooling requirements
- Increase power density
- Increase system switching frequency

## Applications

- EV Battery Chargers
- High Voltage DC/DC Converters

## Package


| Part Number | Package | Marking |
|---|---|---|
| E3M0160120D | TO-247-3L | E3M0160120D |

## Maximum Ratings ($T_C$ = 25 ˚C unless otherwise specified)

| Symbol | Parameter | | Value | Unit | Note |
|---|---|---|---|---|---|
| $V_{DSmax}$ | Drain - Source Voltage | | 1200 | V | |
| $V_{GSmax}$ | Gate - Source Voltage | | -8/+19 | V | Note: 1 |
| $I_D$ | Continuous Drain Current, $V_{GS}$ = 15 V | $T_C$ = 25˚C | 17.9 | A | Fig. 19 Note: 2 |
| | | $T_C$ = 100˚C | 13.5 | | |
| $I_{D(pulse)}$ | Pulsed Drain Current, Pulse width $t_P$ limited by $T_{jmax}$ | | 34 | A | Fig. 22 |
| $P_D$ | Power Dissipation, $T_C$=25˚C, $T_J$ = 175 ˚C | | 103 | W | Fig. 20 Note: 2 |
| $T_J$ , $T_{stg}$ | Operating Junction and Storage Temperature | | -55 to +175 | ˚C | |
| $T_L$ | Solder Temperature, 1.6mm (0.063”) from case for 10s | | 260 | ˚C | |
| $M_d$ | Mounting Torque , M3 or 6-32 screw | | 1<br>8.8 | Nm<br>lbf-in | |

Note (1): Recommended turn off / turn on gate voltage $V_{GS}$ - 4V...0V / +15V
Note (2): Verified by design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## Electrical Characteristics ($T_C$ = 25˚C unless otherwise specified)

| Symbol | Parameter | Min. | Typ. | Max. | Unit | Test Conditions | Note |
|---|---|---|---|---|---|---|---|
| $V_{(BR)DSS}$ | Drain-Source Breakdown Voltage | 1200 | | | V | $V_{GS}$ = 0 V, $I_D$ = 100 μA | |
| $V_{GS(th)}$ | Gate Threshold Voltage | 1.8 | 2.8 | 3.6 | V | $V_{DS}$ = $V_{GS}$, $I_D$ = 2.33 mA | Fig. 11 |
| | | | 2.2 | | V | $V_{DS}$ = $V_{GS}$, $I_D$ = 2.33 mA, $T_J$ = 175ºC | |
| $I_{DSS}$ | Zero Gate Voltage Drain Current | | 1 | 50 | μA | $V_{DS}$ = 1200 V, $V_{GS}$ = 0 V | |
| $I_{GSS}$ | Gate-Source Leakage Current | | 10 | 250 | nA | $V_{GS}$ = 15 V, $V_{DS}$ = 0 V | |
| $R_{DS(on)}$ | Drain-Source On-State Resistance | | 159 | 208 | mΩ | $V_{GS}$ = 15 V, $I_D$ = 8.5 A | Fig. 4, 5, 6 |
| | | | 280 | | | $V_{GS}$ = 15 V, $I_D$ = 8.5 A, $T_J$ = 175ºC | |
| $g_{fs}$ | Transconductance | | 5 | | S | $V_{DS}$ = 20 V, $I_{DS}$= 8.5 A | Fig. 7 |
| | | | 5 | | | $V_{DS}$ = 20 V, $I_{DS}$= 8.5 A, $T_J$ = 175ºC | |
| $C_{iss}$ | Input Capacitance | | 730 | | pF | $V_{GS}$ = 0 V, $V_{DS}$ = 0V to 1000 V<br>F = 1 Mhz<br>$V_{AC}$ = 25 mV | Fig. 17, 18 |
| $C_{oss}$ | Output Capacitance | | 31 | | | | |
| $C_{rss}$ | Reverse Transfer Capacitance | | 2 | | | | |
| $E_{oss}$ | $C_{oss}$ Stored Energy | | 17 | | μJ | $V_{DS}$ = 1000 V, F = 1 Mhz | Fig. 16 |
| $C_{o(er)}$ | Effective Output Capacitance (Energy Related) | | 36 | | pF | $V_{GS}$ = 0 V, $V_{DS}$ = 0V to 800V | Note: 3 |
| $C_{o(tr)}$ | Effective Output Capacitance (Time Related) | | 55 | | pF | | |
| $E_{ON}$ | Turn-On Switching Energy (External Diode) | | 195 | | μJ | $V_{DS}$ = 800 V, $V_{GS}$ = -4 V/15 V, $I_D$= 8.5 A,<br>$R_{G(ext)}$ = 2.5 Ω, L= 404 μH, $T_J$=175°C<br>FWD = External SiC DIODE | Fig. 26 |
| $E_{OFF}$ | Turn Off Switching Energy (External Diode) | | 11 | | | | |
| $E_{ON}$ | Turn-On Switching Energy (Body Diode FWD) | | 337 | | μJ | $V_{DS}$ = 800 V, $V_{GS}$ = -4 V/15 V, $I_D$= 8.5 A,<br>$R_{G(ext)}$ = 2.5 Ω, L= 404 μH, $T_J$=175°C<br>FWD = Internal Body Diode | Fig. 26 |
| $E_{OFF}$ | Turn-Off Switching Energy (Body Diode FWD) | | 11 | | | | |
| $t_{d(on)}$ | Turn-On Delay Time | | 25 | | ns | $V_{DD}$ = 800 V, $V_{GS}$ = -4 V/15 V<br>$I_D$ = 8.5 A, $R_{G(ext)}$ = 2.5 Ω,<br>Timing relative to $V_{DS}$<br>Inductive load | Fig. 27 |
| $t_r$ | Rise Time | | 15 | | | | |
| $t_{d(off)}$ | Turn-Off Delay Time | | 14 | | | | |
| $t_f$ | Fall Time | | 12 | | | | |
| $R_{G(int)}$ | Internal Gate Resistance | | 6.5 | | Ω | f = 1 MHz, $V_{AC}$ = 25 mV | |
| $Q_{gs}$ | Gate to Source Charge | | 10 | | nC | $V_{DS}$ = 800 V, $V_{GS}$ = -4 V/15 V<br>$I_D$ = 8.5 A<br>Per IEC60747-8-4 pg 21 | Fig. 12 |
| $Q_{gd}$ | Gate to Drain Charge | | 13 | | | | |
| $Q_g$ | Total Gate Charge | | 33 | | | | |

Note (3): $C_{o(er)}$, a lumped capacitance that gives same stored energy as Coss while Vds is rising from 0 to 800V
$C_{o(tr)}$, a lumped capacitance that gives same charging time as Coss while Vds is rising from 0 to 800V

## Reverse Diode Characteristics ($T_C$ = 25˚C unless otherwise specified)

| Symbol | Parameter | Typ. | Max. | Unit | Test Conditions | Note |
|---|---|---|---|---|---|---|
| $V_{SD}$ | Diode Forward Voltage | 4.8 | | V | $V_{GS}$ = -4 V, $I_{SD}$ = 4.25 A, $T_J$ = 25 °C | Fig. 8, 9, 10 |
| | | 4.2 | | V | $V_{GS}$ = -4 V, $I_{SD}$ = 4.25 A, $T_J$ = 175 °C | |
| $I_S$ | Continuous Diode Forward Current | | 17 | A | $V_{GS}$ = -4 V, $T_C$ = 25˚C | |
| $I_{S, pulse}$ | Diode pulse Current | | 34 | A | $V_{GS}$ = -4 V, pulse width $t_P$ limited by $T_{jmax}$ | |
| $t_{rr}$ | Reverse Recover time | 36 | | ns | $V_{GS}$ = -4 V, $I_{SD}$ = 8.5 A, $V_R$ = 800 V<br>dif/dt = 1140 A/µs, $T_J$ = 175 °C | |
| $Q_{rr}$ | Reverse Recovery Charge | 199 | | nC | | |
| $I_{rrm}$ | Peak Reverse Recovery Current | 10 | | A | | |
| $t_{rr}$ | Reverse Recover time | 38 | | ns | $V_{GS}$ = -4 V, $I_{SD}$ = 8.5 A, $V_R$ = 800 V<br>dif/dt = 580 A/µs, $T_J$ = 175 °C | |
| $Q_{rr}$ | Reverse Recovery Charge | 174 | | nC | | |
| $I_{rrm}$ | Peak Reverse Recovery Current | 7 | | A | | |

## Thermal Characteristics

| Symbol | Parameter | Typ. | Max. | Unit | Test Conditions | Note |
|---|---|---|---|---|---|---|
| $R_{\theta JC}$ | Thermal Resistance from Junction to Case | 1.13 | 1.24 | °C/W | | Fig. 21 |

## Typical Performance

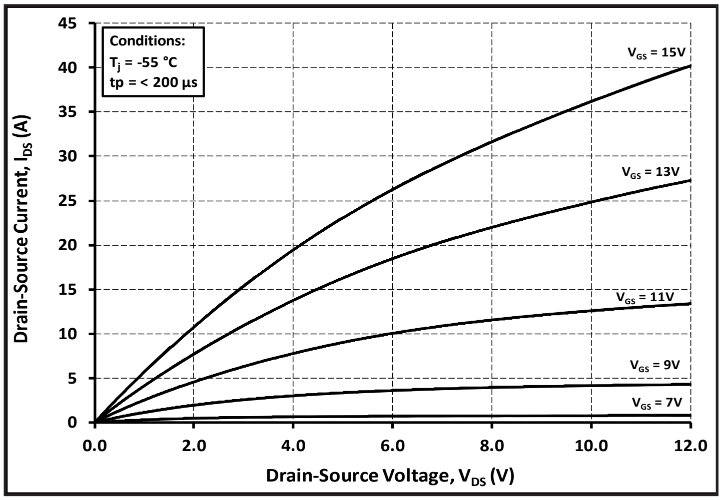



Figure 1. Output Characteristics $T_J$ = -55 °C

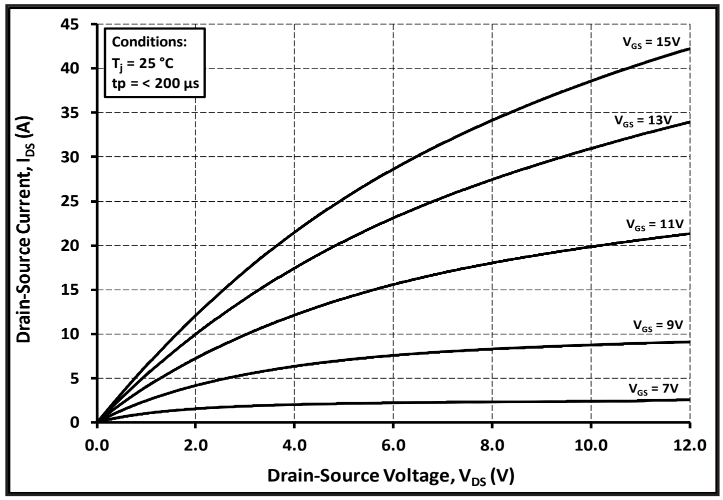



Figure 2. Output Characteristics $T_J$ = 25 °C

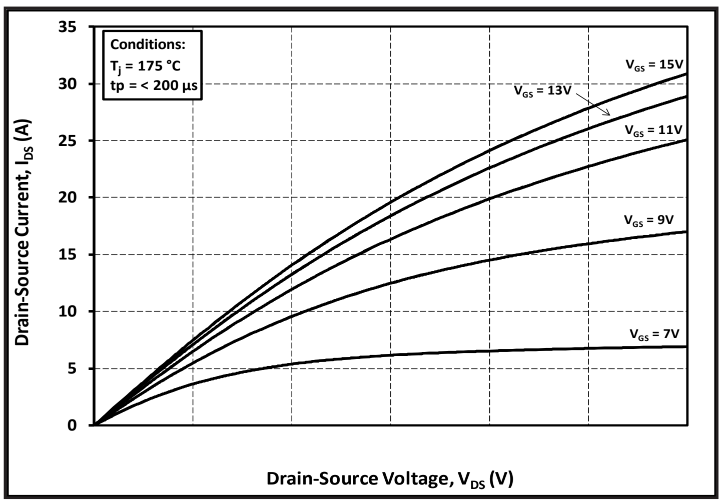



Figure 3. Output Characteristics $T_J$ = 175 °C

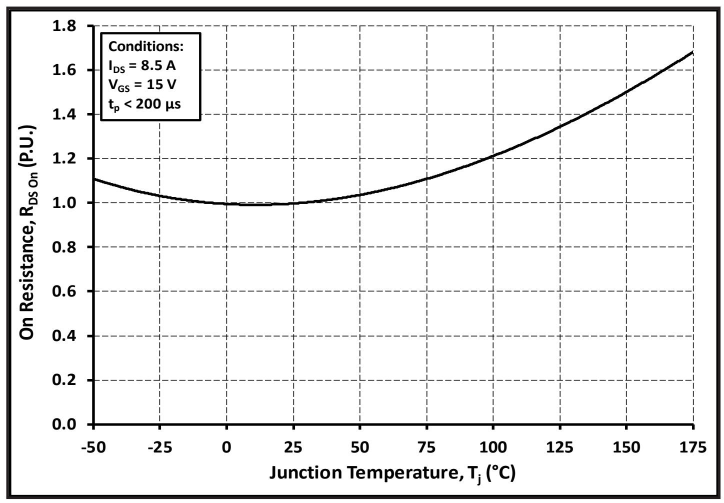



Figure 4. Normalized On-Resistance vs. Temperature

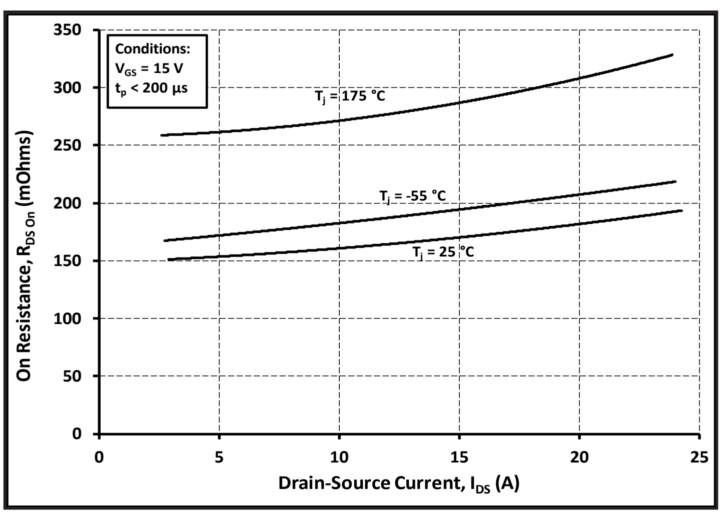



Figure 5. On-Resistance vs. Drain Current
For Various Temperatures

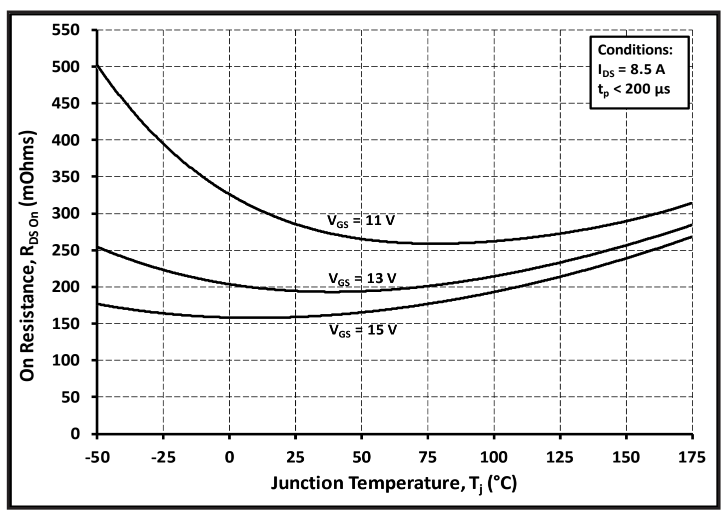



Figure 6. On-Resistance vs. Temperature
For Various Gate Voltage

## Typical Performance

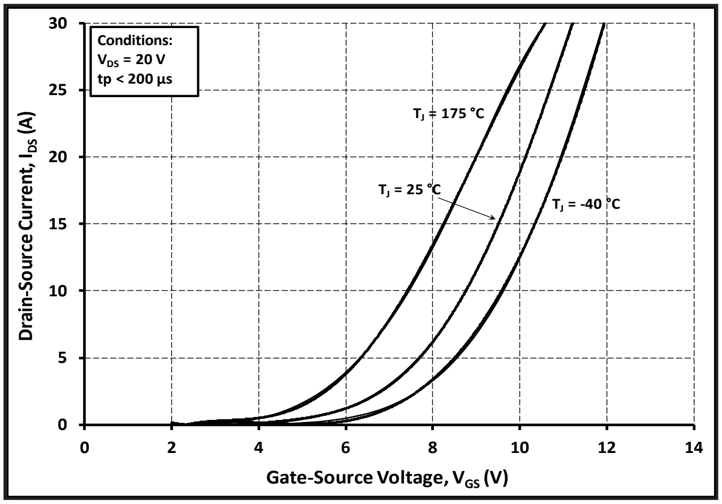



Figure 7. Transfer Characteristic for Various Junction Temperatures

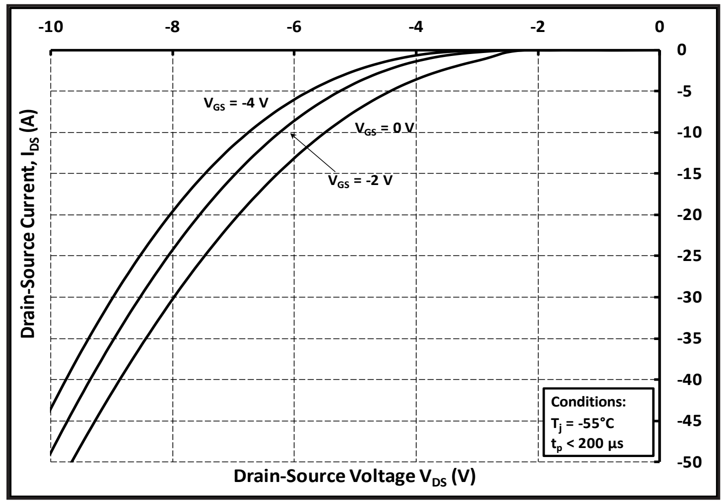



Figure 8. Body Diode Characteristic at -55 °C

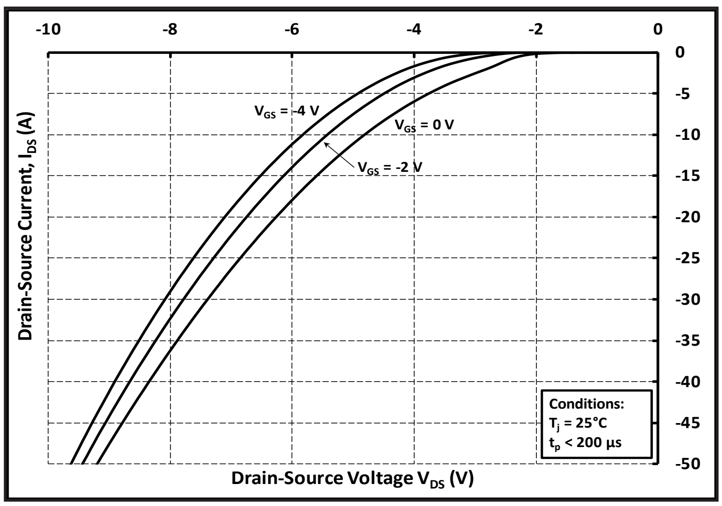



Figure 9. Body Diode Characteristic at 25 °C

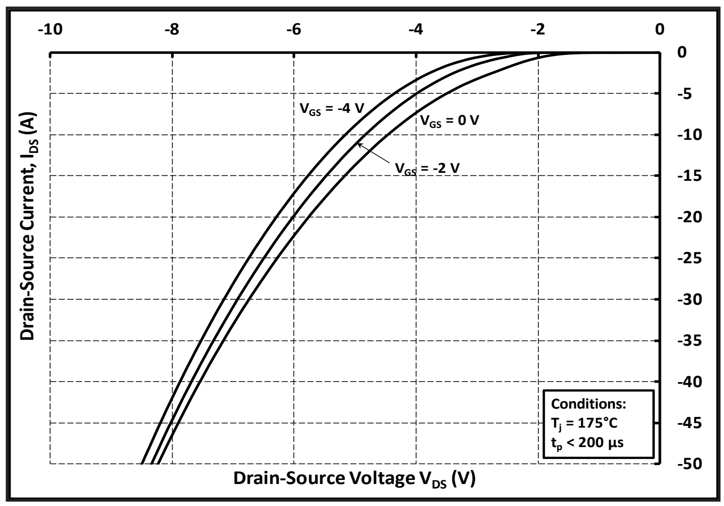



Figure 10. Body Diode Characteristic at 175 °C

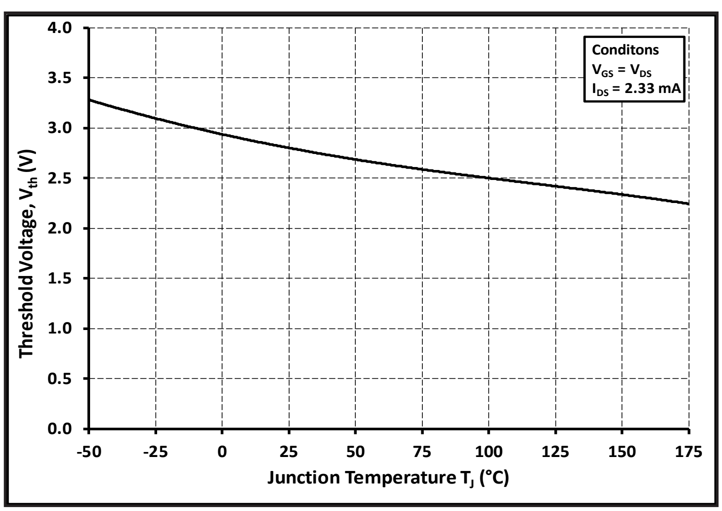



Figure 11. Threshold Voltage vs. Temperature

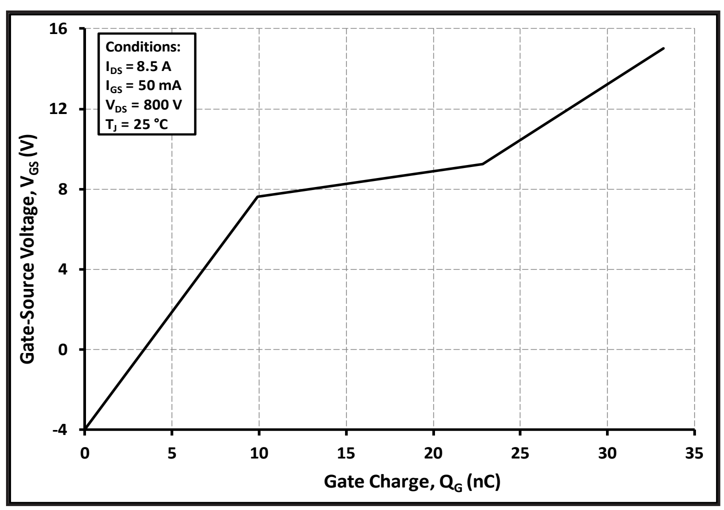



Figure 12. Gate Charge Characteristics

## Typical Performance

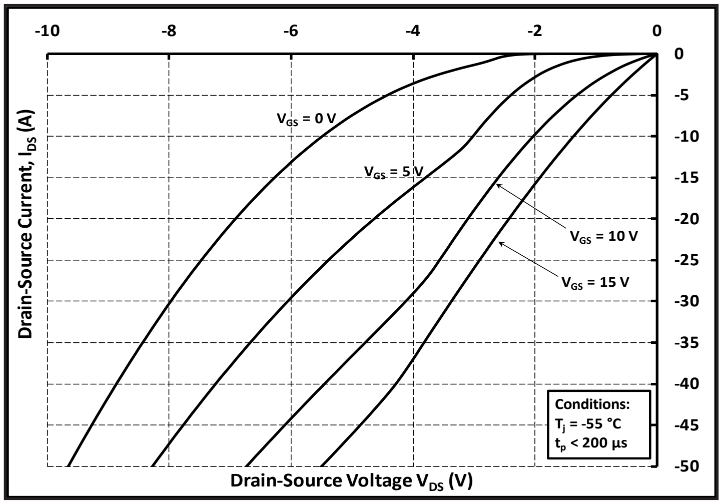



Figure 13. 3rd Quadrant Characteristic at -55 °C

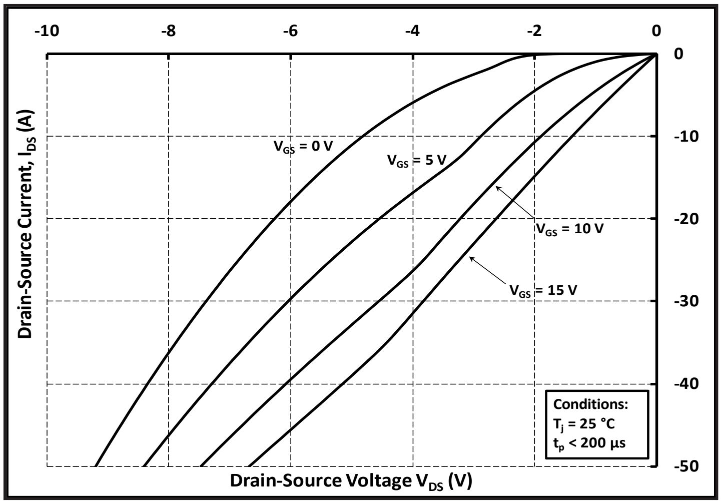



Figure 14. 3rd Quadrant Characteristic at 25 °C

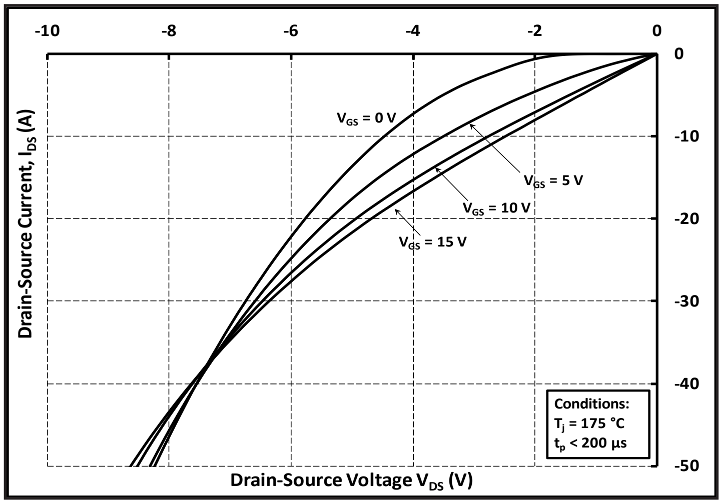



Figure 15. 3rd Quadrant Characteristic at 175 °C

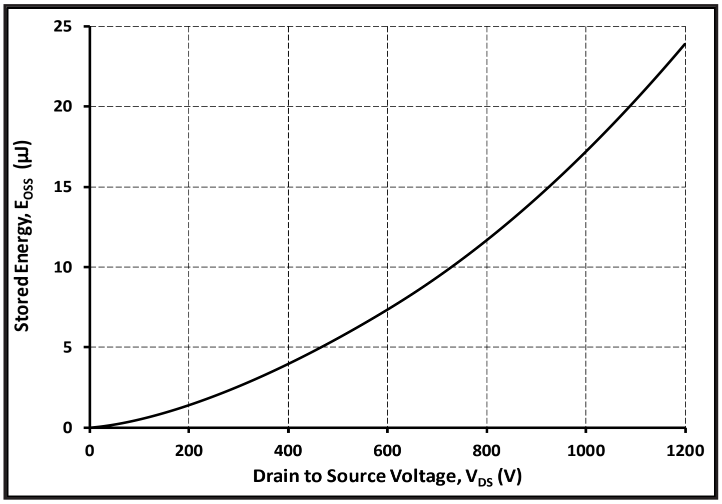



Figure 16. Output Capacitor Stored Energy

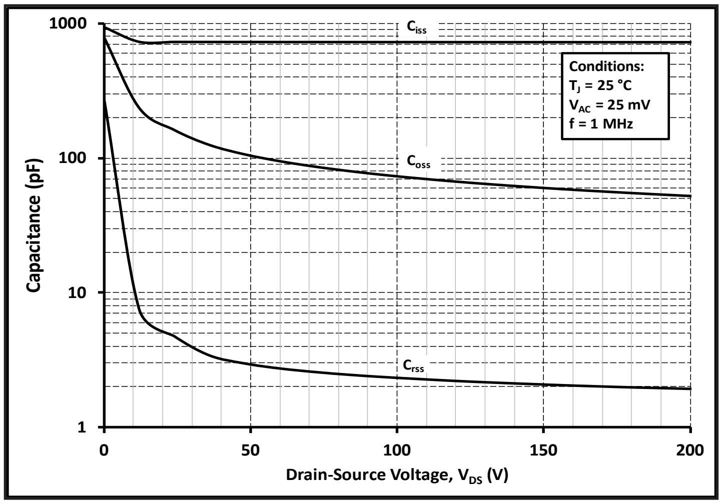



Figure 17. Capacitances vs. Drain-Source Voltage (0 - 200V)

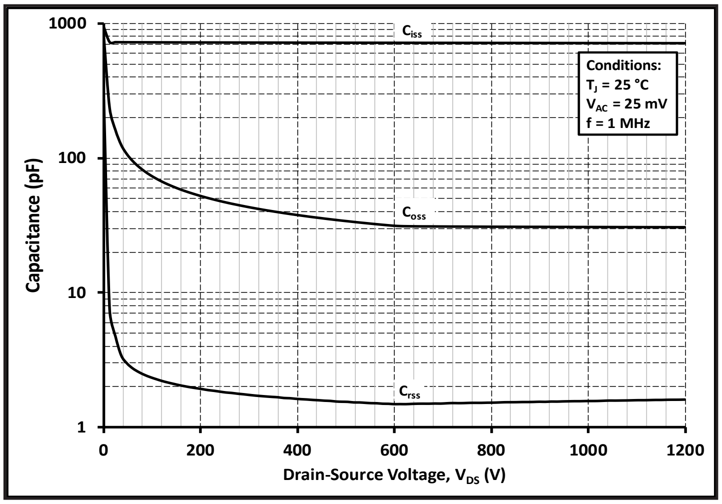



Figure 18. Capacitances vs. Drain-Source Voltage (0 - 1200V)

## Typical Performance

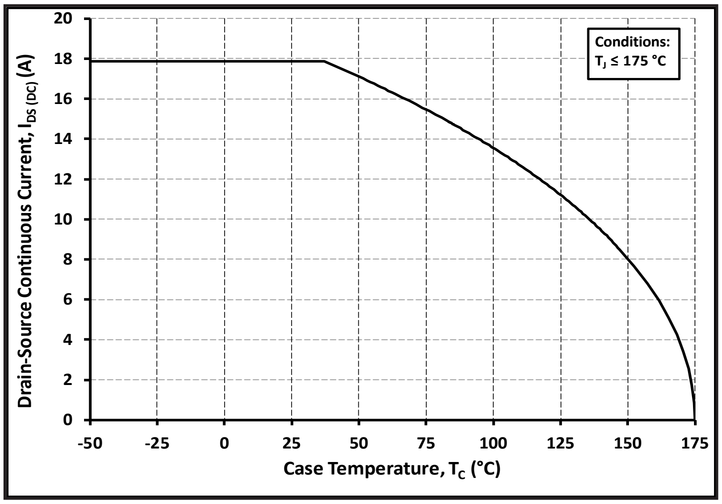



Figure 19. Continuous Drain Current Derating vs. Case Temperature

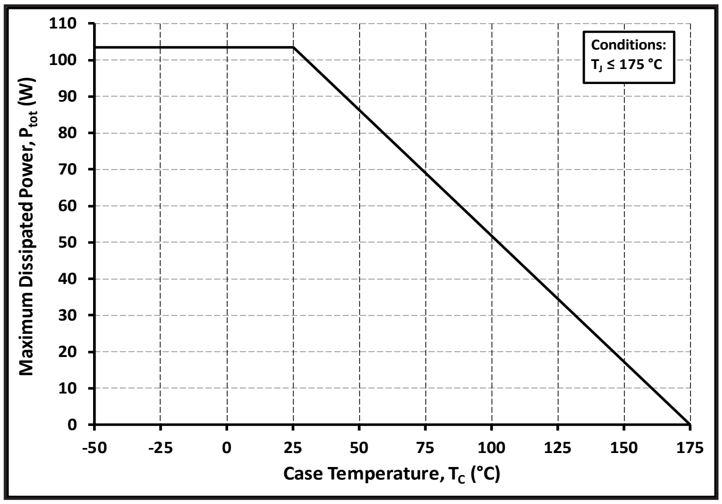



Figure 20. Maximum Power Dissipation Derating vs. Case Temperature

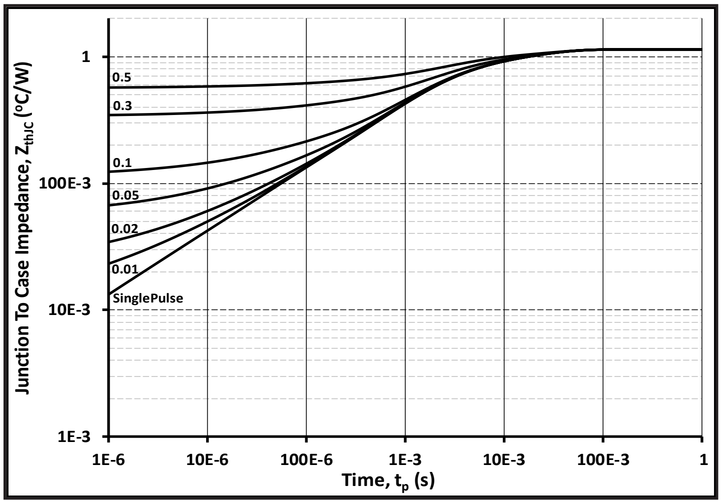



Figure 21. Transient Thermal Impedance (Junction - Case)

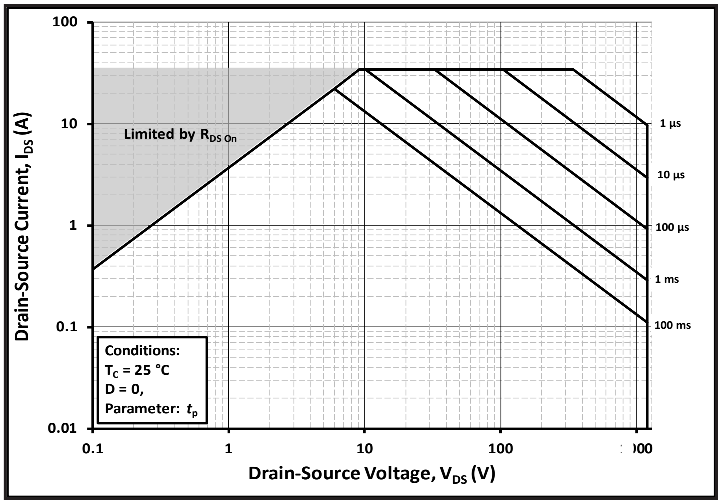



Figure 22. Safe Operating Area

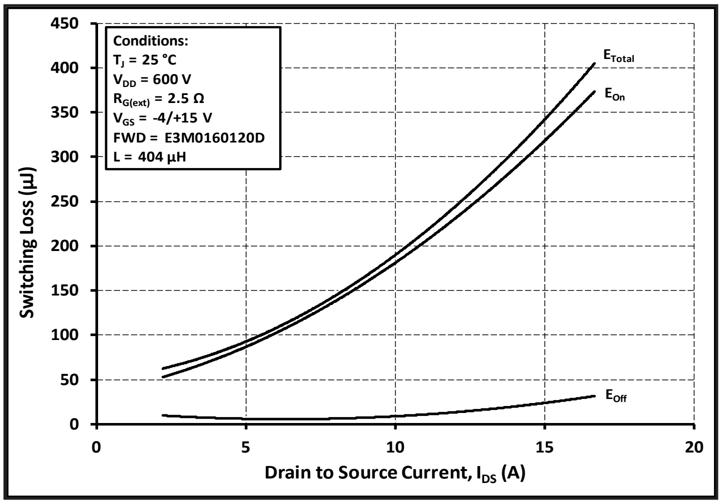



Figure 23. Clamped Inductive Switching Energy vs. Drain Current ($V_{DD}$ = 600V)

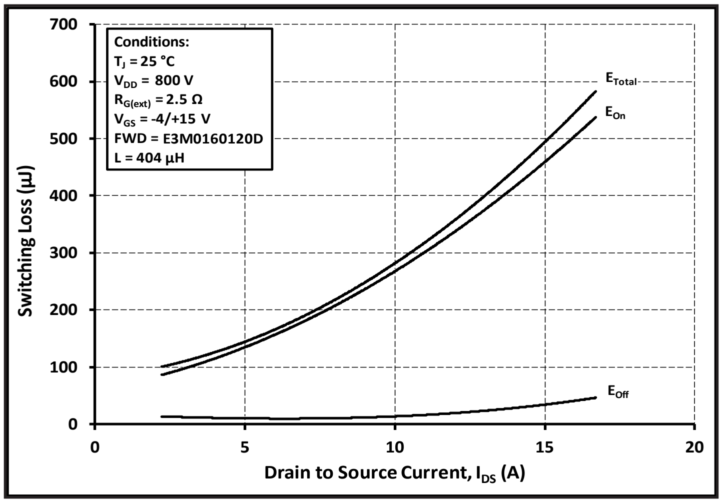



Figure 24. Clamped Inductive Switching Energy vs. Drain Current ($V_{DD}$ = 800V)

## Typical Performance

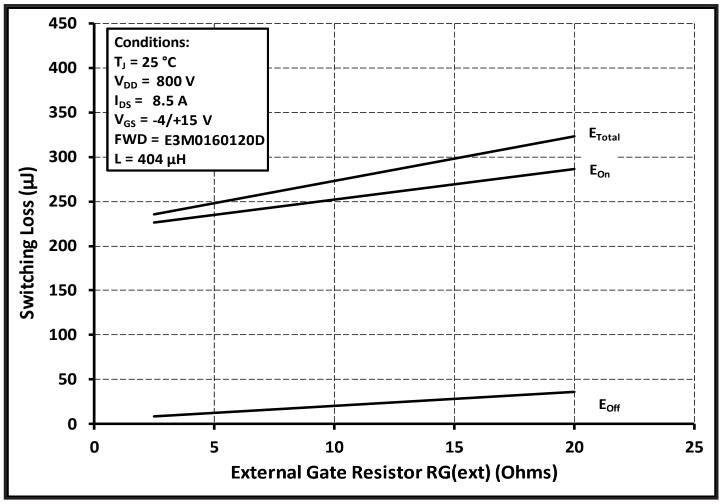



Figure 25. Clamped Inductive Switching Energy vs. $R_{G(ext)}$

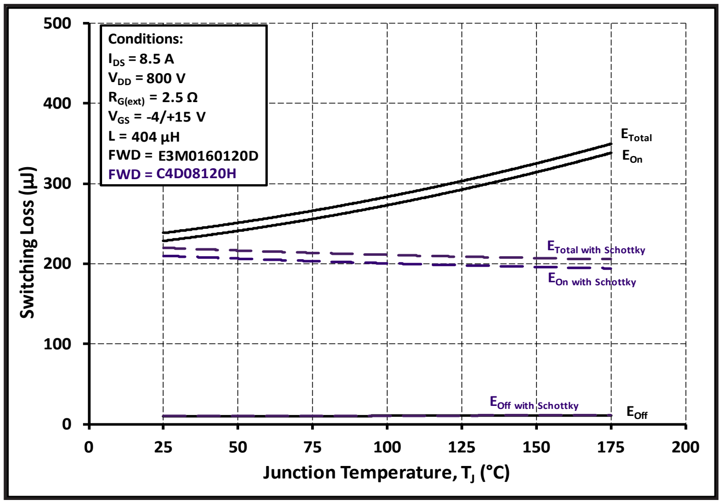



Figure 26. Clamped Inductive Switching Energy vs. Temperature

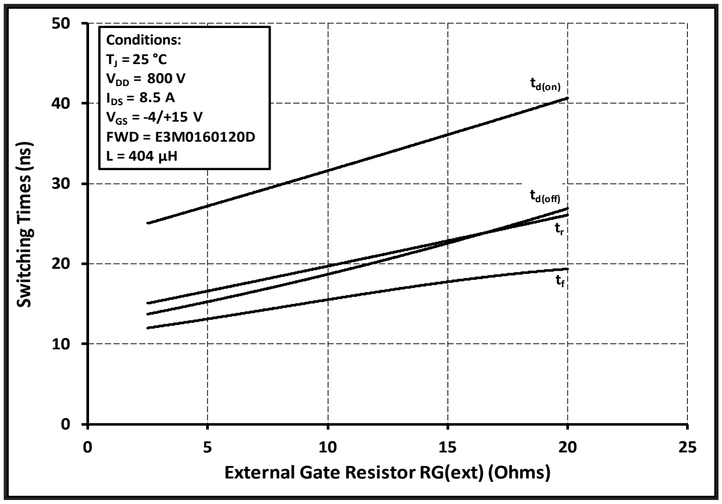



Figure 27. Switching Times vs. $R_{G(ext)}$

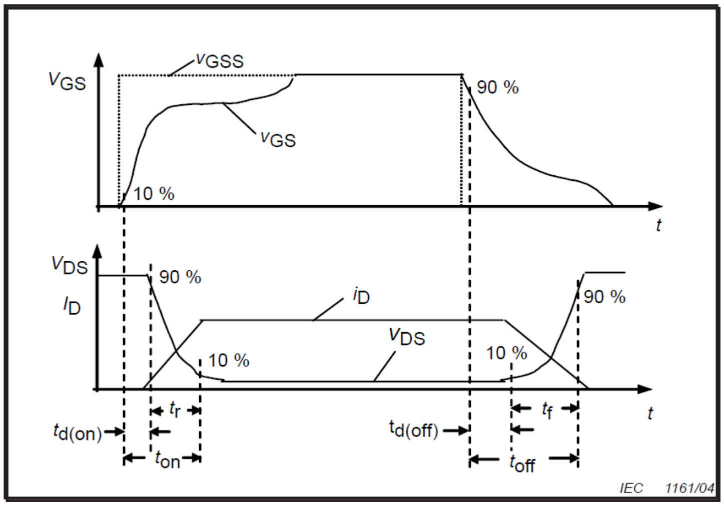



Figure 28. Switching Times Definition

## Test Circuit Schematic

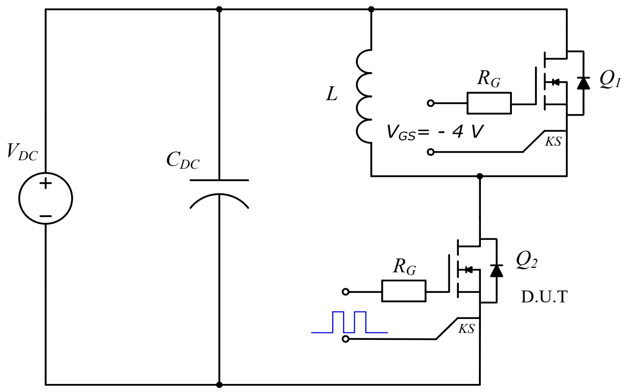
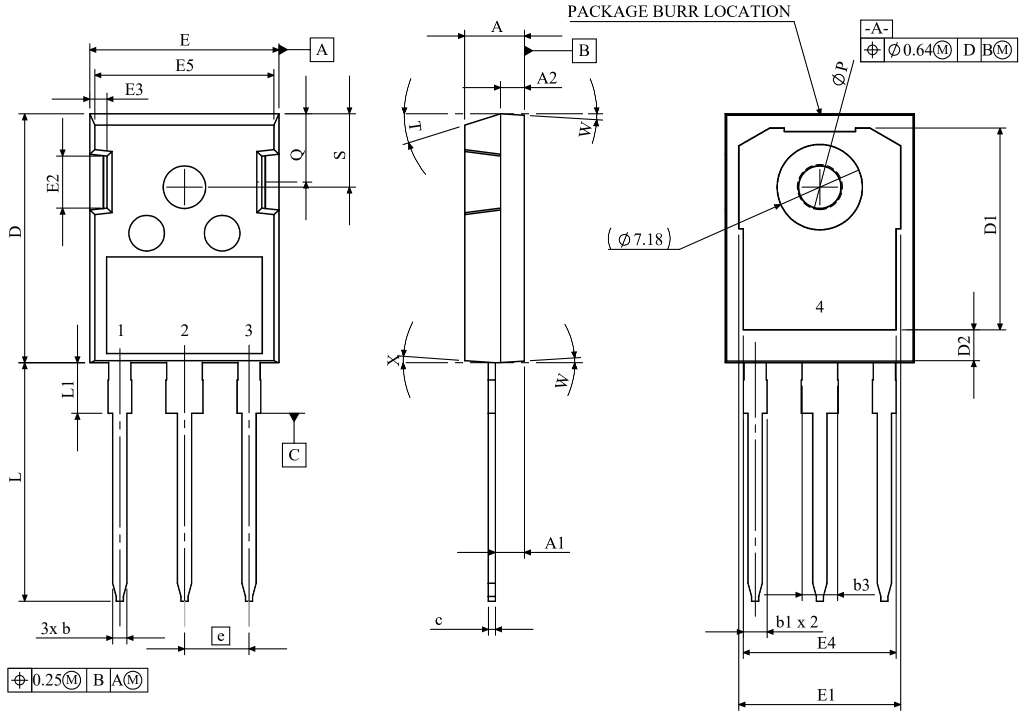



Figure 29. Clamped Inductive Switching Waveform Test Circuit

## Package Dimensions

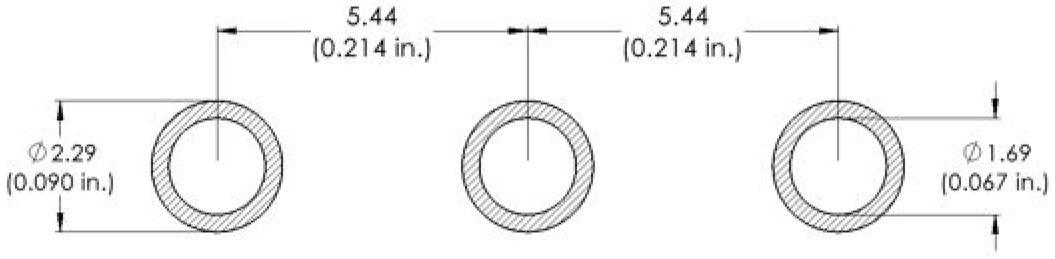



| SYMBOL | MIN (mm) | MAX (mm) |
|---|---|---|
| A | 4.83 | 5.21 |
| A1 | 2.27 | 2.52 |
| A2 | 1.91 | 2.16 |
| b | 1.07 | 1.33 |
| b1 | 1.91 | 2.41 |
| b3 | 2.87 | 3.38 |
| c | 0.55 | 0.74 |
| D | 20.75 | 21.05 |
| D1 | 16 | 17.4 |
| D2 | 2.86 | 3.26 |
| E | 15.75 | 16.13 |
| E1 | 13.5 | 14.55 |
| E2 | 3.68 | 5.1 |
| E3 | 1 | 1.9 |
| E4 | 12.38 | 13.43 |
| E5 | 14.65 | 15.05 |
| e | 5.44 BSC | |
| L | 19.73 | 20.48 |
| L1 | 3.97 | 4.69 |
| ØP | 3.18 | 4.06 |
| Q | 5.42 | 5.96 |
| S | 5.85 | 6.49 |
| T | 17.5° REF. | |
| W | 3.5° REF. | |
| X | 4° REF. | |

| | |
|---|---|
| 1 | GATE |
| 2 | DRAIN |
| 3 | SOURCE |
| 4 | DRAIN |

NOTES:
1. ALL METAL SURFACES ARE TIN PLATED (MATTE), EXCEPT AREA OF CUT.
2. DIMENSIONING & TOLERANCING CONFORM TO ASME Y14.5M-1994.
3. ALL DIMENSIONS ARE LISTED IN MILLIMETERS. ANGLES ARE IN DEGREES.
4. BURR OR MOLD FLASH SIZE (0.5 mm) IS NOT INCLUDED IN THE DIMENSIONS

## Recommended Solder Pad Layout



## Revision history

| Document Version | Date of release | Descriptiion of changes |
|---|---|---|
| 1.0 | July-2023 | Initial datasheet |
| 2.0 | October-2023 | Corrected Rdson max value on page 2 |
| 3 | January - 2025 | Legal Disclaimer Updated |

## Notes & Disclaimer

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REACh substances of high concern (SVHCs) information is available for this product. Since the European Chemical Agency (ECHA) has published notice of their intent to frequently revise the SVHC listing for the foreseeable future, please contact your Wolfspeed representative to ensure you get the most up-to-date REACh SVHC Declaration. REACh banned substance information (REACh Article 67) is also available upon request.

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