

QS1200SCM36: 1200V

N-Channel

SiC MOSFET



www.questsemi.com

Features

- High Operating Temperature 175°C
- Low On-Resistance RDS (on) 0.08Ω
- Fast Switching Speed and Low EMI
- High Peak Current Ratings
- Low Total Gate Charge 60nC for Low Switching Losses
- Improved Power Density: The combination of high voltage, fast switching, and low losses.
- Reduced System Size and Weight

Key Values

PARAMETER	VALUE	UNIT
BV_{DSS}	1200	V
$R_{DS(ON),typ} (20V)$	80	mΩ
$V_{GS(TH),typ}$	2.8	V
E_{ON}	325	μJ
E_{OFF}	219	μJ
$I_D (at 25°C)$	36	A

Part Number

QS1200SCM36

Package

TO247

Marking

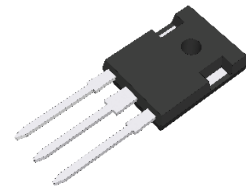
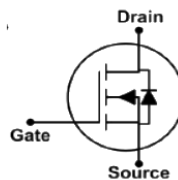
Q

Applications

SiC MOSFETs are well-suited for applications where high-power density, high-frequency operation, and improved efficiency are critical. Their characteristics make them a preferred choice in a variety of modern electronic systems.

- Electric Vehicles
- Solar Inverters
- Uninterruptible Power Supplies (UPS)
- Switched-Mode Power Supplies (SMPS)
- Industrial Motor Drives
- Renewable Energy Systems
- High-Frequency Power Converters
- Grid-Tied Energy Storage Systems

Package



ROHS Compliant
REACH Compliant



QS1200SCM36: 1200V

N-Channel

SiC MOSFET



www.questsemi.com

ABSOLUTE MAXIMUM RATINGS (Ta = 25°C Unless otherwise specified)

Parameter	Symbol	Value	Unit
Drain-to-Source Voltage	V_{DSS}	1200	V
Maximum Gate-to-Source Voltage	V_{GSmax}	-10 ~ + 25	
Recommended operations values of gate to source voltage	$V_{GSop(DC)}$	-5.0 ~ + 20	
Recommended operations values of gate to source voltage (f>1Hz)	$V_{GSop(AC)}$	-5.0 ~ + 20	
Continuous Drain Current	I_D	36.0	A
Continuous Drain Current at $T_c = 100^\circ\text{C}$		25.0	
Pulsed Drain Current at $V_{GS} = 10V^2$	I_{DM}	90	
Single Pulse Avalanche Energy ($V_{DD} = 50V, V_{GS} = 15V, R_G = 25\Omega, L = 1mH$)	E_{AS}	171	mJ
Power Dissipation	P_D	198	W
Derating Factor above 25°C		1.30	°C/W
Soldering Temperature, Distance of 1.6mm from case for 10 seconds	T_L	300	°C
Operating and Storage Temperature Range	T_J, T_{STG}	-55 to 175	
Caution: Stresses greater than those listed in the Absolute Maximum Ratings may cause permanent damage to devices.			
Thermal Characteristics			
Thermal Resistance, Junction-to-Case	$R_{\theta JC}$	0.76	°C/W
Thermal Resistance, Junction-to-Ambient	$R_{\theta JA}$	40	

QS1200SCM36: 1200V

N-Channel

SiC MOSFET



www.questsemi.com

ELECTRICAL CHARACTERISTICS (Ta = 25°C Unless otherwise specified)

Parameter	Symbol	Test Conditions	Value			Unit
			Min	Typ	Max	
OFF Characteristics (Tj = 25°C unless otherwise specified)						
Drain-to-Source Breakdown Voltage	BV_{DSS}	$V_{GS} = 0V, I_D = 100\mu A$	1200	–	–	V
Drain-to-Source Leakage Current	I_{DSS}	$V_{DS} = 1200V, V_{GS} = 0V$	–	–	100	μA
Gate-to-Source Leakage Current	I_{GSS+}	$V_{DS} = 0V, V_{GS} = 20V$	–	–	100	nA
Gate-to-Source Leakage Current	I_{GSS-}	$V_{DS} = 0V, V_{GS} = -10V$	–	–	-100	nA
ON Characteristics (Tj = 25°C unless otherwise specified)						
Static Drain-to-Source On Resistance ³	$R_{DS(ON)}$	$V_{GS} = 20V, I_D = 20A$	–	80	100.0	m Ω
		$V_{GS} = 20V, I_D = 20A, T_j = 150^\circ C$	–	121	–	
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{GS} = V_{DS}, I_D = 5mA$	1.8	2.8	3.8	V
Dynamic Characteristics (Essentially independent of operating temperature)						
Input Capacitance	C_{iss}	$V_{GS} = 0V$	–	1001	–	pF
Reverse Transfer Capacitance	C_{rss}	$V_{DS} = 800V$ $f = 1MHz$	–	7.2	–	
Output Capacitance	C_{oss}		–	60	–	
Gate Series Resistance	R_g	$f = 1MHz$	–	5.6	–	Ω
Total Gate Charge	Q_g	$V_{DD} = 600V$	–	60	–	nC
Gate-to-Source Charge	Q_{gs}	$I_D = 20A$	–	16	–	
Gate-to-Drain (Miller) Charge	Q_{gd}	$V_{GS} = -\frac{5}{20V}$	–	23	–	
Resistive Switching Characteristics (Essentially independent of operating temperature)						
Turn-on Delay Time	$t_{d(on)}$	$V_{DD} = 800V$	–	11	–	nS
Rise Time	t_{rise}	$I_D = 20A$	–	37	–	
Turn-off Delay Time	$t_{d(off)}$	$V_{GS} = -\frac{5}{20V}$	–	24	–	
Fall Time	t_{fall}	$R_G = 4.7\Omega$	–	9.8	–	μJ
Turn-On Switching Energy	E_{ON}	$L = 500\mu H$	–	325	–	
Turn-Off Switching Energy	E_{OFF}		–	219	–	
Source-Drain Body Diode Characteristics (Tj = 25°C unless otherwise specified)						
Continuous Source Current	I_{SD}	Maximum Ratings	–	–	36	A
Diode Forward Voltage	V_{SD}	$I_S = 0.5A, V_{GS} = 0V$	–	2.6	–	V
Reverse Recovery Time	t_{rr}	$V_{GS} = 0V$	–	20	–	nS
Reverse Recovery Charge	Q_{rr}	$I_F = 20A$	–	39	–	
Peak Reverse Recovery Charge	I_{mm}	$\frac{di}{dt} = 800A/\mu s$	–	2.8	–	A

- Tj=25°C to 175°C

- Repetitive rating, pulse width limited by maximum junction temperature

-Pulse width \leq 380 μ s; duty cycles \leq 2%

QS1200SCM36: 1200V

N-Channel

SiC MOSFET



www.questsemi.com

Figure 1: Maximum effective Thermal impedance, junction - to - Case

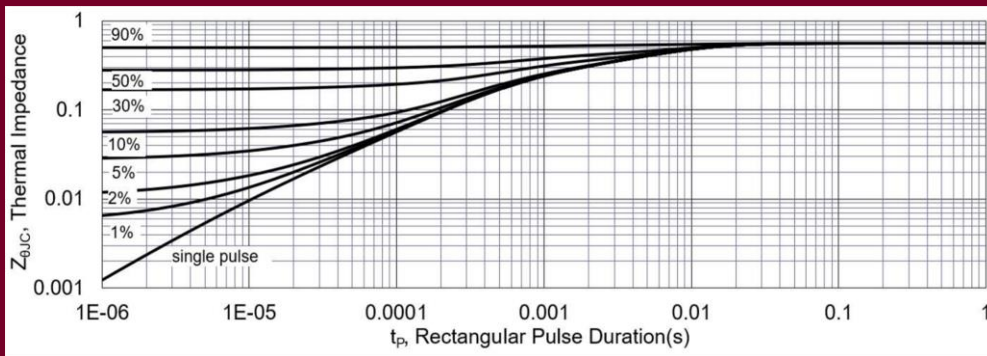


Figure 2: Maximum Power Dissipation vs Case Temperature

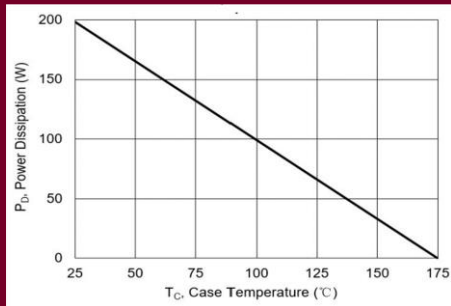


Figure 3: Maximum Continuous Drain Current vs Case Temperature

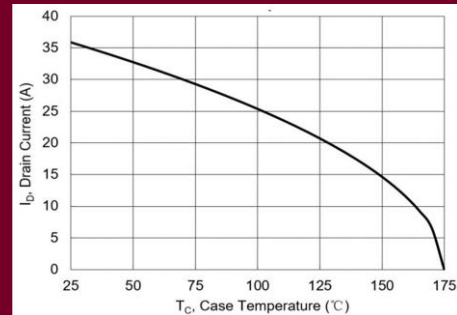


Figure 4: Typical Output Characteristics

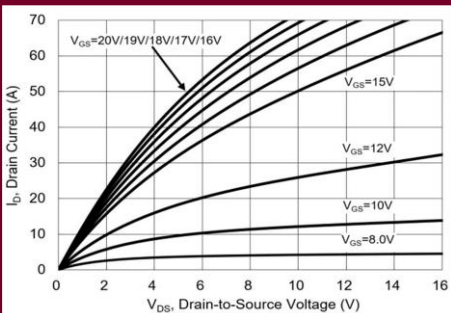
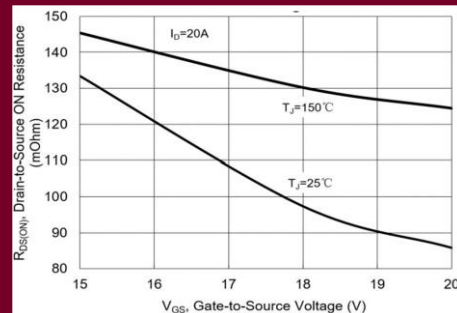


Figure 5: Typical Drain-to-Source ON Resistance vs Gate Voltage



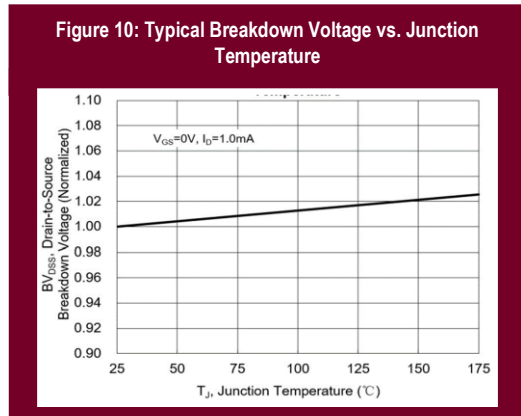
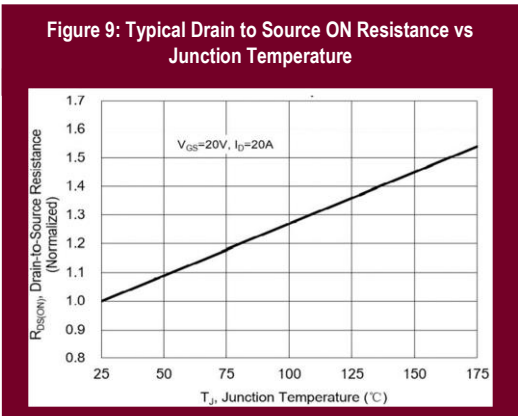
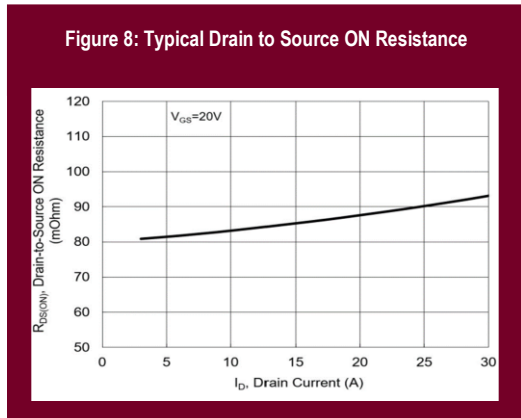
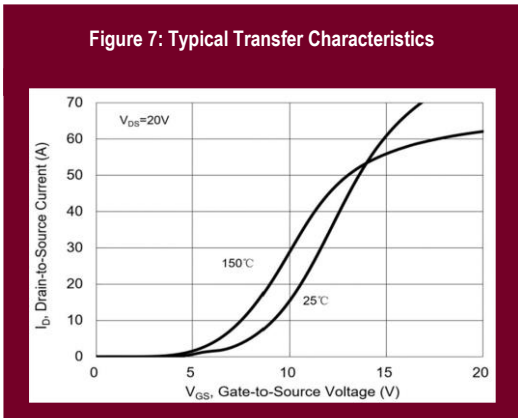
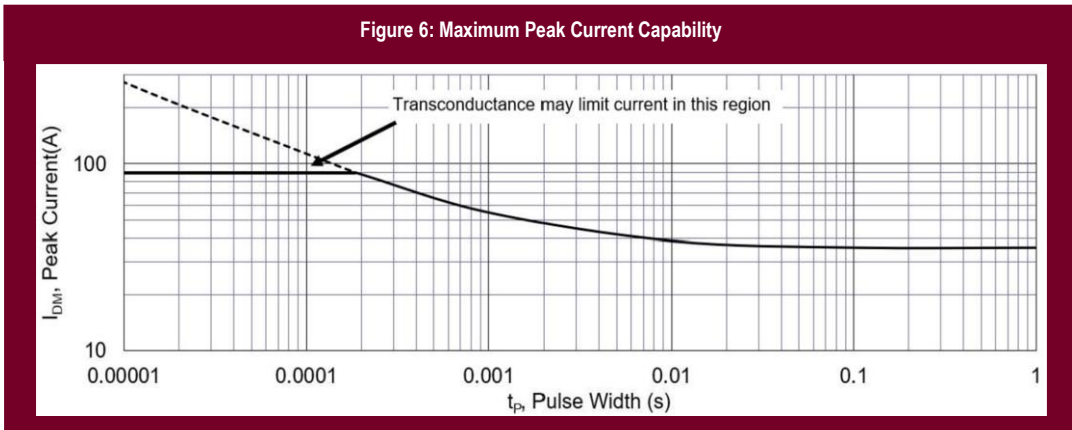
QS1200SCM36: 1200V

N-Channel

SiC MOSFET



www.questsemi.com



QS1200SCM36: 1200V

N-Channel

SiC MOSFET



www.questsemi.com

Figure 11: Typical Threshold Voltage vs. Junction Temperature

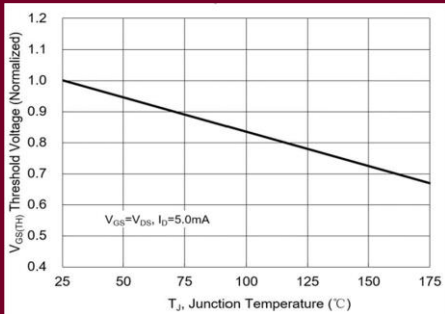


Figure 12: Typical Capacitance vs. Drain-to-Source Voltage

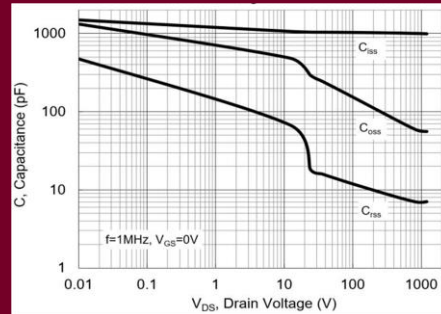


Figure 13: Maximum Forward Safe Operating Area

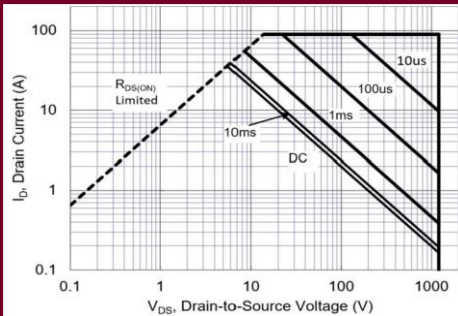
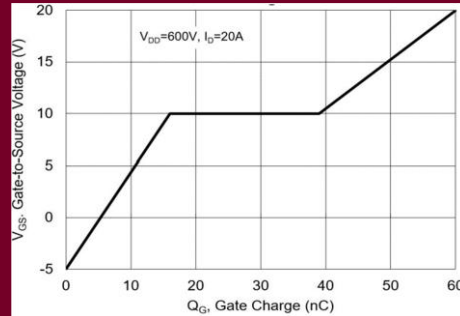


Figure 14: Typical Gate Charge vs. Gate to Source Voltage



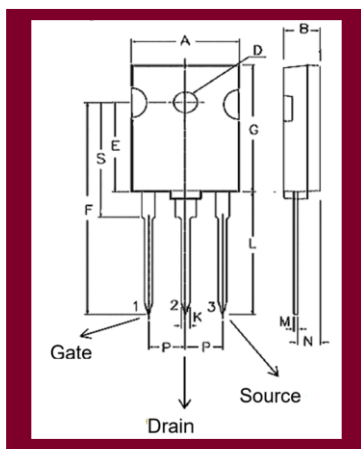
QS1200SCM36: 1200V

N-Channel

SiC MOSFET



www.questsemi.com



DIM	MIN	MAX
A	15.20	15.80
B	4.90	5.10
D	3.90	4.10
E	14.20	14.80
F	28.20	30.50
G	19.50	19.80
K	1.00	1.30
L	14.10	17.50
M	0.40	0.60
N	2.50	2.75
P	5.21	5.72
S	18.25	19.25

Pin configuration:

1. Gate
2. Drain
3. Source

QS1200SCM36: 1200V

N-Channel

SiC MOSFET



www.questsemi.com

Disclaimer:

The products described in this datasheet are intended for general-purpose applications, and their specifications and performance characteristics have been established under standard operating conditions. They are not specifically designed or authorized for use in life-critical or life-support systems. Life-critical systems are those in which the failure of a semiconductor device could lead to loss of life, severe injury, or severe damage to property.

It is essential to note that the use of our products in life-critical systems is strictly prohibited without prior written consent and agreement with Quest Semi. Any such usage is at the sole risk of the customer, and Quest Semi disclaims any liability, damages, or loss arising from the use of our products in such applications.

If you are considering the use of our products in life-critical systems, please contact our sales and technical support teams to discuss the necessary measures, risk assessment, and product customization that may be required to ensure compliance with the stringent safety and reliability standards associated with these applications. Customers are strongly advised to conduct their own analysis and testing to confirm the suitability and reliability of our products for their intended application, especially in life-critical systems.

Quest Semi reserves the right to make changes to product specifications and discontinue products without notice. It is the responsibility of the customer to ensure that the latest versions of datasheets are consulted before finalizing system designs or orders.