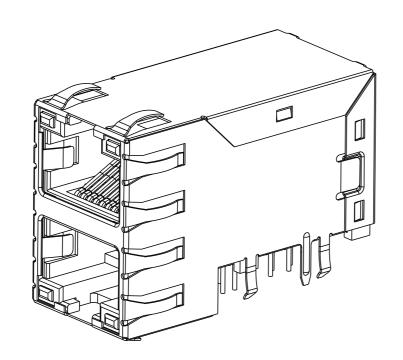
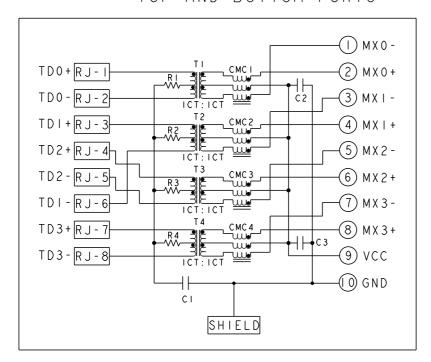
Р	LTR	DESCRIPTION	DATE	DWN	APVD
	1	INITIAL RELEASE	17APR2018	QL	TY
	Α	EC-2009010	21SEP2020	QL	TY



S8G56 GIGABIT CIRCUIT &A

TOP AND BOTTOM PORTS



CI = 1000pF, 2kV CAPACITOR

C2 - C3 = IOnF, 50V CAPACITORS RI - R4 = 75 Ohms, I/I6W, RESISTORS 1 MATERIALS:

- HOUSING: THERMOPLASTIC, BLACK, FLAMMABILITY RATING UL 94V-0.

- SHIELD: BRASS, PREPLATED WITH 0.76um MIN SEMI-BRIGHT NICKEL,

POST DIPPED WITH 2.54um MIN SAC SOLDER ON SOLDER TAILS.

- CONTACTS: PHOSPHOR BRONZE, PLATED WITH 1.27um MIN OVERALL NICKEL UNDERPLATE, SELECTIVE 1.27um MIN GOLD AT MATING INTERFACE

AND 4.00 um MIN MATTE TIN ON SOLDER TAILS.

∠
MAGNETICS

-APPLICATION: 10/100/1000 BASE-T

-IMPEDANCE: 100 OHMS

-TURNS RATIO (CHIP: CABLE): 1:1 ALL FOUR PAIRS

-OPEN CIRCUIT INDUCTANCE (OCL): 350uH MIN @100kHz, 0.1VRMS, 8mADC BIAS FROM 0 °C TO 70 °C, ALL FOUR PAIRS

-ALL FOUR PAIRS BI-DIRECTIONAL

-PERFORMANCE @ 25 °C:

INSERTION LOSS (IL): 1.1dB MAX FROM 0.5MHz TO 100MHz RETURN LOSS (RL): 18dB MIN FROM 0.5MHz TO 40MHz 12-20LOG(f/80)dB MIN FROM 40.1MHz TO 100MHz

CROSSTALK ATTENUATION: 35dB MIN FROM 0.5MHz TO 40MHz 33-20LOG(f/50)dB MIN FROM 40.1MHz TO 100MHz

COMMON MODE REJECTION RATIO (CMRR):

30dB MIN FROM 0.5MHz TO 100MHz

-ISOLATION VOLTAGE: COMPLIES WITH IEEE802.3 2002, PARA 40.6.1.1, ITEM b.

APPROXIMATE AREA SHOWN. DATE CODE YY IS YEAR, WW IS WORK WEEK, D IS DAY OF WEEK. WITH SUNDAY=1

TRP LOGO AND AGENCY APPROVAL LOGO ARE LOCATED IN THE APPROXIMATE AREA SHOWN.

RJ45 CAVITIES CONFORM TO FCC RULES AND REGULATION PART 68 SUBPART F.

6 LEDS ARE DRIVEN WITH CONSTANT CURRENT AT APPROX. 20ma

DOMINANT WAVELENGTH (\(\lambda\)D) GREEN, 568nm TYP AT IF=20mA

YELLOW, 588nm TYP AT IF=20mA

FORWARD VOLTAGE(VF)

GREEN, 2.2V TYP AT IF=20mA

YELLOW, 2.1V TYP AT IF=20mA

⚠ INDICATED MAGNETIC CONNECTIONS ARE SYMMETRICAL AND SUPPORT AUTO-MDI/MDIX.

8 OPERATING TEMPERATURE: FROM 0 °C TO 70 °C

108-104004

9 THE PART IS RECOMMENDED FOR WAVE SOLDERING PROCESS, PEAK TEMPERATURE 260°C MAX, 10 SECONDS MAX.

2.10	GREEN/Y	GREEN/YELLOW		REEN/YELLOW	GREEN/YELLOW		V GREEN/YELLOW		0577-1
3.30	GREEN		YELLOW		GREEN		YELLOW	2250577-2	
3.30	GREEN/Y	GREEN/YELLOW		REEN/YELLOW	GREEN/YELLOW		GREEN/YELLOW	2250577-1	
DIM A	BOTTOM LED	2(LEFT)	ВОТТО	M LED I(RIGHT:	TOP LED 2(LEF	T)	TOP LED I(RIGHT)	PART	NUMBER
	AND COMPLIANT TO ASME Y14.5M		DWN		trp connec	TOR D	ONGGUAN CHINA		
	*		0 PLC ±0.25 1 PLC ±0.25 2 PLC ±0.25 3 PLC ±- 4 PLC ±-	MAGJACK	SIZE	2XI S8G56 GIGA CIRCUIT, OFFSET, W cage code Drawing no	ITH LEC) RESTRICTED TO	
		PRODUCT SPEC		APPLICATION SPEC	STACK NON-POE	IA 1	C-2250577		-

CUSTOMER DRAWING

