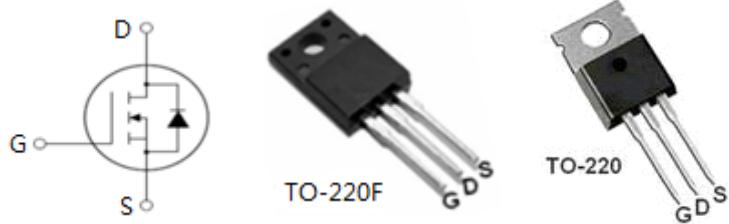


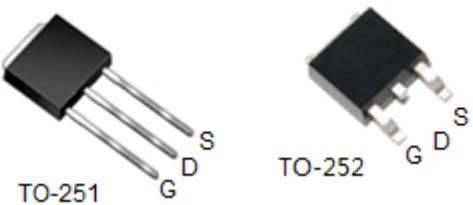
## FEATURES

- Fast switching
- 100% avalanche tested
- Improved dv/dt capability



## APPLICATIONS

- Switch Mode Power Supply (SMPS)
- Uninterruptible Power Supply (UPS)
- Power Factor Correction (PFC)



## Absolute Maximum Ratings $T_C = 25^\circ\text{C}$ , unless otherwise noted

Parameter	Symbol	Value				Unit
		TO-220F	TO-220	TO-251	TO-252	
Drain-Source Voltage ( $V_{GS} = 0\text{V}$ )	$V_{DSS}$	600				V
Continuous Drain Current	$I_D$	3				A
Pulsed Drain Current (note1)	$I_{DM}$	12				A
Gate-Source Voltage	$V_{GSS}$	$\pm 30$				V
Single Pulse Avalanche Energy (note2)	$E_{AS}$	90				mJ
Avalanche Current (note1)	$I_{AR}$	3				A
Repetitive Avalanche Energy (note1)	$E_{AR}$	10				mJ
Power Dissipation ( $T_C = 25^\circ\text{C}$ )	$P_D$	30	45			W
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55~+150				°C

## Thermal Resistance

Parameter	Symbol	Value				Unit
		TO-220F	TO-220	TO-251	TO-252	
Thermal Resistance, Junction-to-Case	$R_{thJC}$	4.1	2.8			°C/W
Thermal Resistance, Junction-to-Ambient	$R_{thJA}$	62.5	60			

**Specifications**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

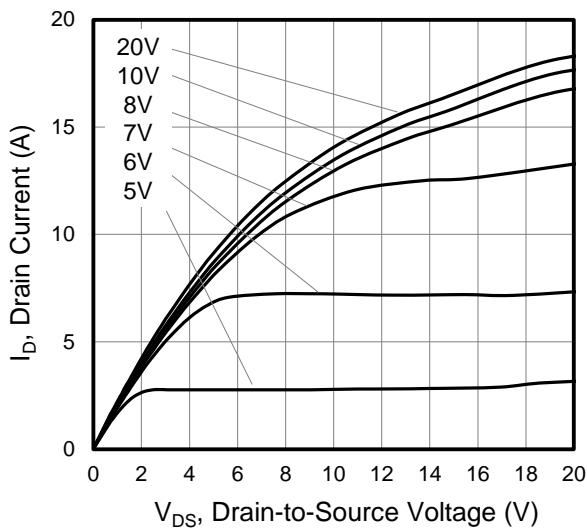
Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
<b>Static</b>						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	600	--	--	V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}} = 600\text{V}, V_{\text{GS}} = 0\text{V}, T_J = 25^\circ\text{C}$	--	--	1	$\mu\text{A}$
Gate-Source Leakage	$I_{\text{GSS}}$	$V_{\text{GS}} = \pm 30\text{V}$	--	--	$\pm 100$	nA
Gate-Source Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	3.0	--	4.0	V
Drain-Source On-Resistance (Note3)	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 10\text{V}, I_D = 1.5\text{A}$	--	2.4	2.8	$\Omega$
<b>Dynamic</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 25\text{V}, f = 1.0\text{MHz}$	--	462	--	pF
Output Capacitance	$C_{\text{oss}}$		--	54.2	--	
Reverse Transfer Capacitance	$C_{\text{rss}}$		--	8.8	--	
Total Gate Charge	$Q_g$	$V_{\text{DD}} = 480\text{V}, I_D = 3.0\text{A}, V_{\text{GS}} = 10\text{V}$	--	13.5	--	nC
Gate-Source Charge	$Q_{\text{gs}}$		--	2	--	
Gate-Drain Charge	$Q_{\text{gd}}$		--	6	--	
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = 300\text{V}, I_D = 3.0\text{A}, R_G = 25 \Omega$	--	10	--	ns
Turn-on Rise Time	$t_r$		--	25	--	
Turn-off Delay Time	$t_{\text{d}(\text{off})}$		--	40	--	
Turn-off Fall Time	$t_f$		--	52	--	
<b>Drain-Source Body Diode Characteristics</b>						
Continuous Body Diode Current	$I_S$	$T_C = 25^\circ\text{C}$	--	--	3	A
Pulsed Diode Forward Current	$I_{\text{SM}}$		--	--	12	
Body Diode Voltage	$V_{\text{SD}}$	$T_J = 25^\circ\text{C}, I_{\text{SD}} = 3.0\text{A}, V_{\text{GS}} = 0\text{V}$	--	--	1.4	V
Reverse Recovery Time	$t_{\text{rr}}$	$V_{\text{GS}} = 0\text{V}, I_S = 3.0\text{A}, dI_F/dt = 100\text{A}/\mu\text{s}$	--	220	--	ns
Reverse Recovery Charge	$Q_{\text{rr}}$		--	3	--	

**Notes**

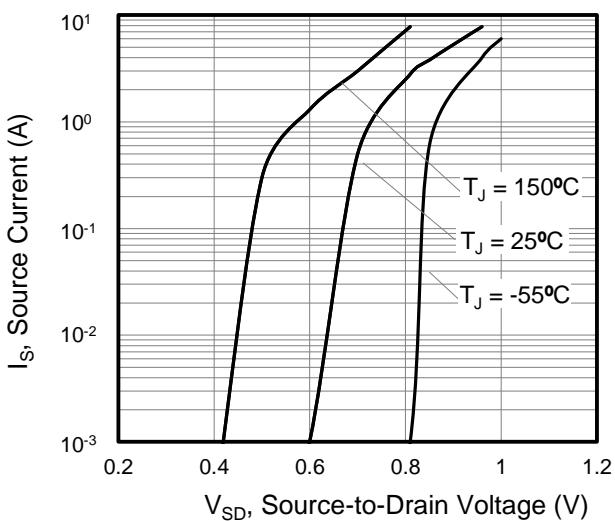
1. Repetitive Rating: Pulse width limited by maximum junction temperature
2.  $I_{AS} = 3\text{A}, V_{DD} = 50\text{V}, R_G = 25 \Omega$ , Starting  $T_J = 25^\circ\text{C}$
3. Pulse Test: Pulse width  $\leq 300\mu\text{s}$ , Duty Cycle  $\leq 1\%$

**Typical Characteristics**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

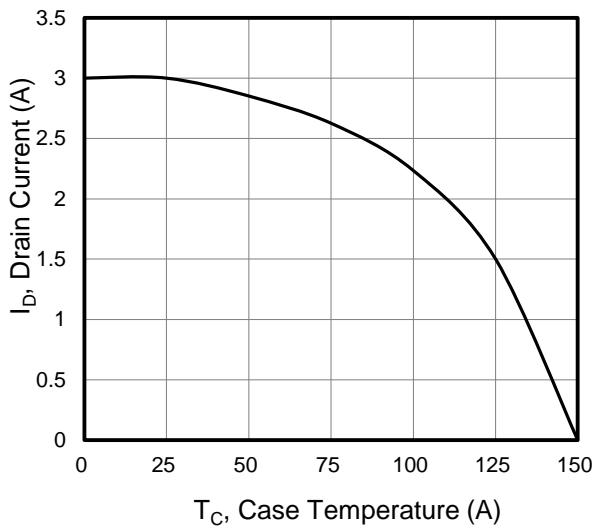
**Figure 1. Output Characteristics ( $T_J = 25^\circ\text{C}$ )**



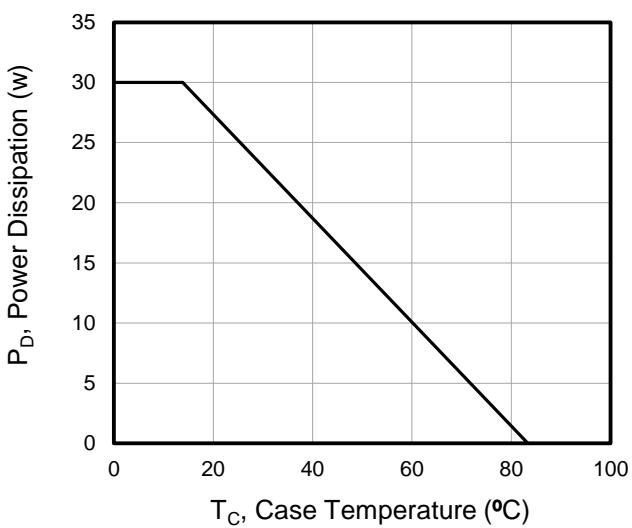
**Figure 2. Body Diode Forward Voltage**



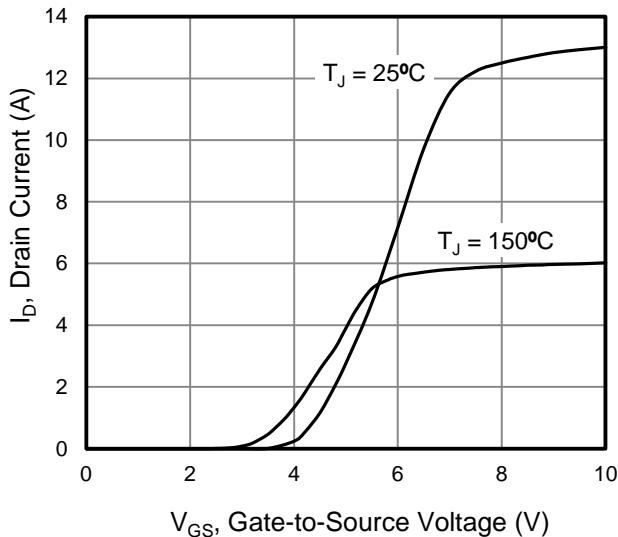
**Figure 3. Drain Current vs. Temperature**



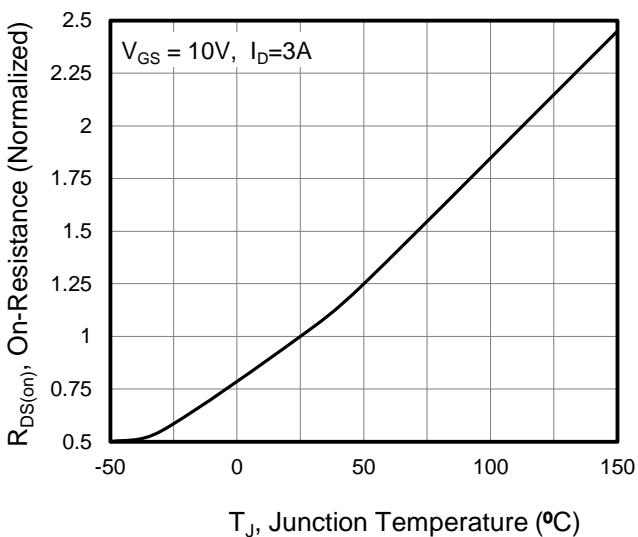
**Figure 4. Power Dissipation vs. Temperature**



**Figure 5. Transfer Characteristics**

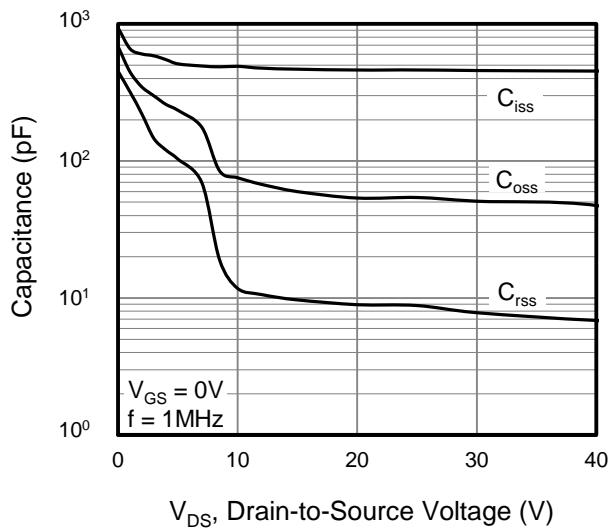


**Figure 6. On-Resistance vs. Temperature**

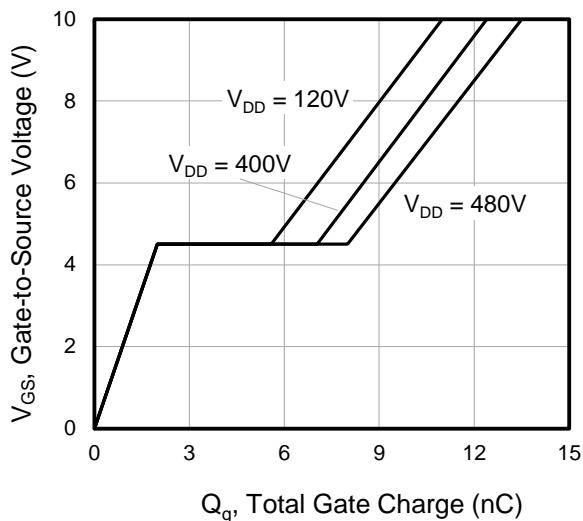


**Typical Characteristics**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

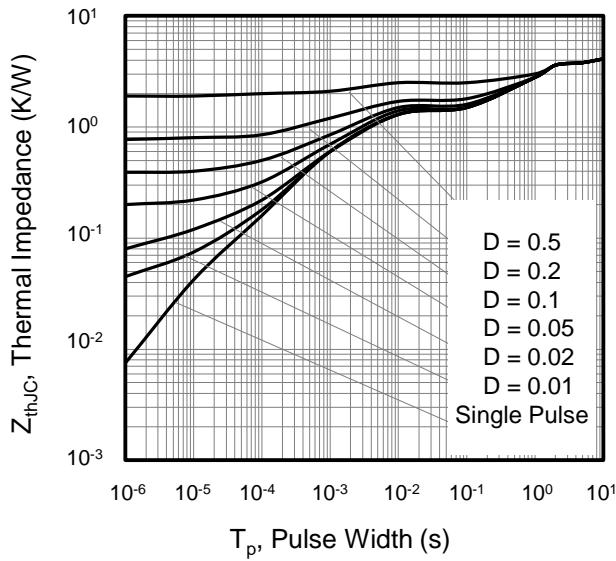
**Figure 7. Capacitance**



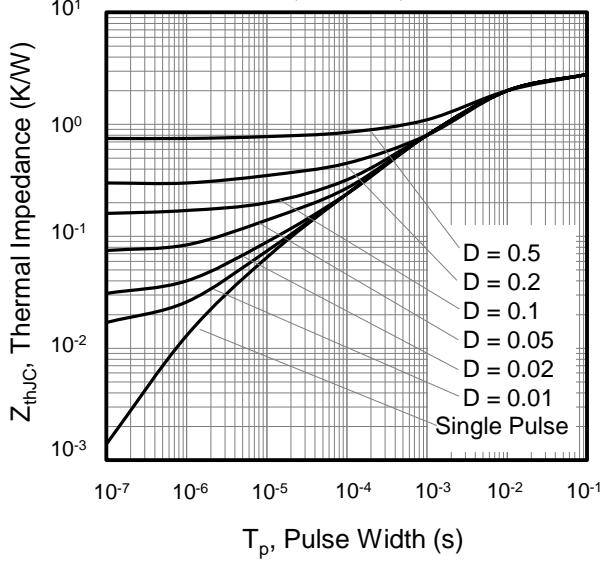
**Figure 8. Gate Charge**



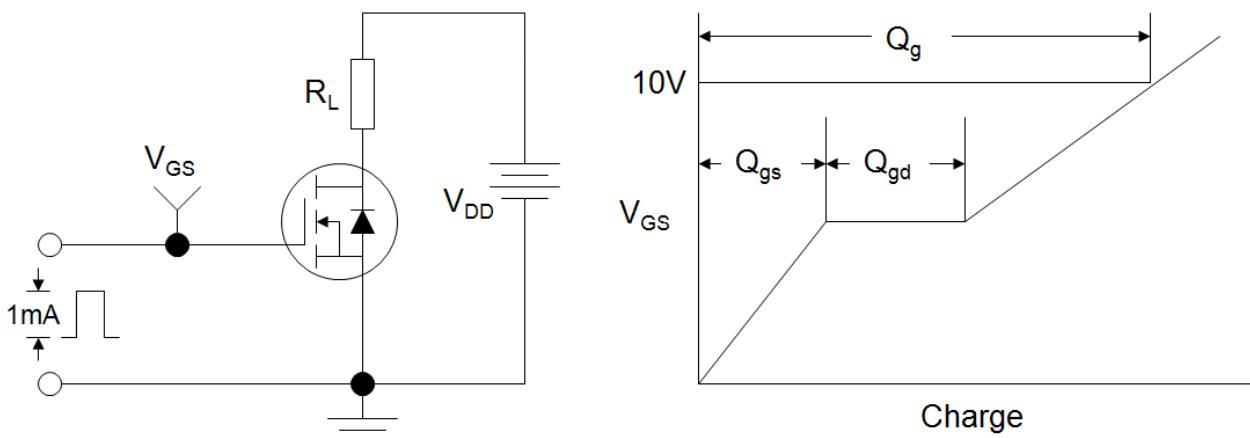
**Figure 9. Transient Thermal Impedance  
TO-220F**



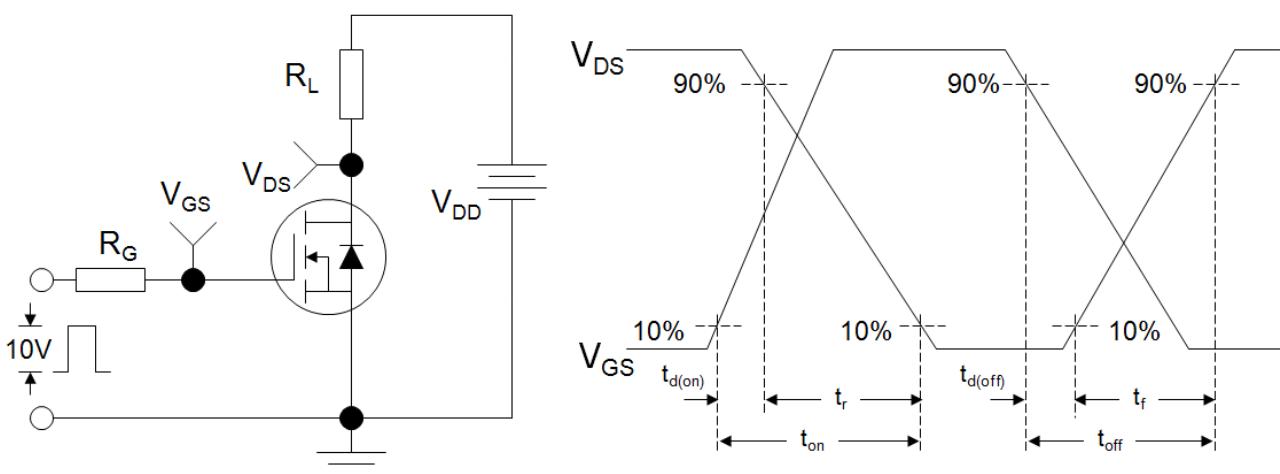
**Figure 10. Transient Thermal Impedance  
TO-220, TO-251, TO-252**



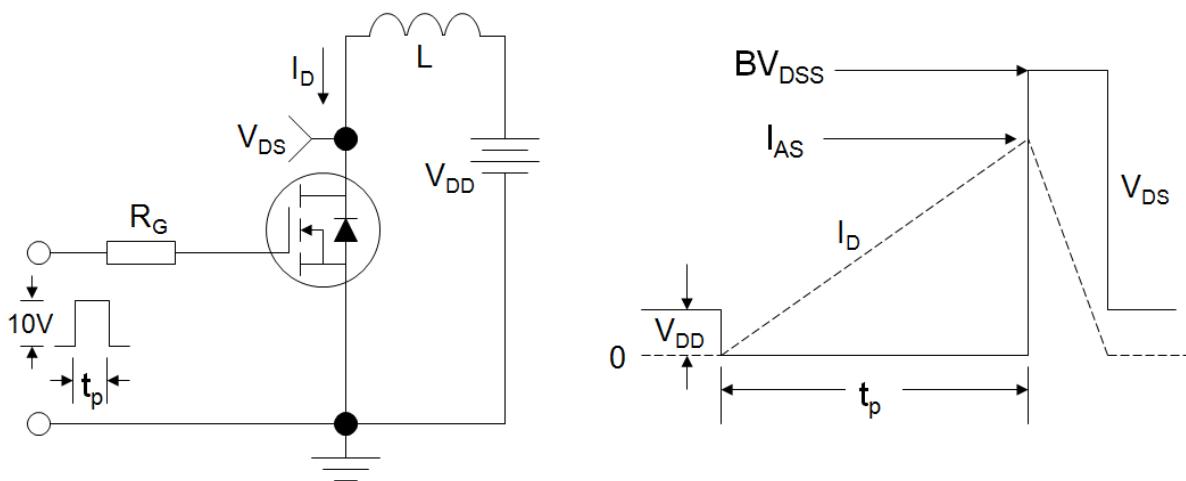
**Figure A: Gate Charge Test Circuit and Waveform**



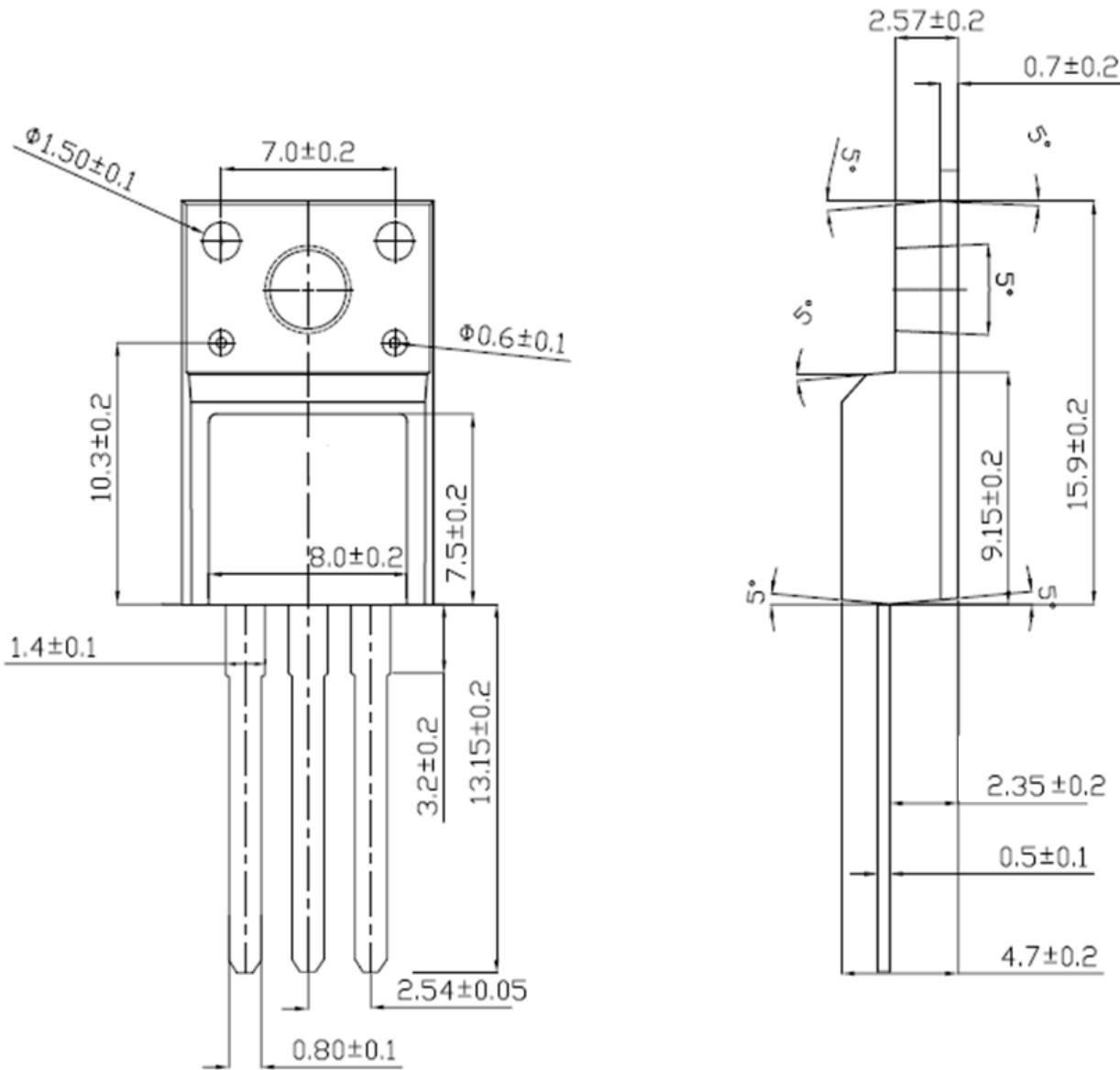
**Figure B: Resistive Switching Test Circuit and Waveform**



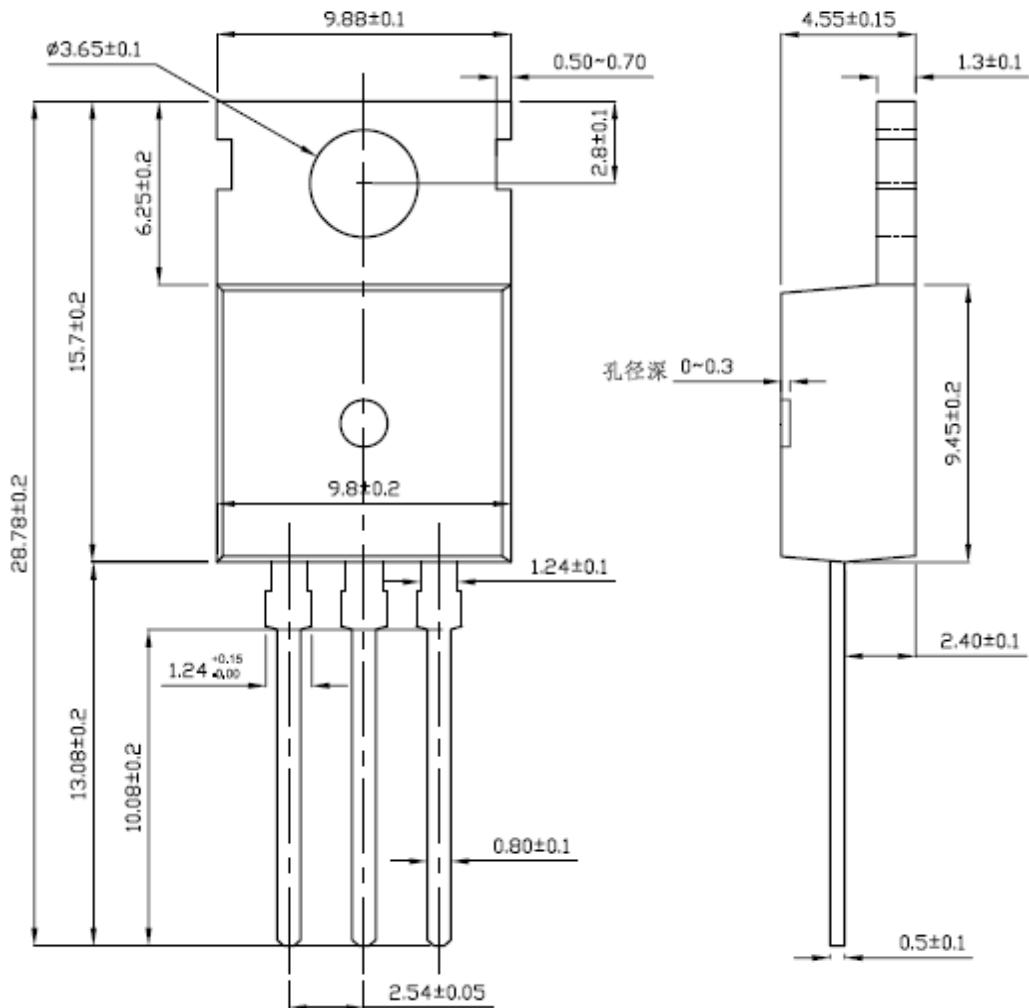
**Figure C: Unclamped Inductive Switching Test Circuit and Waveform**



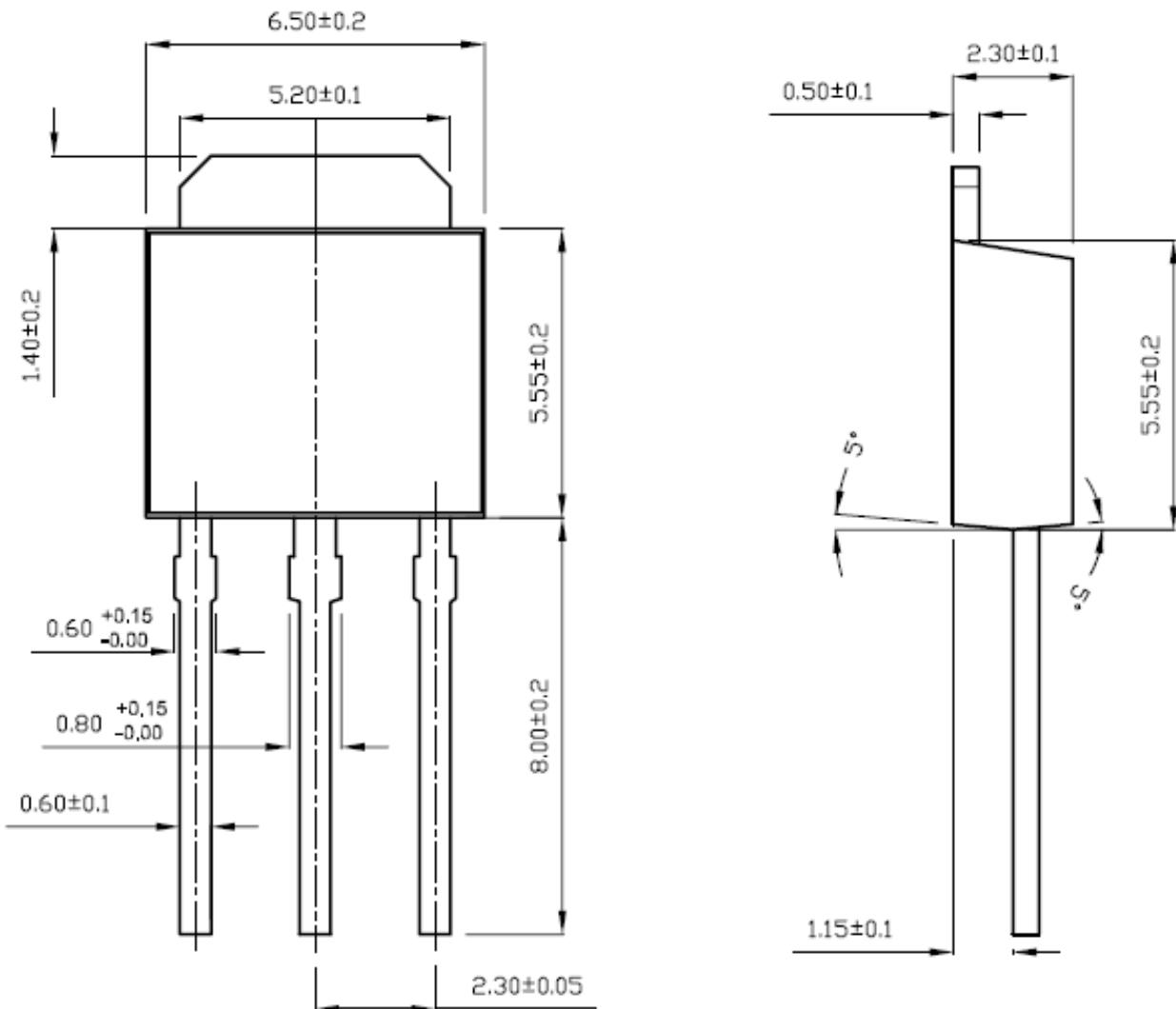
## TO-220F



## TO-220



## TO-251



## TO-252

