Low-power dual buffer and single inverter Rev. 5 — 29 April 2024

1. General description

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The 74AUP3G3404 is a dual buffer and single inverter.

Schmitt trigger action at all inputs makes the circuit tolerant of slower input rise and fall times across the entire V_{CC} range from 0.8 V to 3.6 V.

This device ensures a very low static and dynamic power consumption across the entire V_{CC} range from 0.8 V to 3.6 V.

This device is fully specified for partial Power-down applications using I_{OFF} . The I_{OFF} circuitry disables the output, preventing a damaging backflow current through the device when it is powered down.

2. Features and benefits

- Wide supply voltage range from 0.8 V to 3.6 V
- High noise immunity
- Low static power consumption; I_{CC} = 0.9 µA (maximum)
- Latch-up performance exceeds 100 mA per JESD 78B Class II
- Inputs accept voltages up to 3.6 V
- Low noise overshoot and undershoot < 10 % of V_{CC}
- IOFF circuitry provides partial Power-down mode operation
- Complies with JEDEC standards:
- JESD8-12 (0.8 V to 1.3 V)
- JESD8-11 (0.9 V to 1.65 V)
- JESD8-7 (1.2 V to 1.95 V)
- JESD8-5 (1.8 V to 2.7 V)
- JESD8-B (2.7 V to 3.6 V)
- ESD protection:
 - HBM: ANSI/ESDA/JEDEC JS-001 class 3A exceeds 5000 V
 - CDM: ANSI/ESDA/JEDEC JS-002 class C3 exceeds 1000 V
- Multiple package options
- Specified from -40 °C to +85 °C and -40 °C to +125 °C

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3. Ordering information

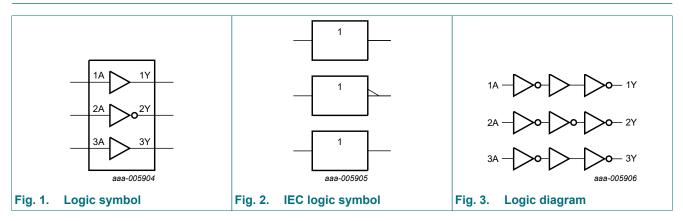
Type number	Package								
	Temperature range	Name	Description	Version					
74AUP3G3404DC	-40 °C to +125 °C	VSSOP8	plastic very thin shrink small outline package; 8 leads; body width 2.3 mm	<u>SOT765-1</u>					
74AUP3G3404GT	-40 °C to +125 °C	XSON8	plastic extremely thin small outline package; no leads; 8 terminals; body 1 × 1.95 × 0.5 mm	<u>SOT833-1</u>					
74AUP3G3404GN	-40 °C to +125 °C	XSON8	extremely thin small outline package; no leads; 8 terminals; body 1.2 × 1.0 × 0.35 mm	<u>SOT1116</u>					
74AUP3G3404GS	-40 °C to +125 °C	XSON8	extremely thin small outline package; no leads; 8 terminals; body 1.35 × 1.0 × 0.35 mm	<u>SOT1203</u>					

4. Marking

Table 2. Marking codes	
Type number	Marking code[1]
74AUP3G3404DC	aZ
74AUP3G3404GT	aZ
74AUP3G3404GN	aZ
74AUP3G3404GS	aZ

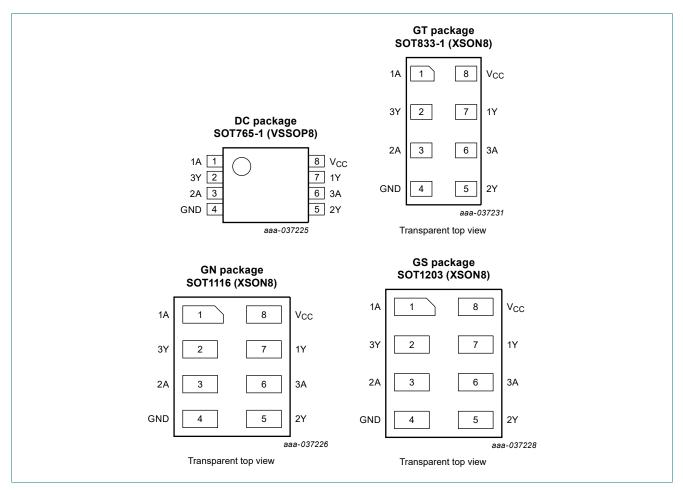
[1] The pin 1 indicator is located on the lower left corner of the device, below the marking code.

5. Functional diagram



6. Pinning information

6.1. Pinning



6.2. Pin description

Table 3. Pin description

Symbol	Pin	Description
1A, 2A, 3A	1, 3, 6	data input
1Y, 2Y, 3Y	7, 5, 2	data output
GND	4	ground (0 V)
V _{cc}	8	supply voltage

7. Functional description

Table 4. Function table

H = HIGH voltage level; L = LOW voltage level.

	Output
1A, 3A	1Y, 3Y
L	L
Н	Н

Table 5. Function table

H = HIGH voltage level; L = LOW voltage level.

Input	Output
2A	2Y
L	Н
Н	L

74AUP3G3404

8. Limiting values

Table 6. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134). Voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions		Min	Max	Unit
V _{CC}	supply voltage			-0.5	+4.6	V
l _{IK}	input clamping current	V _I < 0 V		-50	-	mA
VI	input voltage]	1]	-0.5	+4.6	V
I _{OK}	output clamping current	V ₀ < 0 V		-50	-	mA
Vo	output voltage	Active mode and Power-down mode [1]	-0.5	+4.6	V
I _O	output current	$V_{O} = 0 V \text{ to } V_{CC}$		-	±20	mA
I _{CC}	supply current			-	50	mA
I _{GND}	ground current			-50	-	mA
T _{stg}	storage temperature			-65	+150	°C
P _{tot}	total power dissipation	T _{amb} = -40 °C to +125 °C	2]	-	250	mW

[1] The minimum input and output voltage ratings may be exceeded if the input and output current ratings are observed.

[2] For SOT765-1 (VSSOP8) package: P_{tot} derates linearly with 4.9 mW/K above 99 °C. For SOT833-1 (XSON8) package: P_{tot} derates linearly with 3.1 mW/K above 68 °C.

For SOT1116 (XSON8) package: P_{tot} derates linearly with 4.2 mW/K above 90 °C.

For SOT1203 (XSON8) package: P_{tot} derates linearly with 3.6 mW/K above 81 °C.

9. Recommended operating conditions

Table 7. Operating conditions

Symbol	Parameter	Conditions	Min	Мах	Unit
V _{CC}	supply voltage		0.8	3.6	V
VI	input voltage		0	3.6	V
Vo	output voltage	Active mode	0	V _{CC}	V
		Power-down mode; V _{CC} = 0 V	0	3.6	V
T _{amb}	ambient temperature		-40	+125	°C
Δt/ΔV	input transition rise and fall rate	V _{CC} = 0.8 V to 3.6 V	-	200	ns/V

10. Static characteristics

Table 8. Static characteristics

At recommended operating conditions; voltages are referenced to GND (ground = 0 V).

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T _{amb} = 2	25 °C	1			1	
V _{IH}	HIGH-level input voltage	V _{CC} = 0.8 V	0.70V _{CC}	-	-	V
		V _{CC} = 0.9 V to 1.95 V	0.65V _{CC}	-	-	V
		V _{CC} = 2.3 V to 2.7 V	1.6	-	-	V
		V _{CC} = 3.0 V to 3.6 V	2.0	-	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 0.8 V	-	-	0.30V _{CC}	V
		V _{CC} = 0.9 V to 1.95 V	-	-	0.35V _{CC}	V
		V _{CC} = 2.3 V to 2.7 V	-	-	0.7	V
		V _{CC} = 3.0 V to 3.6 V	-	-	0.9	V
V _{OH}	HIGH-level output voltage	V _I = V _{IH} or V _{IL}				
		I_{O} = -20 µA; V_{CC} = 0.8 V to 3.6 V	V _{CC} - 0.1	-	-	V
		I _O = -1.1 mA; V _{CC} = 1.1 V	0.75V _{CC}	-	-	V
		I _O = -1.7 mA; V _{CC} = 1.4 V	1.11	-	-	V
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.32	-	-	V
		I _O = -2.3 mA; V _{CC} = 2.3 V	2.05	-	-	V
		I _O = -3.1 mA; V _{CC} = 2.3 V	1.9	-	-	V
		I _O = -2.7 mA; V _{CC} = 3.0 V	2.72	-	-	V
		I _O = -4.0 mA; V _{CC} = 3.0 V	2.6	-	-	V
V _{OL}	LOW-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		$I_0 = 20 \ \mu A; V_{CC} = 0.8 \ V \ to \ 3.6 \ V$	-	-	0.1	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	0.3V _{CC}	V
		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.31	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.31	V
		I _O = 2.3 mA; V _{CC} = 2.3 V	-	-	0.31	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.44	V
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.31	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	0.44	V
lı	input leakage current	V_I = GND to 3.6 V; V_{CC} = 0 V to 3.6 V	-	-	±0.1	μA
I _{OFF}	power-off leakage current	V_{I} or V_{O} = 0 V to 3.6 V; V_{CC} = 0 V	-	-	±0.2	μA
ΔI _{OFF}	additional power-off leakage current	$V_1 \text{ or } V_0 = 0 \text{ V to } 3.6 \text{ V};$ $V_{CC} = 0 \text{ V to } 0.2 \text{ V}$	-	-	±0.2	μA
I _{CC}	supply current	V _I = GND or V _{CC} ; I _O = 0 A; V _{CC} = 0.8 V to 3.6 V	-	-	0.5	μA
Δl _{CC}	additional supply current	$V_{I} = V_{CC} - 0.6 \text{ V}; I_{O} = 0 \text{ A}; V_{CC} = 3.3 \text{ V}$	-	-	40	μA
CI	input capacitance	V_{CC} = 0 V to 3.6 V; V _I = GND or V _{CC}	-	1.0	-	pF
Co	output capacitance	$V_0 = GND; V_{CC} = 0 V$	-	1.8	-	pF

Symbol	Parameter	Conditions	Min	Тур	Max	Unit	
T _{amb} = -4	40 °C to +85 °C						
V _{IH}	HIGH-level input voltage	V _{CC} = 0.8 V	0.70V _{CC}	-	-	V	
		V _{CC} = 0.9 V to 1.95 V	0.65V _{CC}	-	-	V	
		V _{CC} = 2.3 V to 2.7 V	1.6	-	-	V	
		V _{CC} = 3.0 V to 3.6 V	2.0	-	-	V	
V _{IL}	LOW-level input voltage	V _{CC} = 0.8 V	-	-	0.30V _{CC}	V	
		V _{CC} = 0.9 V to 1.95 V	-	-	0.35V _{CC}	V	
		V _{CC} = 2.3 V to 2.7 V	-	-	0.7	V	
		V _{CC} = 3.0 V to 3.6 V	-	-	0.9	V	
V _{OH}	HIGH-level output voltage	V _I = V _{IH} or V _{IL}					
		I_{O} = -20 µA; V_{CC} = 0.8 V to 3.6 V	V _{CC} - 0.1	-	-	V	
		I _O = -1.1 mA; V _{CC} = 1.1 V	0.7V _{CC}	-	-	V	
		I _O = -1.7 mA; V _{CC} = 1.4 V	1.03	-	-	V	
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.30	-	-	V	
		I _O = -2.3 mA; V _{CC} = 2.3 V	1.97	-	-	V	
		I _O = -3.1 mA; V _{CC} = 2.3 V	1.85	-	-	V	
		I _O = -2.7 mA; V _{CC} = 3.0 V	2.67	-	-	V	
		I _O = -4.0 mA; V _{CC} = 3.0 V	2.55		V		
V _{OL}	LOW-level output voltage	$V_{I} = V_{IH}$ or V_{IL}					
		I_{O} = 20 µA; V_{CC} = 0.8 V to 3.6 V	-	-	0.1	V	
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	0.3V _{CC}	V	
V _{OL} L		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.37	V	
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.35	V	
		I _O = 2.3 mA; V _{CC} = 2.3 V	-	-	0.33	V	
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.45	V	
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	0.33	V	
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-	- 0.30V _{CC} - 0.35V _{CC} - 0.7 - 0.7 - 0.9 	V	
l _l	input leakage current	V_I = GND to 3.6 V; V_{CC} = 0 V to 3.6 V	-	-	±0.5	μA	
I _{OFF}	power-off leakage current	$V_{I} \text{ or } V_{O} = 0 \text{ V to } 3.6 \text{ V}; V_{CC} = 0 \text{ V}$	-	-	±0.5	μA	
ΔI _{OFF}	additional power-off leakage current	$V_1 \text{ or } V_0 = 0 \text{ V to } 3.6 \text{ V};$ $V_{CC} = 0 \text{ V to } 0.2 \text{ V}$	-	-	±0.6	μA	
I _{CC}	supply current	$V_{I} = GND \text{ or } V_{CC}; I_{O} = 0 \text{ A}; \\ V_{CC} = 0.8 \text{ V to } 3.6 \text{ V}$	-				
ΔI _{CC}	additional supply current	$V_{I} = V_{CC} - 0.6 \text{ V}; I_{O} = 0 \text{ A}; V_{CC} = 3.3 \text{ V}$	-	-	50	μA	

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
T _{amb} = -4	40 °C to +125 °C				1	
VIH	HIGH-level input voltage	V _{CC} = 0.8 V	0.75V _{CC}	-	-	V
		V _{CC} = 0.9 V to 1.95 V	0.70V _{CC}	-	-	V
		V _{CC} = 2.3 V to 2.7 V	1.6	-	-	V
		V _{CC} = 3.0 V to 3.6 V	2.0	-	-	V
V _{IL}	LOW-level input voltage	V _{CC} = 0.8 V	-	-	0.25V _{CC}	V
		V _{CC} = 0.9 V to 1.95 V	-	-	0.30V _{CC}	V
		V _{CC} = 2.3 V to 2.7 V	-	-	0.7	V
		V _{CC} = 3.0 V to 3.6 V	-	-	0.9	V
V _{ОН}	HIGH-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		I_{O} = -20 µA; V_{CC} = 0.8 V to 3.6 V	V _{CC} - 0.11	-	-	V
		I _O = -1.1 mA; V _{CC} = 1.1 V	0.6V _{CC}	-	-	V
		I _O = -1.7 mA; V _{CC} = 1.4 V	0.93	-	-	V
		I _O = -1.9 mA; V _{CC} = 1.65 V	1.17	-	-	V
		I _O = -2.3 mA; V _{CC} = 2.3 V	1.77	-	-	V
		I _O = -3.1 mA; V _{CC} = 2.3 V	1.67	-	-	V
		I _O = -2.7 mA; V _{CC} = 3.0 V	2.40	-	-	V
		I _O = -4.0 mA; V _{CC} = 3.0 V	2.30	-	-	V
V _{OL}	LOW-level output voltage	$V_{I} = V_{IH} \text{ or } V_{IL}$				
		I_{O} = 20 µA; V_{CC} = 0.8 V to 3.6 V	-	-	0.11	V
		I _O = 1.1 mA; V _{CC} = 1.1 V	-	-	0.33V _{CC}	V
V _{OL} I		I _O = 1.7 mA; V _{CC} = 1.4 V	-	-	0.41	V
		I _O = 1.9 mA; V _{CC} = 1.65 V	-	-	0.39	V
		I _O = 2.3 mA; V _{CC} = 2.3 V	-	-	0.36	V
		I _O = 3.1 mA; V _{CC} = 2.3 V	-	-	0.50	V
		I _O = 2.7 mA; V _{CC} = 3.0 V	-	-	$\begin{array}{c c c c c }\hline & - \\ & - \\ & 0.25 V_{CC} \\ & 0.30 V_{CC} \\ & 0.7 \\ & 0.9 \\ \hline & & \\ & 0.7 \\ & 0.9 \\ \hline & & \\ & 0.7 \\ & & \\ & - \\ & - \\ & & \\ & -$	V
		I _O = 4.0 mA; V _{CC} = 3.0 V	-	-		V
I _I	input leakage current	V_{I} = GND to 3.6 V; V_{CC} = 0 V to 3.6 V	-	-	±0.75	μA
IOFF	power-off leakage current	V_{I} or V_{O} = 0 V to 3.6 V; V_{CC} = 0 V	-	-	±0.75	μA
ΔI _{OFF}	additional power-off leakage current	$V_1 \text{ or } V_0 = 0 \text{ V to } 3.6 \text{ V};$ $V_{CC} = 0 \text{ V to } 0.2 \text{ V}$	-	-	±0.75	μA
I _{CC}	supply current	V_I = GND or V_{CC} ; I_O = 0 A; V_{CC} = 0.8 V to 3.6 V	-	-	1.4	μA
ΔI _{CC}	additional supply current	$V_{I} = V_{CC} - 0.6 \text{ V}; I_{O} = 0 \text{ A}; V_{CC} = 3.3 \text{ V}$	-	-	75	μA

11. Dynamic characteristics

Table 9. Dynamic characteristics

Voltages are referenced to GND (ground = 0 V); for test circuit see Fig. 5.

Symbol	Parameter	ameter Conditions		25 °C			-40 °C to +85 °C		-40 °C to +125 °C	
			Min	Typ[1]	Max	Min	Max	Min	Max	
C _L = 5 p	F									
t _{pd}	propagation	nA to nY; see Fig. 4 [2]								
	delay	V _{CC} = 0.8 V	-	16.0	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	2.4	5.0	10.3	2.0	11.4	2.0	12.6	ns
		V _{CC} = 1.4 V to 1.6 V	1.8	3.6	6.4	1.6	7.4	1.6	8.2	ns
		V _{CC} = 1.65 V to 1.95 V	1.5	2.9	5.0	1.4	5.9	1.4	6.5	ns
		V _{CC} = 2.3 V to 2.7 V	1.2	2.4	3.9	1.1	4.5	1.1	5.0	ns
		V _{CC} = 3.0 V to 3.6 V	1.1	2.1	3.2	1.0	3.9	1.0	4.3	ns
C _L = 10	pF									
t _{pd}	propagation	nA to nY; see Fig. 4 [2]								
	delay	V _{CC} = 0.8 V	-	19.8	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	2.8	5.9	12.2	2.3	13.7	2.3	15.1	ns
		V _{CC} = 1.4 V to 1.6 V	2.3	4.2	7.5	1.9	8.7	1.9	9.6	ns
		V _{CC} = 1.65 V to 1.95 V	2.0	3.5	5.9	1.7	7.0	1.7	7.7	ns
		V _{CC} = 2.3 V to 2.7 V	1.7	2.9	4.6	1.5	5.4	1.5	6.0	ns
		V _{CC} = 3.0 V to 3.6 V	1.6	2.7	3.8	1.4	4.5	1.4	5.1	ns
C _L = 15	pF							<u> </u>	1	
t _{pd}	propagation	nA to nY; see Fig. 4 [2]								
	delay	V _{CC} = 0.8 V	-	23.3	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	3.2	6.7	13.0	2.6	15.8	2.6	17.4	ns
		V _{CC} = 1.4 V to 1.6 V	2.6	4.7	8.6	2.2	10.0	2.2	11.0	ns
		V _{CC} = 1.65 V to 1.95 V	2.3	4.0	6.7	2.0	8.0	2.0	8.8	ns
		V _{CC} = 2.3 V to 2.7 V	2.1	3.3	5.1	1.8	6.1	1.8	6.8	ns
		V _{CC} = 3.0 V to 3.6 V	2.0	3.1	4.2	1.6	5.0	1.6	5.5	ns
C _L = 30	pF							I		
t _{pd}	propagation	nA to nY; see Fig. 4 [2]								
	delay	V _{CC} = 0.8 V	-	33.6	-	-	-	-	-	ns
		V _{CC} = 1.1 V to 1.3 V	4.4	8.9	16.3	3.6	19.0	3.6	20.9	ns
		V _{CC} = 1.4 V to 1.6 V	3.6	6.3	10.8	3.2	12.9	3.2	14.2	ns
		V _{CC} = 1.65 V to 1.95 V	3.2	5.3	9.0	2.9	10.5	2.9	11.6	ns
		V _{CC} = 2.3 V to 2.7 V	2.9	4.5	6.5	2.6	7.6	2.6	8.5	ns
		V _{CC} = 3.0 V to 3.6 V	2.9	4.2	5.6	2.5	6.2	2.5	7.2	ns

Symbol	Parameter	ter Conditions		25 °C		-40 °C to +85 °C		-40 °C to +125 °C		Unit
			Min	Typ[1]	Мах	Min	Max	Min	Max	
C _L = 5 p	F, 10 pF, 15 pF	and 30 pF				<u> </u>				
C _{PD}	power dissipation	f _i = 1 MHz; [3] [4] V _I = GND to V _{CC}								
	capacitance	V _{CC} = 0.8 V	-	2.5	-	-	-	-	-	pF
		V _{CC} = 1.1 V to 1.3 V	-	2.7	-	-	-	-	-	pF
		V _{CC} = 1.4 V to 1.6 V	-	2.8	-	-	-	-	-	pF
		V _{CC} = 1.65 V to 1.95 V	-	3.0	-	-	-	-	-	pF
		V_{CC} = 2.3 V to 2.7 V	-	3.5	-	-	-	-	-	pF
		V _{CC} = 3.0 V to 3.6 V	-	4.0	-	-	-	-	-	pF

All typical values are measured at nominal V_{CC}. [1]

[2]

 t_{pd} is the same as t_{PLH} and t_{PHL} . All specified values are the average typical values over all stated loads. [3]

 C_{PD} is used to determine the dynamic power dissipation (P_D in μ W). $P_D = C_{PD} x V_{CC}^2 x f_i x N + \Sigma (C_L x V_{CC}^2 x f_o)$ where: [4]

f_i = input frequency in MHz;

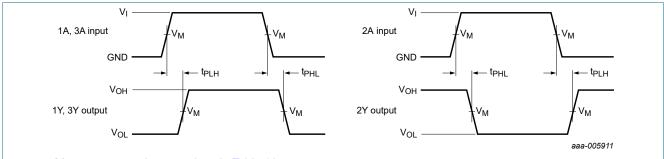
f_o = output frequency in MHz;

C_L = load capacitance in pF;

V_{CC} = supply voltage in V;

N = number of inputs switching; $\Sigma(C_L \times V_{CC})^2 \times f_o) = \text{sum of the outputs.}$

11.1. Waveforms and test circuit



Measurement points are given in Table 10.

Logic levels: V_{OL} and V_{OH} are typical output voltage levels that occur with the output load.

The data input 1A to output 1Y, input 2A to output 2Y and input 3A to output 3Y propagation delays Fig. 4.

Table 10. Measurement points

Supply voltage	Output	Input		
V _{cc}	V _M	V _M	VI	$t_r = t_f$
0.8 V to 3.6 V	0.5V _{CC}	0.5V _{CC}	V _{CC}	≤ 3.0 ns

Low-power dual buffer and single inverter

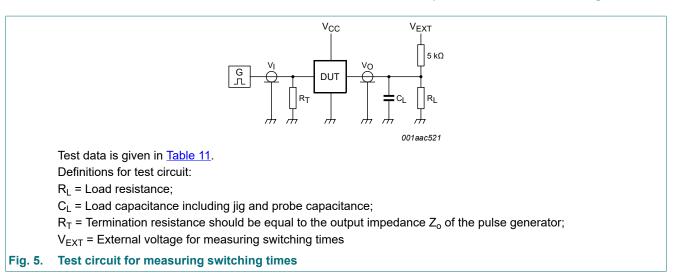


Table 11. Test data

Supply voltage	Load		age Load V _{EXT}		
Vcc	CL	R _L [1]	t _{PLH} , t _{PHL}	t _{PZH} , t _{PHZ}	t _{PZL} , t _{PLZ}
0.8 V to 3.6 V	5 pF, 10 pF, 15 pF, and 30 pF	5 kΩ or 1 MΩ	open	GND	2V _{CC}

[1] For measuring enable and disable times, $R_L = 5 k\Omega$.

For measuring propagation delays, setup and hold times, and pulse width, R_L = 1 M Ω .

74AUP3G3404

12. Package outline

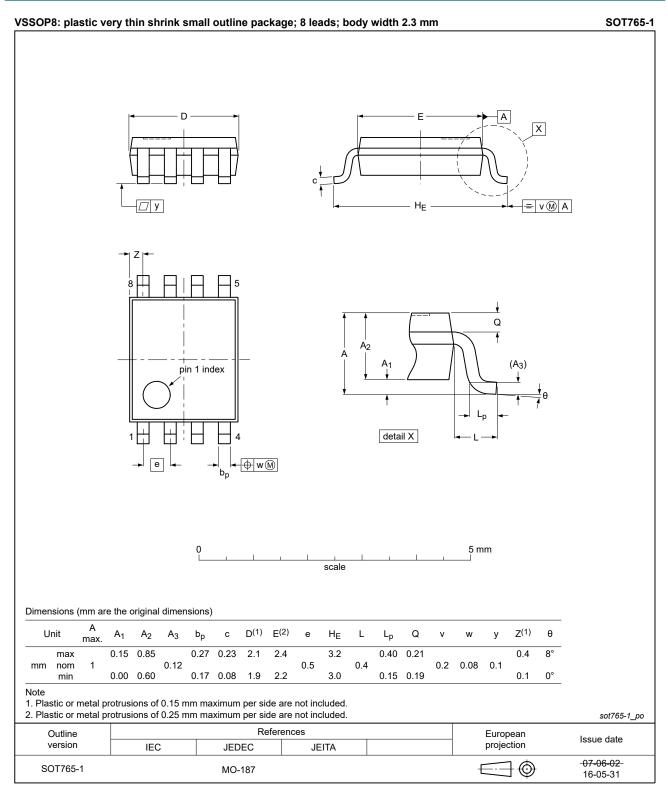


Fig. 6. Package outline SOT765-1 (VSSOP8)

Low-power dual buffer and single inverter

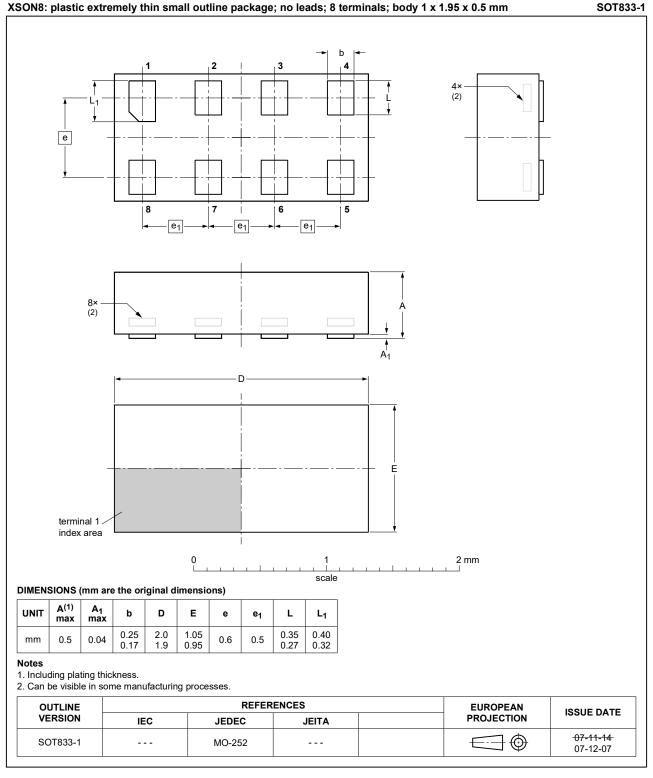


Fig. 7. Package outline SOT833-1 (XSON8)

Low-power dual buffer and single inverter

XSON8: extremely thin small outline package; no leads; 8 terminals; body 1.2 x 1.0 x 0.35 mm

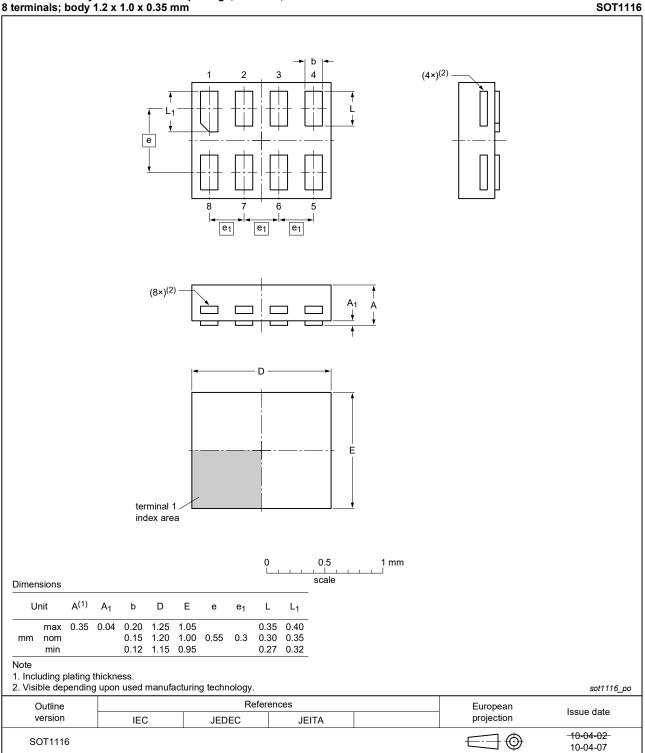


Fig. 8. Package outline SOT1116 (XSON8)

Low-power dual buffer and single inverter

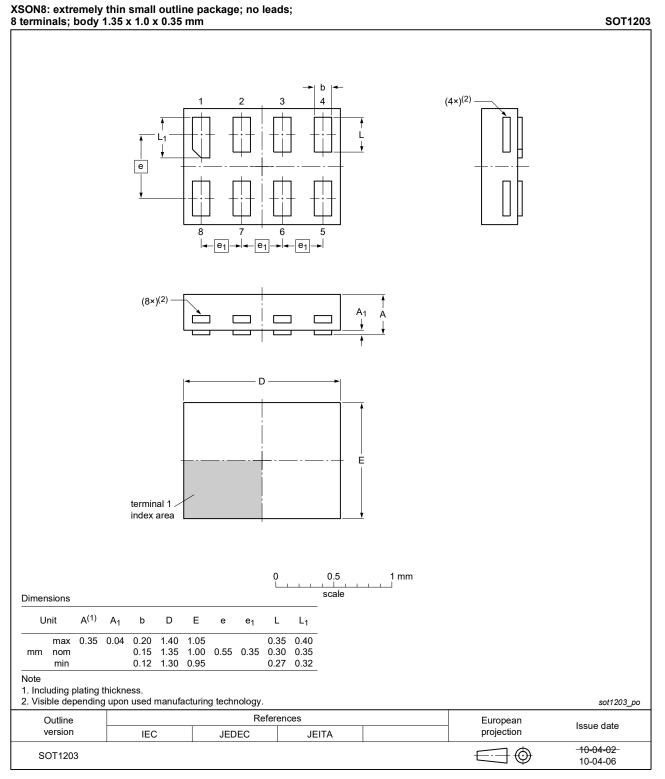


Fig. 9. Package outline SOT1203 (XSON8)

13. Abbreviations

Table 12. Abbreviations		
Acronym	Description	
CDM	Charged Device Model	
DUT	Device Under Test	
ESD	ElectroStatic Discharge	
HBM	Human Body Model	

14. Revision history

Table 13. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
74AUP3G3404 v.5	20240429	Product data sheet	-	74AUP3G3404 v.4
Modifications:	Type numb	er 74AUP3G3404GF (SO	T1089/XSON8) rer	noved.
74AUP3G3404 v.4	20230728	Product data sheet	-	74AUP3G3404 v.3
Modifications:	• <u>Section 2</u> : I	SD specification updated	according to the la	atest JEDEC standard.
74AUP3G3404 v.3	20210113	Product data sheet	-	74AUP3G3404 v.2
Modifications:		er 74AUP3G3404GM (SO Derating values for P _{tot} tota	,	
74AUP3G3404 v.2	20190426	Product data sheet	-	74AUP3G3404 v.1
Modifications:	guidelines o Legal texts Type numb	of this data sheet has bee of Nexperia. have been adapted to the er 74AUP3G3404GD (SO utline drawing <u>SOT765-1</u> a	new company nar T996-2/XSON8) re	me where appropriate. emoved.
74AUP3G3404 v.1	20130114	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

- [2] The term 'short data sheet' is explained in section "Definitions".
- [3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <u>https://www.nexperia.com</u>.

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