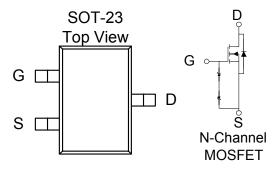
## N-Channel 20V (D-S) MOSFET

These miniature surface mount MOSFETs utilize a high cell density trench process to provide low  $r_{DS(on)}$  and to ensure minimal power loss and heat dissipation. Typical applications are DC-DC converters and power management in portable and battery-powered products such as computers, printers, PCMCIA cards, cellular and cordless telephones.

PRODUCT SUMMARY				
$V_{DS}(V)$	$\eta_{\mathrm{DS(on)}}(\Omega)$	$I_{D}(A)$		
20	$0.022@V_{CS}=4.5V$	6.5		
20	$0.026 @V_{CS} = 2.5V$	5.8		

- Low r<sub>DS(on)</sub> provides higher efficiency and extends battery life
- Low thermal impedance copper leadframe SOT-23 saves board space
- Fast switching speed
- High performance trench technology





ABSOLUTE MAXIMUM RATINGS ( $T_A = 25$ °C UNLESS OTHERWISE NOTED)					
Parameter Parameter			Maximum	Units	
Drain-Source Voltage			20	V	
Gate-Source Voltage			±8	V	
Continuo Dorio Como da	T <sub>A</sub> =25°C	Τ	6.3		
Continuous Drain Current <sup>a</sup>	$T_A=25^{\circ}C$ $T_A=70^{\circ}C$	тD	5.2	A	
Pulsed Drain Current <sup>b</sup>			±20		
Continuous Source Current (Diode Conduction) <sup>a</sup>		$I_S$	1.6	A	
D a	$T_A=25^{\circ}C$	D	1.3	$\mathbf{w}$	
Power Dissipation <sup>a</sup>	$T_A=25^{\circ}C$ $T_A=70^{\circ}C$	FD	0.9	VV	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	-55 to 150	°C	

THERMAL RESISTANCE RATINGS				
Parameter	Symbol	Maximum	Units	
M · I	t <= 5 sec	R <sub>THJA</sub>	100	°C/W
Maximum Junction-to-Ambient <sup>a</sup>	Steady-State		166	

1

## Notes

- a. Surface Mounted on 1" x 1" FR4 Board.
- b. Pulse width limited by maximum junction temperature

SPECIFICATIONS (T <sub>A</sub> = 25°C UNLESS OTHERWISE NOTED)							
Parameter	G 1.1	T. (C. P.	Limits			TT.*4	
	Symbol	Test Conditions	Min	Тур	Max	Unit	
Static							
Cate-Threshold Voltage	VGS(th)	$V_{DS}=V_{GS}$ , $I_D=250$ uA	0.4			V	
Gate-Body Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{CS} = \pm 8 \text{ V}$			±100	nA	
7 (4 1/4   D : C   4	T	$V_{DS} = 16 \text{ V}, V_{GS} = 0 \text{ V}$			1		
Zero Cate Voltage Drain Current	Ides	$V_{DS} = 16 \text{ V}, V_{CS} = 0 \text{ V}, T_J = 55^{\circ}\text{C}$			10	uA	
On-State Drain Current <sup>A</sup>	I <sub>D(on)</sub>	$V_{DS} = 5 V, V_{GS} = 4.5 V$	10			Α	
Drain-Source On-Resistance <sup>A</sup>		$V_{GS} = 4.5 \text{ V, ID} = 6.5 \text{ A}$			22	mΩ	
Drain-Source On-Resistance	fDS(on)	$V_{GS} = 2.5 \text{ V}, I_D = 5.8 \text{ A}$			26		
Forward Tranconductance <sup>A</sup>	gs	$V_{DS} = 10 \text{ V}, I_D = 6.5 \text{ A}$		11.3		S	
Diode Forward Voltage	V <sub>SD</sub>	$I_S = 1.6 A, V_{GS} = 0 V$		0.75		V	
Dynamic <sup>b</sup>							
Total Gate Charge	Q	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_D = 6.5 \text{ A}$		13.4		nC	
Gate-Source Charge	$Q_{gs}$			0.9			
Gate-Drain Charge	Qgd			2.0			
Turn-On Delay Time	td(on)			8			
Rise Time	tr	$V_{DD} = 10 \text{ V}, R_L = 15 \Omega, I_D = 1 \text{ A}, V_{GEN} = 4.5 \text{ V}$		24		ns	
Tum-Off Delay Time	td(off)			35			
Fall-Time	tf			10			

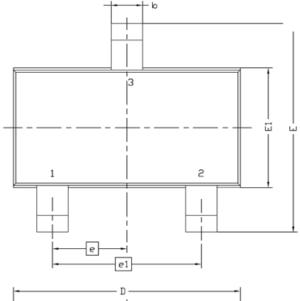
## Notes

- a. Pulse test:  $PW \le 300us duty cycle \le 2\%$ .
- b. Guaranteed by design, not subject to production testing.

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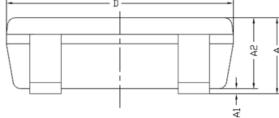
**Analog Power** SOT-23

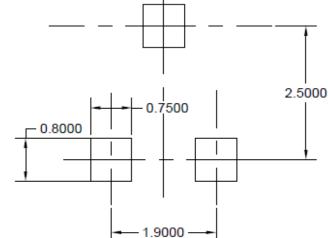
## **Package Information**

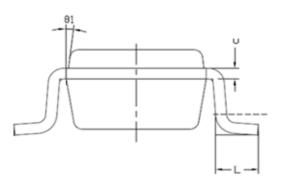


Symbol	MILLIMETERS		
Symbol	MIN	MAX	
Α	0.8	1.2	
A1	0	0.1	
A2	0.7	1.1	
b	0.3	0.5	
С	0.1	0.2	
D	2.7	3.1	
Е	2.6	3	
E1	1.4	1.8	
е	0.95 BSC		
e1	1.9 BSC		
L	0.3	0.6	
θ1	7° NOM		

**Recommended Pad Layout** 







Note: Drain opening is recommended to be solder mask defined in a copper fill for improved thermal performance

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