



Pictorial View Only; NTS

### PRODUCT OVERVIEW

D1U54P-W-2000-12-HxxC is a series of 2000W highly efficient power factor corrected front end power supplies with a 12Vdc main output, capable of active current sharing, and a standby output. A status LED, hardware logic signals, PMBus™ digital communications and cold redundancy capability are provided. The 1U low profile > 46W/cubic inch package make this series ideal for delivering reliable, efficient power to servers, workstations, storage systems and other 12V distributed power architectures.

### ORDERING GUIDE\*

Part Number	Output power at Nominal Input Voltage						Main Output	Standby Output	Airflow		
	230-240 Vac	220 Vac	208 Vac	200 Vac	110-120 Vac	100Vac					
D1U54P-W-2000-12-HA3C	2000W	1956W	1848W	1776W	1400W	1260W	12Vdc	5Vdc	F ⇒ B B ⇒ F		
D1U54P-W-2000-12-HA4C								12Vdc	F ⇒ B B ⇒ F		
D1U54P-W-2000-12-HB3C										3.3Vdc	F ⇒ B B ⇒ F
D1U54P-W-2000-12-HB4C											
D1U54P-W-2000-12-HC3C											
D1U54P-W-2000-12-HC4C											

\*See [www.murata.com/products/power](http://www.murata.com/products/power) for model-specific availability.

### FEATURES

- 2000W continuous output power
- Cold Redundant power management features
- IEC60320-C16 connector for maximized low line operation
- 80 Plus® Certified Platinum, HxXC models
- 12V main output, 120% surge current capability
- 3.3V, 5.0V & 12V Standby Output Options
- 1U height: 2.15" x 12.65" x 1.57"
- > 46 Watts per cubic inch density
- N+1 redundant, Hot Swap Capable
- Active (digital) current sharing on 12V main output; Integral ORing /isolation provided for both outputs; compatible with DC input series
- Internal cooling fan (variable speed)
- Overvoltage, overcurrent, overtemperature Protection
- PMBus™/I<sup>2</sup>C interface with LED status indicators
- RoHS compliant
- Two Year Warranty

### INPUT CHARACTERISTICS

Parameter	Conditions	Min.	Nom.	Max.	Units
Input Source Voltage AC Operating Range <sup>1</sup>		90	100/110/240	264	Vac
Input Source Voltage DC Operating Range <sup>1,2</sup>			240		Vdc
Input Source Frequency		47	50/60	63	Hz
Turn-on Input Voltage	Ramp up	74		84	Vac
Turn-off Input Voltage	Ramp down	70		80	
Maximum current at Vin = 180Vac/60Hz	2,000W			15	Arms
Inrush Current	Cold start between 0 to 200msec, 264Vac			35	Apk
Power Factor	At 230Vac, full load		0.99		
Efficiency (230Vac), excluding fan load HxXC models 80 Plus® Certified Platinum	20% load	90			%
	50% load	94			
	100% load	91			

<sup>1</sup> Insert power supply into mating connector prior to applying input voltage;

<sup>2</sup> Only for China

### OUTPUT VOLTAGE CHARACTERISTICS

Output	Parameter	Conditions	Min.	Typ.	Max.	Units	
12V	Nominal Output Voltage			12		Vdc	
	Output Set Point Accuracy	50% load; Tamb =25°C	-0.5		+0.5		
	Line and Load Regulation <sup>2</sup>	Measured at remote sense	-1.0		+1.5	%	
	Ripple Voltage & Noise <sup>1,2</sup>	20MHz Bandwidth			120	mV p-p	
	Output Current	2000W (207-264 Vac) Continuous <sup>4</sup>		0		166.7	A
		1956W (198-242 Vac) Continuous		0		163	
		1848W (188-228 Vac) Continuous		0		154	
		1776W (180-220 Vac) Continuous		0		148	
		1400W (99-132Vac) Continuous		0		116.7	
		1260W (90-110 Vac) Continuous		0		105	
Load Capacitance				30,000	µF		
12VSB	Nominal Output Voltage	50% load; Tamb =25°C		12.0		Vdc	
	Line and Load Regulation <sup>3</sup>	Measured at PSU side of connector	11.7		12.3		
	Ripple Voltage & Noise <sup>1,3</sup>	20MHz Bandwidth			120	mV p-p	
	Output Current		0		3.0	A	
	Load Capacitance				1500	µF	
3.3VSB	Nominal Output Voltage	50% load; Tamb =25°C		3.30		Vdc	
	Line and Load Regulation <sup>3</sup>	Measured at PSU side of connector	3.14		3.46	Vdc	
	Ripple Voltage & Noise <sup>1,3</sup>	20MHz Bandwidth			75	mV p-p	
	Output Current		0		3.0	A	
	Load Capacitance				3,000	µF	
5.0VSB	Nominal Output Voltage	50% load; Tamb =25°C		5.0		Vdc	
	Line and Load Regulation <sup>3</sup>	Measured at PSU side of connector	4.76		5.24	Vdc	
	Ripple Voltage & Noise <sup>1,3</sup>	20MHz Bandwidth			75	mV p-p	
	Output Current		0		3.0	A	
	Load Capacitance				3,000	µF	

<sup>1</sup> Ripple and noise are measured with 0.1 µF of ceramic capacitance and 10 µF of tantalum capacitance on each of the power supply outputs. A short coaxial cable to the scope termination is used.

<sup>2</sup> Minimum Load of 7A applied to meet these limits. <sup>3</sup> Minimum Load of 0.25A applied to meet these limits <sup>4</sup> Peak current 200A, 100ms max.



For full details please visit our website:  
[www.murata-ps.com/rohs](http://www.murata-ps.com/rohs)



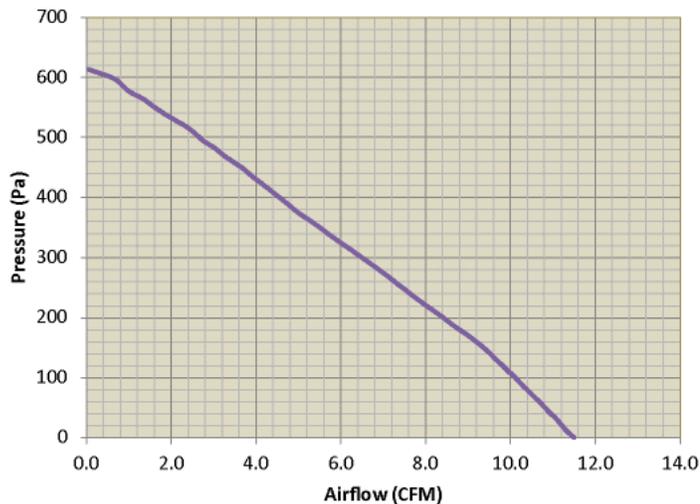
Test Certificate  
and Test Report

OUTPUT CHARACTERISTICS					
Parameter	Conditions	Min.	Typ.	Max.	Units
Startup Time	AC ramp up			3	s
Transient Response	12V Main 10% to 60% load step (50% max load change); 1A/ $\mu$ s slew rate; 2,000 $\mu$ F load capacitance	-5		+5	% nom
	Recovery Time to within 1% Vnom		2		ms
	VSB, 10% to 60% load step (50% max. load change); 1A/ $\mu$ s slew rate.	-5		+5	% nom
	Recovery Time to within 1% Vnom		2		ms
Current sharing accuracy	At 100% load	-5		+5	%
Hot Swap Transients	All outputs remain in regulation	-5		+5	%
Holdup Time	230-240Vac in voltage ranges, 2000W load, output dropping to 10.8V	10			ms
	230-240Vac in voltage ranges, 1000W load, output dropping to 10.8V	20			ms

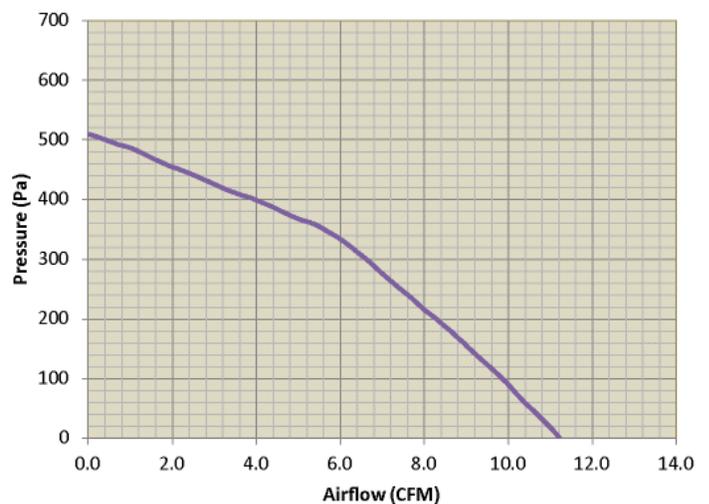
ENVIRONMENTAL CHARACTERISTICS					
Parameter	Conditions	Min.	Typ.	Max.	Units
Storage Temperature Range		-20		70	°C
Operating Temperature Range	2000W; 230-240Vac nom.	-5		50	
	1900W @ 4000M; 220Vac, 230-240Vac	-5		40	
Operating Humidity	Noncondensing	5		90	%
Storage Humidity		5		95	
Altitude (Derating $\geq$ 40°C intake)				4,000	m
Shock	30G non-operating				
Operational Vibration	Sine sweep; 5-150Hz, 2G; random vibration, 5-500Hz, 1.11G				
MTBF	Per Telcordia SR-332 M1C3 @40°C	540K			hrs.
Safety Approval Standards (Planned Submissions)	CAN/CSA-C22.2 No. 60950-1-07, Amendment 1:2011, Amendment 2:2014 (MOD)				
	ANSI/UL 60950-1-2014				
	CSA: IEC 60950-1:2005 (Second Edition) + Am 1:2009 + Am 2:2013				
	TUV: EN 60950-1:2006+A11:2009+A1:2010+A12:2011+A2:2013				
	BSMI: CNS14336-1 (099/09/30); CNS13438 (095/06/01)				
Input Fuse	CQC: GB4943.1-2011; GB9254-1-2008; GB17625.1-2012				
Weight	Dual 20A/420VAC/420DC fuse provided as a series protective element in both input "line" and "neutral" connection				
	2.56 lbs. (1.16 kg)				

**AIRFLOW CHARACTERISTICS**

**P - Q Curve (Front to Back)**



**P - Q Curve (Back to Front)**



Fan Speed = 100% Duty Cycle

### PROTECTION CHARACTERISTICS

Output Voltage	Parameter	Conditions	Min.	Typ.	Max.	Units
-	Over temperature <sup>2,3</sup>	Auto restart	76	80	84	°C
12V	Overvoltage	Latching <sup>1</sup>	13.0		14.5	V
	Short-circuit	Latching <sup>1</sup>	200		-	
	Overcurrent (180-264Vac)	Hiccup mode, 5 retries before <sup>1</sup> Latch-off. Protection is delayed 100mS to accommodate Peak Power	184		200	A
12VSB	Overcurrent (90-150Vac)	Hiccup mode, 5 retries before <sup>1</sup> Latch-off.	110		120	A
	Overvoltage	Latching <sup>1</sup>	13.0		14.5	V
5.0VSB	Overcurrent	Hiccup	3.1		4.5	A
	Overvoltage	Latching <sup>1</sup>	5.4		6.0	V
3.3VSB	Overcurrent	Hiccup	3.1		5.0	A
	Overvoltage	Latching <sup>1</sup>	3.6		4.0	V
	Overcurrent	Hiccup	3.1		5.0	A

<sup>1</sup> Latch-off requires recycling either the AC input or PS\_ON to resume operation

<sup>2</sup> Warning indication (PMBus status register bits and Amber LED status) occurs at 75°C nominal and recovers at 70°C nominal; fault indication and shutdown engages at 80 °C nominal and recovers at 75°C nominal

<sup>3</sup> Operating the power supply above the maximum operating temperature (see "ENVIRONMENTAL CHARACTERISTICS") is considered an abnormal condition, may negatively impact power supply life and is not recommended.

### ISOLATION CHARACTERISTICS

Parameter	Conditions	Min.	Typ.	Max.	Units
Insulation Safety Rating / Test Voltage	Input to Output - Reinforced	3000			Vrms
	Input to Chassis - Basic	1500			Vrms
Isolation	Output to Chassis	500			Vdc

### EMISSIONS AND IMMUNITY

Characteristic	Standard	Compliance
Input Current Harmonics	IEC/EN 61000-3-2	Complies with Class A limits
Voltage Fluctuation and Flicker	IEC/EN 61000-3-3	Complies
Conducted Emissions	FCC 47 CFR Part15/CISPR22/EN55032	Class A with 6dB margin
ESD Immunity	IEC/EN 61000-4-2	±8KV Contact; ±15KV air discharge; Criteria A
Radiated Field Immunity	IEC/EN 61000-4-3	3V/m, 1KHz, 80% AM, 80MHz to 1GHz Criteria A <sup>2</sup>
Electrical Fast Transients/Burst Immunity	IEC/EN 61000-4-4	<sup>1</sup> Level 3 (2kV), criteria A
Surge Immunity	IEC/EN 61000-4-5	<sup>1</sup> Level 3 (2kV Line-Earth, 1kV Line-Line), criteria A
RF Conducted Immunity	IEC/EN 61000-4-6	Level 2 (3V/M) criteria A
Voltage Dips, Interruptions	IEC/EN 61000-4-11	230V <sub>in</sub> , 100% load, Phase 0°, Dip 100% Duration 10ms (A) 230V <sub>in</sub> , 50% load, Phase 0°, Dip 100% Duration 20ms (VSB:A, V1:B) 230V <sub>in</sub> , 100% load, Phase 0°, Dip 100% Duration > 20ms (VSB, V1:B)

<sup>1</sup> measured at power supply's AC input connector 2 installed in system

<sup>2</sup> Contingent upon final system design

### STATUS INDICATOR (BICOLOUR LED)

Condition	LED Status
Standby - ON; Main output - OFF; AC PRESENT	Blinking green 1Hz
Standby - ON; Main output - ON; No faults detected	Solid green
Fault Detected ;Main output, VSB output, Fan, overtemperature, input OVP (Note: coincides with the PMBus™ Status_Register bit flags)	Solid Amber
AC input power absent or no I <sup>2</sup> C slave address detected (See <a href="#">ADDR signal</a> for configuration details); VSB OVP	Off
Power Supply Warning Event	Blinking Amber
Cold Redundant mode – "COLD_STANDBY" / "FORCED STANDBY" MODE	Blinking green 2Hz

### ADDR ADDRESS SELECTION

ADDR pin (D2) resistor to GND (K-ohm)*	Power Supply Main Controller (Serial Communications Slave Address)	Power Supply External EEPROM (Serial Communications Slave Address)
0.82	0xB0	0xA0
2.7	0xB2	0xA2
5.6	0xB4	0xA4
8.2	0xB6	0xA6
15	0xB8	0xA8
27	0xBA	0xAA
56	0xBC	0xAC
180	0xBE	0xAE
OPEN/PS_INHIBIT	0x80	0xA0

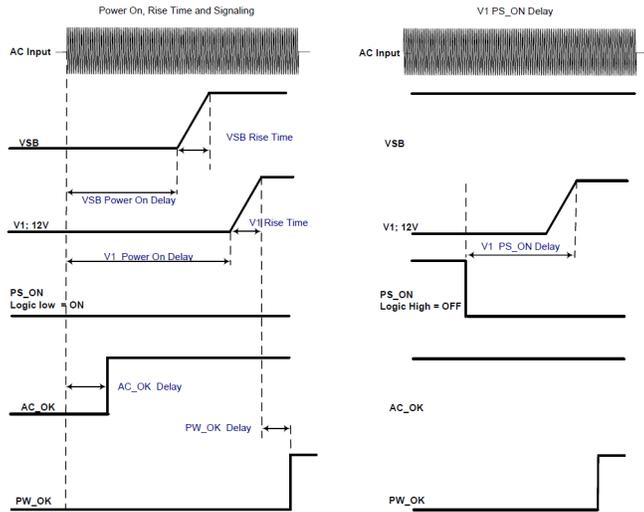
\* The resistor shall be +/-5% tolerance or better

STATUS AND CONTROL SIGNALS			
Signal Name	I/O	Description	Interface Details
AC_OK/RAPID_ON Link to: <a href="#">Pin Table</a>	Output	<p>This multi-function signal and is configured as one of the following:</p> <p><b>AC_OK</b> (Default setting at initial power up): Output is driven high when input source is available and within acceptable limits. The output is driven low to indicate loss of input power. This signal de-asserts a minimum of 5ms before loss of main output and provides an accurate indication of loss of AC input voltage.</p> <p><b>RAPID_ON</b> is a two state analog signal forms the cold redundant bus with up to four (4) load connected PSUs. This signal is used exclusively by the PSU for cold redundant mode operation, and is configured via PMBus™; see <a href="#">ACAN-80 and 81</a> for details.</p> <p>Rapid_ON signal/bus provides these three functions:</p> <ul style="list-style-type: none"> <li>• Pull-up bus voltage: Bus pull-up is provided by the single PSU or the first PSU assigned the roll of "ACTIVE &amp; MASTER" aka "COLD_REDUNDANT ACTIVE". More than one PSU can be assigned as "ACTIVE" only the first PSU assigned this roll provides the pull-up path and is why this PSU is referred to as the "Master".</li> <li>• Each bus connected PSU drives the Rapid_ON bus low when any fault is detected.</li> <li>• Each bus connected PSU powers on its main output rapidly within 100µs after detection of LOW state.</li> </ul> <p><b>Note:</b> "Rapid_ON" pin configuration is retained once setup via PMBus™, even if AC power is recycled and remains the new default setting until commanded to INPUT_OK via PMBus™.</p>	<p><b>AC_OK</b> Pulled up via 511R to internal 5V bias supply and pulled down to DC Return via 10K OHM resistor.</p> <p><b>RAPID_ON:</b> Pulled 511R to 5V internal bias supply of the ACTIVE &amp; MASTER PSU; Pull-Down = 10K. Bus voltage reduces with the QTY of bus connected power supplies</p>
PW_OK (Output OK) Link to: <a href="#">Pin Table</a>	Output	<p>The signal is asserted, driven high, by the power supply to indicate 12V main output is valid. Should a 12v main output fault occur, the PW_OK signal will de-assert + driven low. PW_OK output is driven low to indicate that the main output is outside of lower limit of regulation.</p>	<p>Pulled up internally via 10K to VDD<sup>1</sup>. A logic high &gt;2.0Vdc A logic low &lt;0.8Vdc Driven low by internal CMOS buffer (open drain output).</p>
SMB_ALERT (FAULT) Link to: <a href="#">Pin Table</a>	Output	<p>The signal output is driven low to indicate that the power supply has detected a fault / status register bits (except Status_CML) and is intended to alert the system. This output must be driven high when the power is operating correctly (within specified limits). The signal will revert to a high level when the fault stimulus (that caused the alert) is removed. The LED Status (Fault) reflects the status of SMB_ALERT signal</p>	<p>Pulled up internally via 10K to VDD<sup>1</sup>. A logic high &gt;2.0Vdc A logic low &lt;0.8Vdc Driven low by internal CMOS buffer (open drain output).</p>
PRESENT_L (Power Supply Absent) Link to: <a href="#">Pin Table</a>	Output	<p>The signal is used to detect the presence (installed) of a PSU by the host system. The signal is connected to PSU logic SGND within the power module.</p>	<p>Passive connection to +VSB_Return. A logic low &lt;0.8Vdc</p>
PS_ON (Main Out Enable/Disable) Link to:	Input	<p>This signal is pulled up, within the power supply, to the internal housekeeping supply. The power supply main 12Vdc output will be enabled when this signal is pulled low (to output return). In the low state the signal input shall not source more than 1mA of current. The 12Vdc output will be disabled when the input is driven higher than 2.4V, or open circuited. Cycling this signal shall clear latched fault conditions.</p>	<p>Pulled up internally via 10K to VDD<sup>1</sup>. A logic high &gt;2.0Vdc A logic low &lt;0.8Vdc Input is via CMOS Schmitt trigger buffer.</p>
ADDR (Address Select) / PS_INHIBIT  Link to: <a href="#">Pin Table</a> <a href="#">LED Table</a>	Input	<p>A multi-function signal used to detect presence in the system and also to set the slave device address. When this pin is left open all power module operation will be inhibited and a default slave address will be assigned (0x80h) to allow communication with slave devices. When the power module is inserted into a system this pin will be pulled (via a suitable external select resistor to +VSB_Return, and in conjunction with an internal resistor divider chain, shall configure the required slave (EEPROM and microprocessor) address used for digital communications. Note: An external pulldown resistor is required from ADDR to +VSB_Return for operation of the main output, ≤180K.</p>	<p>DC voltage between the limits of 0 and +3.3Vdc. System side pull-down resistor required, ≤180K</p>
SCL (Serial Clock) Link to Pin Table: Link to: <a href="#">Pin Table</a>	Both	<p>A serial clock line compatible with PMBus™ Power Systems Management Protocol Part 1 – General Requirements Rev 1.2. No additional internal capacitance is added that would affect the speed of the bus. The signal is provided with a series isolator device to disconnect the internal power supply bus in the event that the power module is unpowered.</p>	<p>VIL is 0.8V maximum VOL is 0.4V maximum when sinking 3mA VIH is 2.1V minimum</p>
SDA (Serial Data) Link to: <a href="#">Pin Table</a>	Both	<p>A serial data line compatible with PMBus™ Power Systems Management Protocol Part 1 – General Requirements Rev 1.2. The signal is provided with a series isolator device to disconnect the internal power supply bus in the event that the power module is unpowered.</p>	<p>VIL is 0.8V maximum VOL is 0.4V maximum when sinking 3mA VIH is 2.1V minimum</p>
V1_SENSE & V1SENSE_RTN Link to: <a href="#">Pin Table</a> <a href="#">Pin Table</a>	Input	<p>Remote sense connections intended to be connected at and sense the voltage at the point of load. The voltage sense will interact with the internal module regulation loop to compensate for voltage drops due to connection resistance between the output connector and the load. If remote sense compensation is not required then the voltage can be configured for local sense by:</p> <ol style="list-style-type: none"> <li>1. V1_SENSE directly connected to main output</li> <li>2. V1_SENSE_RTN directly connected to main output RTN</li> </ol>	<p>Compensation for up to 0.12Vdc total connection drop (output and return connections).</p>
ISHARE Link to: <a href="#">Pin Table</a>	Both	<p>This signal is connected between sharing units forming an ISHARE bus. It is a bi-directional analog bus voltage controls the current share between sharing units. PSU responds to change in bus voltage and also can change the bus voltage based on the load drawn from it. For single PSU, the voltage on the pin/ISHARE bus would read approximately 8VDC at 100% load. For two identical units sharing the same 100% load this would read approximately 4VDC for perfect current sharing (i.e. 50% module load capability per unit). This signal is also used by cold redundant enabled power supplies to determine Main output on/off state. Refer to <a href="#">ACAN-81</a> for details.</p>	<p>Analogue voltage: +8V maximum; 10K to +12V_RTN</p>

<sup>1</sup> VDD is an internal voltage rail derived from VSB and an internal housekeeping rail ("diode ORed") and is compatible with the voltage levels of TTL and CMOS logic families.

## TIMING SPECIFICATIONS

### Turn-On Delay & Output Rise Time:

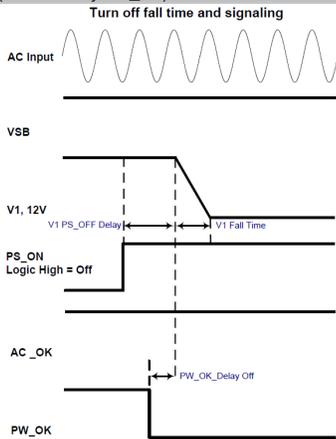


Time	Min	Max
3.3 Vsb Rise time <sup>1</sup>	3.9ms	4.8ms
5 Vsb Rise time <sup>1</sup>	6.8ms	8.3ms
12 Vsb Rise time <sup>1</sup>	7.2ms	8.8ms
V1 Rise time	7ms	15ms
Vsb Power-on-delay		2700ms
V1 Power-on-delay		3000ms
V1 PS_ON delay	100ms	150ms
V1 PWOK delay	100ms	400ms
ACOK detect	250ms	1500ms

<sup>1</sup>The output rise times shall be measured from 10% of the nominal output to the lower limit of the regulation band as defined in the following tables.

## TIMING SPECIFICATIONS

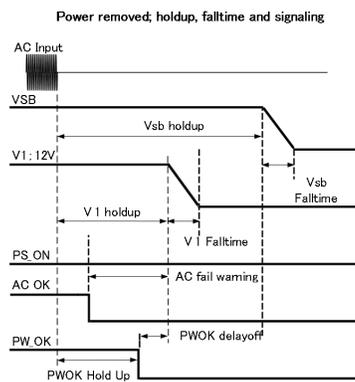
### Turn-Off (Shutdown by PS\_ON)



Turn-Off Timing	Min	Max	Notes
V1 Fall time	-	-	Must be monotonic
V1 PS_OFF delay	0ms	5ms	
PW_OK delay off	0.5ms		

## TIMING SPECIFICATIONS

### Power Removal Holdup



Power Removal Timing	Min	Typ.	Max	Notes
Vsb holdup	40ms		-	
V1 holdup	10ms		-	Full load (2000W)
AC Fail Warning	5ms		-	All load and input conditions
PWOK delay off	1ms		-	Full load (2000W)
Vsb Fall time	-		-	Must be monotonic
V1 Fall time	-		-	

### DC OUTPUT & SIGNAL INTERFACE MATING CONNECTOR

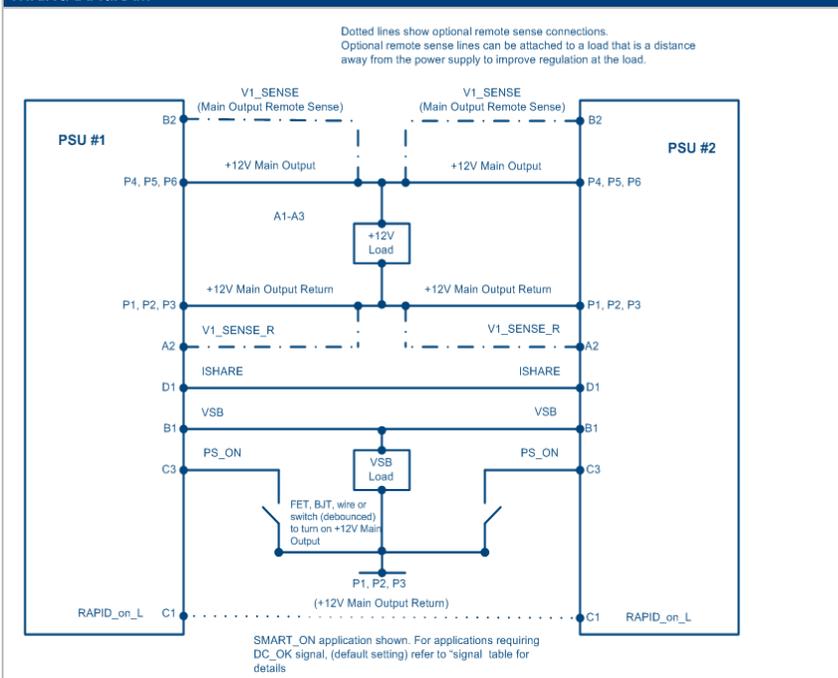
Part Number	Description																
<ul style="list-style-type: none"> <li>Manufacturer: Amphenol FCI</li> <li>Manufacturers Type: Power Blade RA STB PLUG</li> <li>Manufacturers Part Number: 10106264-6003003LF</li> </ul>																	
<p><b>PSU Side connector:</b> FCI: 10106262-6003006</p>	<table border="1"> <thead> <tr> <th>PRODUCT NO.</th> <th>ROWS</th> <th>HP</th> <th>S</th> </tr> <tr> <td></td> <td></td> <td>E1 P6 P5 P4 P3 P2 P1 3 2 1</td> <td>E2</td> </tr> </thead> <tbody> <tr> <td>10106264-6003003LF</td> <td>D C B A</td> <td>H3 H3 H3 H1 H1 H1</td> <td>H3 H3 H3 H3 H3 H3</td> </tr> </tbody> </table>	PRODUCT NO.	ROWS	HP	S			E1 P6 P5 P4 P3 P2 P1 3 2 1	E2	10106264-6003003LF	D C B A	H3 H3 H3 H1 H1 H1	H3 H3 H3 H3 H3 H3				
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10106264-6003003LF	D C B A	H3 H3 H3 H1 H1 H1	H3 H3 H3 H3 H3 H3														
	<table border="1"> <thead> <tr> <th>CODE</th> <th>DESCRIPTION</th> </tr> </thead> <tbody> <tr> <td>E</td> <td>STD SIGNAL CONTACT, ROW A</td> </tr> <tr> <td>F</td> <td>STD SIGNAL CONTACT, ROW B</td> </tr> <tr> <td>G</td> <td>STD SIGNAL CONTACT, ROW C</td> </tr> <tr> <td>H</td> <td>STD SIGNAL CONTACT, ROW D</td> </tr> <tr> <td>H1</td> <td>MFBL HIGH POWER CONTACT(3.43)</td> </tr> <tr> <td>H3</td> <td>STD HIGH POWER CONTACT(3.43)</td> </tr> <tr> <td>HA</td> <td>METAL HOLD DOWN</td> </tr> </tbody> </table>	CODE	DESCRIPTION	E	STD SIGNAL CONTACT, ROW A	F	STD SIGNAL CONTACT, ROW B	G	STD SIGNAL CONTACT, ROW C	H	STD SIGNAL CONTACT, ROW D	H1	MFBL HIGH POWER CONTACT(3.43)	H3	STD HIGH POWER CONTACT(3.43)	HA	METAL HOLD DOWN
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<sup>1</sup> recessed (the shortest pin) in order to facilitate glitch free hot swap. It is "last to make, first to break" in the mating sequence.

### PIN ASSIGNMENTS - Power Module Output & Signal Interface Connector: FCI 10106262-6003006LF

Pin	Signal Name	Comments
P4, P5, P6	V1	+ 12V main output
P1, P2, P3	V1 & V2 RETURN	+ 12V main and standby output return
A3	SDA	Short Pin <sup>1</sup> I2C data signal line; shorter MLFB pin; Link to definition table: <a href="#">STATUS AND CONTROL SIGNALS_SDA</a>
B3	SCL	Short Pin <sup>1</sup> I2C clock signal line; shorter MLFB pin; Link to definition table: <a href="#">STATUS AND CONTROL SIGNALS_SCL</a>
C3	PS_ON	Short Pin <sup>1</sup> Remote on/off Short; shorter MLFB pin
D3	SMB_ALERT	Short Pin <sup>1</sup> 2C alert signal; shorter MLFB pin; Link to definition table: <a href="#">STATUS AND CONTROL SIGNALS_SMB_ALERT</a>
A2	V1_SENSE_R	- Remote Sense/ return; Link to definition table: <a href="#">STATUS AND CONTROL SIGNALS_V_Sense</a>
B2	V1_SENSE	+ Remote Sense; Link to definition table: <a href="#">STATUS AND CONTROL SIGNALS_V_Sense</a>
C2	PW_OK	Power OK; Link to definition table: <a href="#">STATUS AND CONTROL SIGNALS_SMB_PW_OK</a>
D2	ADDR	Address Selection (select by external pull down resistor); Link to selection table <a href="#">Address Selection Table</a> ; Link to definition table <a href="#">STATUS AND CONTROL SIGNALS_ADDR</a>
A1	PRESENT_L	PS Present; Link to definition table <a href="#">STATUS AND CONTROL SIGNALS Present</a>
B1	VSB	Standby output
C1	AC_OK/ RAPID_ON_L	Default: AC_OK; Selectable via PMBus™; Link to definition table <a href="#">STATUS AND CONTROL SIGNALS_AC_OK/RAPID_ON</a>
D1	ISHARE	Current share bus; Link to definition table <a href="#">STATUS AND CONTROL SIGNALS_ISHARE</a>

### WIRING DIAGRAM

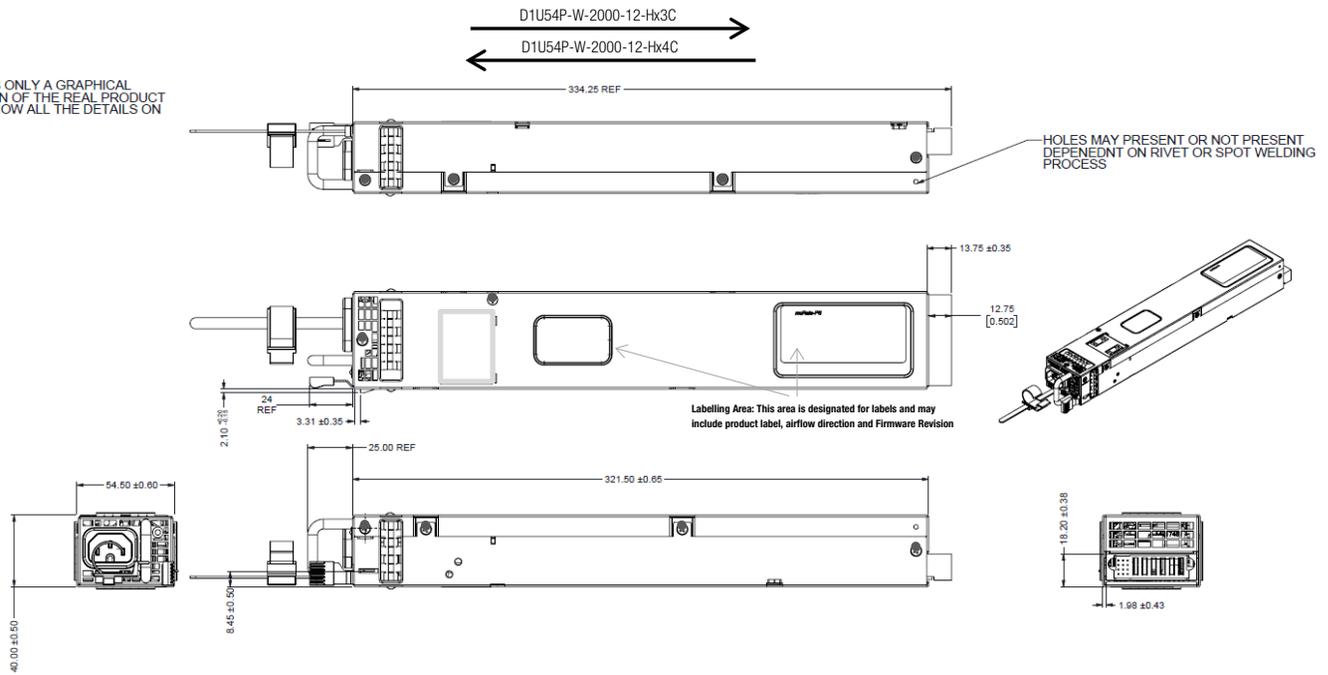


### CURRENT SHARING NOTES

- Main Output current sharing is achieved using the active current share method.
- Current sharing can be achieved with or without the remote (V\_SENSE) connected to the common load.
- +VSB Outputs can be tied together for redundancy but total combined output power must not exceed the rated standby power of a single unit. The +VSB output has an internal ORING MOSFET for additional redundancy/internal short protection.
- Main output power of units sharing must not exceed the rated power of a single unit during power up.
- The current sharing pin D1 is connected between sharing units (forming an ISHARE bus). It is an input and/or an output (bi-directional analog bus) as the voltage on the line controls the current share between sharing units. A power supply will respond to a change in this voltage but a power supply can also change the voltage depending on the load drawn from it. On a single unit the voltage on the pin (and the common ISHARE bus would read approximately 8VDC at 100% load (power module capability). For two units sharing the same load this would read approximately 4VDC for perfect current sharing (i.e. 50% power capability per unit).
- The load for both the main 12V and the VSB outputs at initial startup shall not be allowed to exceed the capability of a single unit. The load can be increased after a delay of 3 sec (minimum), to allow all sharing units to achieve steady state regulation

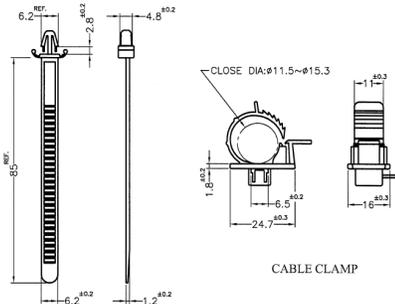
### MECHANICAL OUTLINE

THIS DRAWING IS ONLY A GRAPHICAL REPRESENTATION OF THE REAL PRODUCT AND MAY NOT SHOW ALL THE DETAILS ON THE PRODUCT.



### AC Cord Retainer: Kang Yang SWPL-65(B)

CHASSIS HOLE  $\phi 5.0$   
CHASSIS THICKNESS 0.5~2.0mm



1. AC input connector: IEC 60320-C16
2. This drawing is a graphical representation of the product and may not show all fine details. Please contact Murata for 3D model for details
3. Reference File: \\tor-file06\mechanical\_design\Eng\_wip\UserPDDwg\1965\_1966\_1967\D75090019652\_DIMENSIONAL DRAWING AND BUSINESS SPEC Mar.15, 2018
4. Dimensions in mm, Material: 0.80mm hot dipped galvanized steel, Grade G60 minimum spangle finished with a CR(6+) free corrosion resistant coating
5. Product under development, subject to change. Contact factory for latest version.

### OPTIONAL ACCESSORIES

Description	Part Number
Connector Card	D1U54P-12-CONC2K

### APPLICATION NOTES

Document Number	Description	URL Link to Document
ACAN-82	D1U54P-12-CONC2K , Output Connector Card	<a href="#">Click to open ACAN-82</a>
ACAN-81	D1U54P-W-2000-12-HxxTC PMBus™ Protocol	<a href="#">Click to open ACAN-81</a>
ACAN-80	Cold Redundancy; RAPID_ON	<a href="#">Click to open ACAN-80</a>

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ISO 9001 and 14001 REGISTERED



This product is subject to the following operating requirements and the Life and Safety Critical Application Sales Policy. Refer to: <https://www.murata-ps.com/requirements/>

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