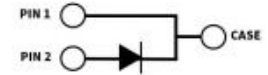


E6D40065G

E-Series Automotive
650 V, 40 A Silicon Carbide Schottky Diode

Description

With the performance advantages of a Silicon Carbide (SiC) Schottky Barrier diode, power electronics systems can expect to meet higher efficiency standards than Si-based solutions, while also reaching higher frequencies and power densities. SiC diodes can be easily paralleled to meet various application demands, without concern of thermal runaway. In combination with the reduced cooling requirements and improved thermal performance of SiC products, SiC diodes are able to provide lower overall system costs in a variety of diverse applications.



Part Number	Package	Marking
E6D40065G	TO-263-2	E6D40065G

Features

- Low Forward Voltage (V_F) Drop with Positive Temperature Coefficient
- Zero Reverse Recovery Current / Forward Recovery Voltage
- Temperature-Independent Switching Behavior
- Automotive Qualified (AEC Q101) and PPAP Capable

Applications

- Interleaved or Bridgless PFC
- DC/DC On Board Battery Chargers
- Boost for PFC & DC-DC Stages
- AC/DC On Board Chargers
- PFC Output Rectification

Maximum Ratings ($T_C = 25^\circ\text{C}$ Unless Otherwise Specified)

Parameter	Symbol	Value	Unit	Test Conditions	Notes
Repetitive Peak Reverse Voltage	V_{RRM}	650	V		
Surge Peak Reverse Voltage	V_{RSM}	650			
DC Blocking Voltage	V_{DC}	650			
Continuous Forward Current	I_F	131	A	$T_C = 25^\circ\text{C}$	Fig. 3
		66		$T_C = 125^\circ\text{C}$	
		42		$T_C = 150^\circ\text{C}$	
Repetitive Peak Forward Surge Current	I_{FRM}	145	A	$T_C = 25^\circ\text{C}, t_p = 10\text{ ms}, \text{Half Sine Wave}$	
		83		$T_C = 110^\circ\text{C}, t_p = 10\text{ ms}, \text{Half Sine Wave}$	
Non-Repetitive Forward Surge Current	I_{FSM}	245	A	$T_C = 25^\circ\text{C}, t_p = 10\text{ ms}, \text{Half Sine Wave}$	
		223		$T_C = 110^\circ\text{C}, t_p = 10\text{ ms}, \text{Half Sine Wave}$	
Power Dissipation	P_{tot}	366	W	$T_C = 25^\circ\text{C}$	Fig. 4
		158		$T_C = 110^\circ\text{C}$	
i^2t value	$\int i^2 dt$	300	A^2s	$T_C = 25^\circ\text{C}, t_p = 10\text{ ms}$	
		248		$T_C = 110^\circ\text{C}, t_p = 10\text{ ms}$	

Electrical Characteristics

Parameter	Symbol	Typ.	Max.	Unit	Test Conditions	Notes
Forward Voltage	V_F	1.35	1.5	V	$I_F = 40\text{ A}, T_j = 25\text{ }^{\circ}\text{C}$	Fig. 1
		1.5	1.6		$I_F = 40\text{ A}, T_j = 175\text{ }^{\circ}\text{C}$	
Reverse Current	I_R	10	150	μA	$V_R = 650\text{ V}, T_j = 25\text{ }^{\circ}\text{C}$	Fig. 2
		55	500		$V_R = 650\text{ V}, T_j = 175\text{ }^{\circ}\text{C}$	
Total Capacitive Charge	Q_C	135		nC	$V_R = 400\text{ V}, T_j = 25\text{ }^{\circ}\text{C}$	Fig. 5
Total Capacitance	C	2485		pF	$V_R = 0\text{ V}, T_j = 25\text{ }^{\circ}\text{C}, f = 1\text{ MHz}$	Fig. 6
		259			$V_R = 200\text{ V}, T_j = 25\text{ }^{\circ}\text{C}, f = 1\text{ MHz}$	
		198			$V_R = 400\text{ V}, T_j = 25\text{ }^{\circ}\text{C}, f = 1\text{ MHz}$	
Capacitance Stored Energy	E_C	20.3		μJ	$V_R = 400\text{ V}$	Fig. 7

Notes:
SiC Schottky Diodes are majority carrier devices, so there is no reverse recovery charge.

Thermal & Mechanical Characteristics

Parameter	Symbol	Value	Unit	Notes
Thermal Resistance, Junction to Case (Typical)	$R_{\theta, JC (TYP)}$	0.28	$^{\circ}\text{C} / \text{W}$	
Thermal Resistance, Junction to Case (Max)	$R_{\theta, JC (MAX)}$	0.41	$^{\circ}\text{C} / \text{W}$	
Junction Temperature	T_j	-55 to +175	$^{\circ}\text{C}$	
Case & Storage Temperature	T_c	-55 to +175		

Typical Performance

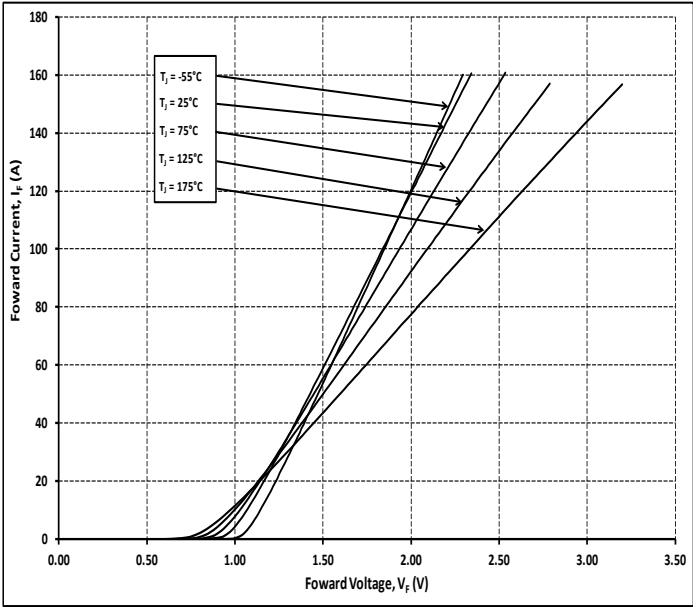


Figure 1
Forward Characteristics

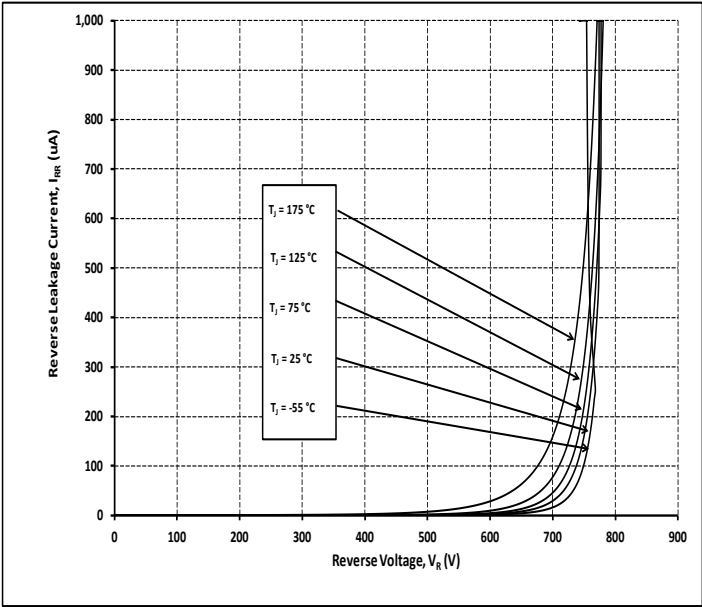


Figure 2
Reverse Characteristics

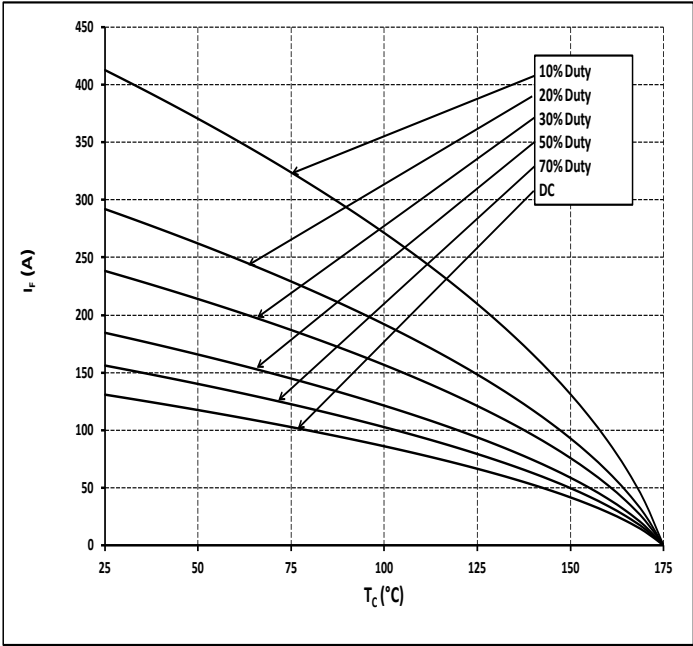


Figure 3
Current Derating

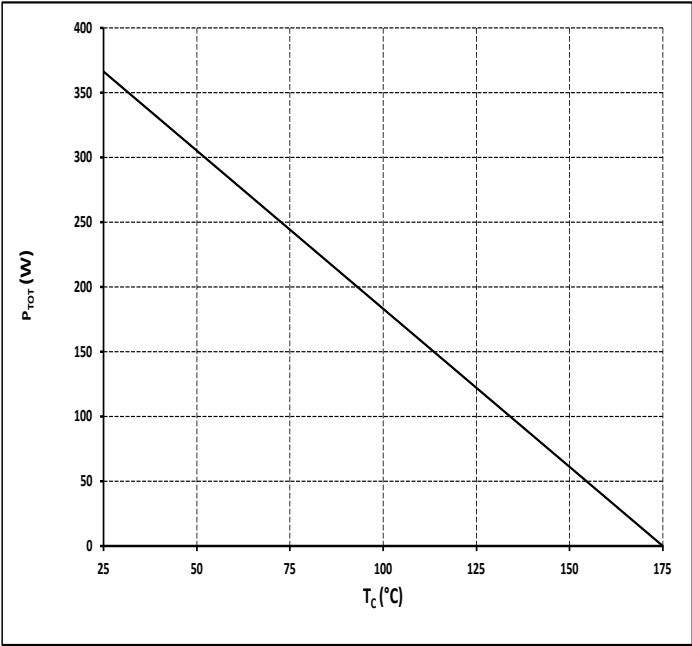


Figure 4
Power Derating

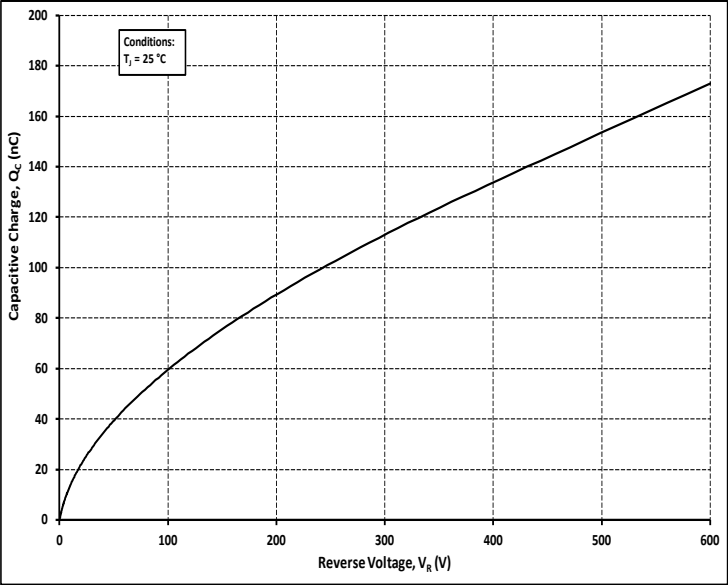


Figure 5
Total Capacitance vs. Reverse Voltage

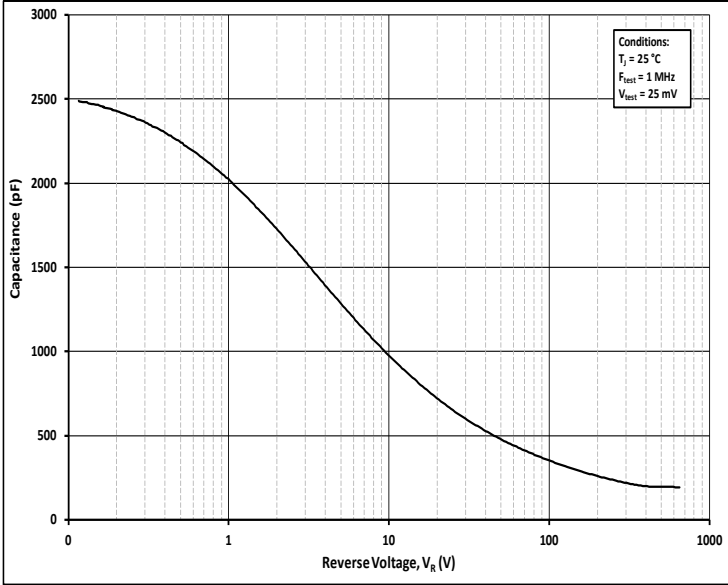


Figure 6
Capacitance vs. Reverse Voltage

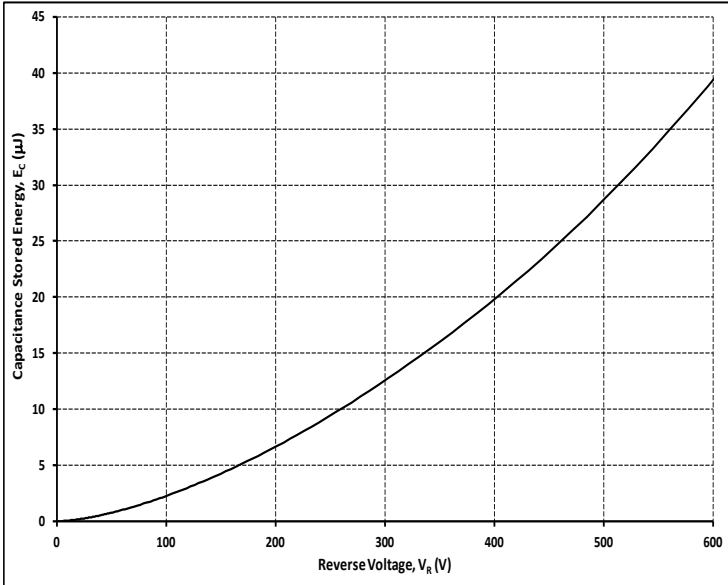


Figure 7
Capacitance Stored Energy

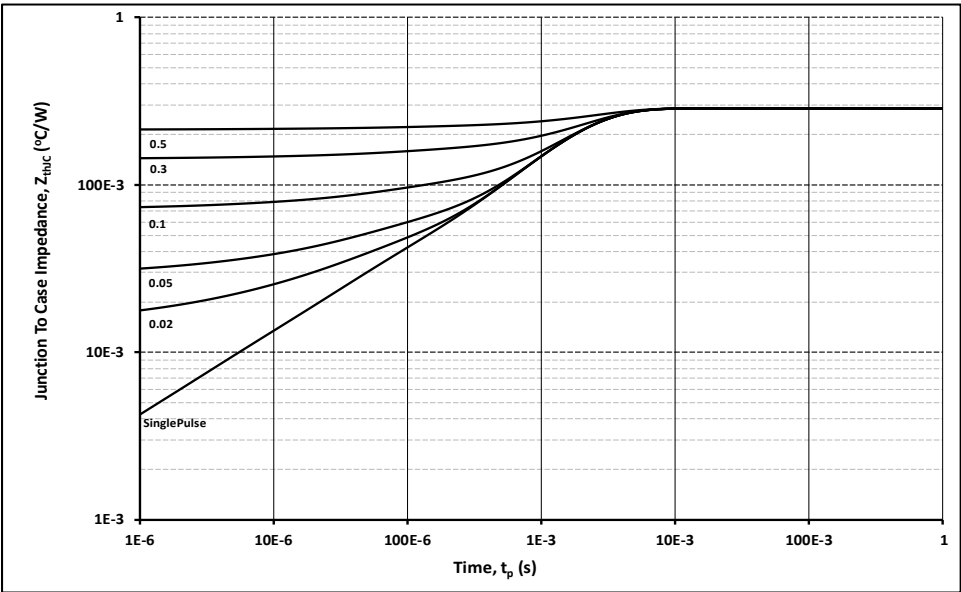
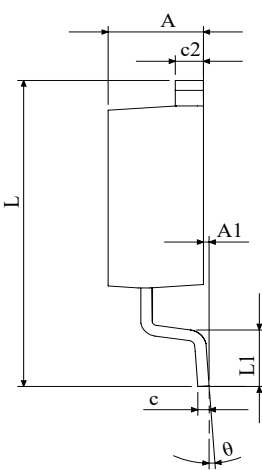
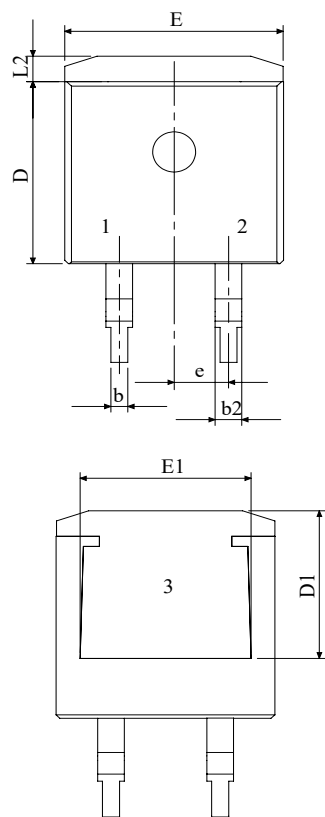


Figure 8
Transient Thermal Impedance

Package Dimensions & Pin-Out

Package: TO-263-2



1	CATHODE
2	ANODE
3	CATHODE

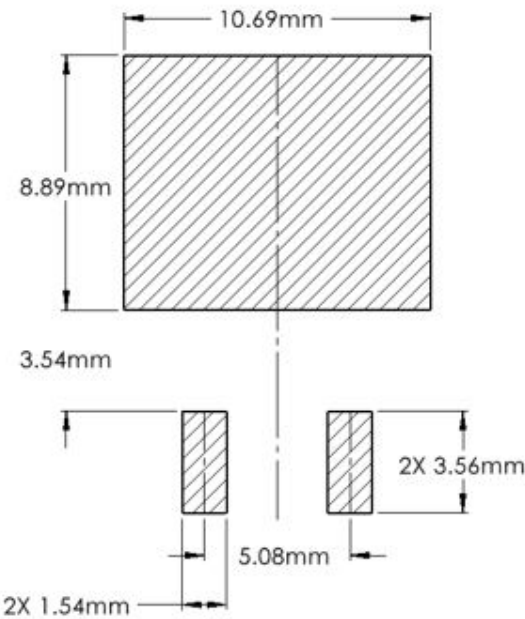
SYMBOL	MIN (mm)	MAX (mm)
A	4.32	4.57
A1	---	0.25
b	0.71	0.94
b2	1.15	1.40
c	0.356	0.635
c2	1.22	1.40
D	8.89	9.40
D1	6.48	6.88
E	10.04	10.28
E1	7.535	8.425
e	2.54	
L	14.73	15.75
L1	2.29	2.79
L2	1.15	1.39
θ	0°	8°

- NOTE
- 1. ALL METAL SURFACES ARE TIN PLATED (MATTE), EXCEPT AREA OF CUT.
 - 2. DIMENSIONING & TOLERANCING CONFORM TO ASME Y14.5M-1994.
 - 3. ALL DIMENSIONS ARE LISTED IN MILLIMETERS. ANGLES ARE IN DEGREES.
 - 4. PACKAGE BURR FLASH SIZE (0.5 mm) IS NOT INCLUDED IN THE DIMENSIONS



Recommended Solder Pad Layout

Primary dimensions shown in mm.



Product Ordering Information

Order Number	Packing Type
E6D40065G-TR	Tape & Reel
E6D40065G	Tube



Revision History

Document Version	Date of Release	Description of Changes
1	February 2024	Initial Release



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