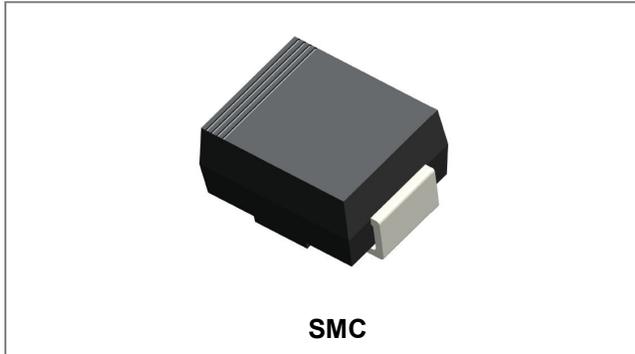


ER3A-ER3J SURFACE MOUNT SUPER FAST RECTIFIER



Features

- Glass Passivated Die Construction
- Ideally Suited for Automatic Assembly
- Low Forward Overload Drop, High Efficiency
- Low Power Loss
- Super-Fast Recovery Time
- Plastic Case Material has UL Flammability Classification Rating 94V-O
- This is a Pb – Free Device
- All SMC parts are traceable to the wafer lot
- Additional testing can be offered upon request

Circuit Diagram



Mechanical Data

- Case: Low Profile Molded Plastic
- Terminals: Solder Plated, Solderable per MIL-STD-750, Method 2026
- Polarity: Cathode Band or Cathode Notch
- Marking: Type Number
- Weight: 0.23grams(approx)

Maximum Ratings and Electrical Characteristics @ $T_A=25^\circ\text{C}$ unless otherwise specified

Characteristic	Symbol	ER3A	ER3B	ER3C	ER3D	ER3E	ER3G	ER3J	Units	
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V_{RRM} V_{RWM} V_R	50	100	150	200	300	400	600	V	
RMS Reverse Voltage	$V_{R(RMS)}$	35	70	105	140	210	280	420		
Average Rectified Output Current @ $T_L = 75^\circ\text{C}$	I_o	3.0							A	
Peak Forward Surge Current 8.3ms Single half sine-wave superimposed on rated load (JEDEC Method)	I_{FSM}	100							A	
Forward Voltage @ $I_F = 3.0\text{A}$, $T_J = 25^\circ\text{C}$	V_F	0.95				1.25		1.7	V	
Maximum DC reverse current at rated DC blocking voltage $T_A = 25^\circ\text{C}$ $T_A = 125^\circ\text{C}$	I_R	5.0				100				μA
Typical junction capacitance (Note 1)	C_J	45								pF
Maximum Reverse Recovery Time (Note 2)	T_{rr}	35								ns
Typical thermal resistance (Note 3)	$R_{\theta JL}$	16								$^\circ\text{C/W}$
Operating junction and storage temperature range	T_J, T_{STG}	-65 to +150							$^\circ\text{C}$	

Note: 1. Measured at 1.0 MHz and applied reverse voltage of 4.0 V_{DC}
 2. Measured with $I_F=0.5\text{A}$, $I_R=1.0\text{A}$, $I_{rr}=0.25\text{A}$,
 3. Mounted on P.C. Board with 8.0mm² lead area

Ratings and Characteristics Curves

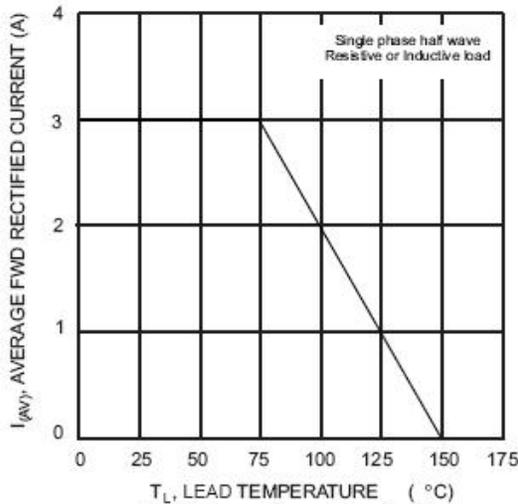


Fig. 1 Forward Current Derating Curve

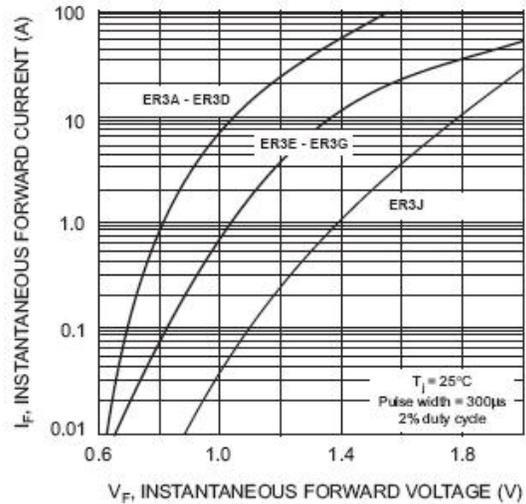


Fig. 2 Typical Forward Characteristics

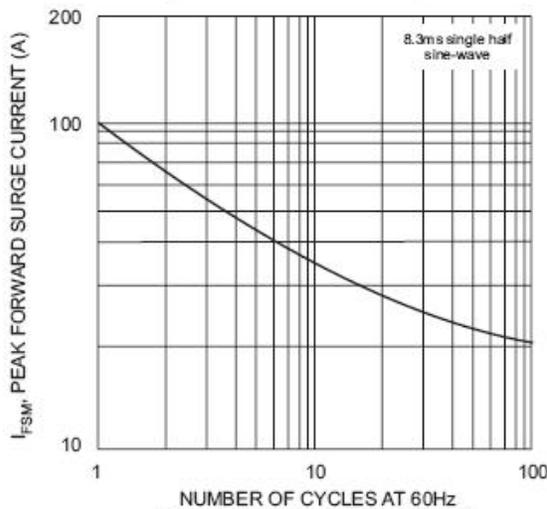


Fig. 3 Peak Forward Surge Current

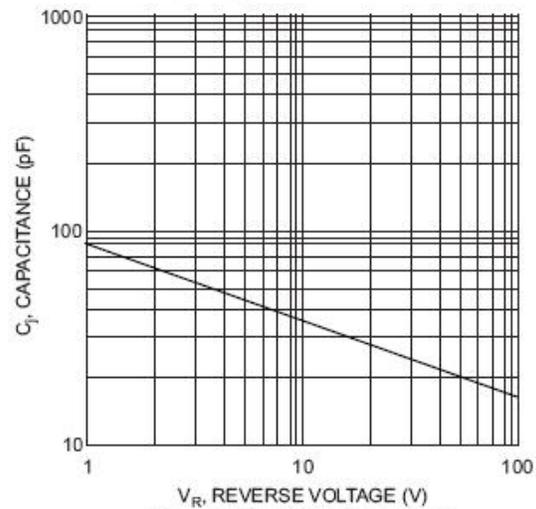
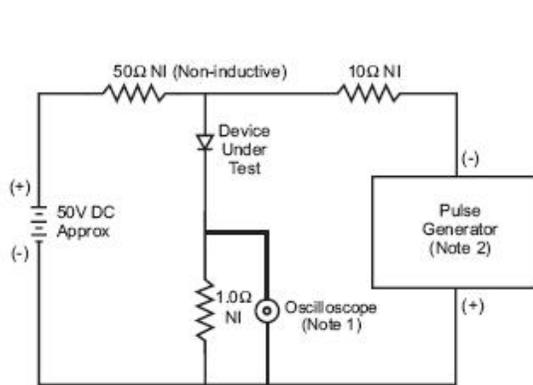


Fig. 4 Typical Junction Capacitance



Notes:
1. Rise Time = 7.0ns max. Input Impedance = 1.0MΩ, 22pF.
2. Rise Time = 10ns max. Input Impedance = 50Ω.

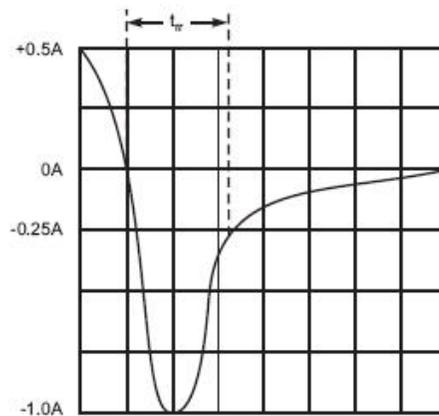
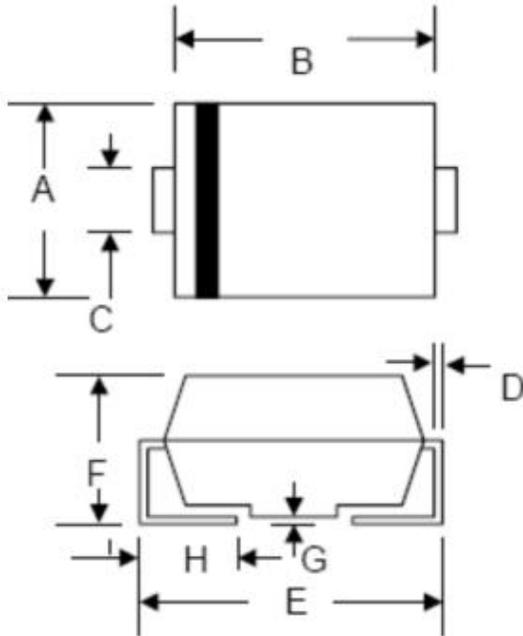


Fig. 5 Reverse Recovery Time Characteristic and Test Circuit

Mechanical Dimensions SMC



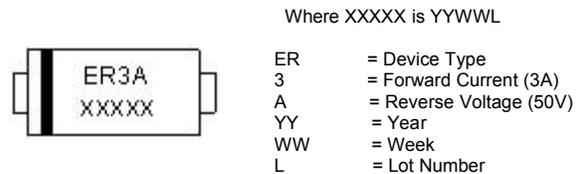
SYMBOL	Millimeters		Inches	
	Min.	Max.	Min.	Max.
A	5.59	6.22	0.220	0.245
B	6.60	7.11	0.260	0.280
C	2.75	3.25	0.108	0.128
D	0.152	0.305	0.006	0.012
E	7.75	8.25	0.305	0.325
F	2.00	2.95	0.079	0.116
G	0.051	0.203	0.002	0.008
H	0.76	1.60	0.030	0.063

Ordering Information

Device	Package	Shipping
ER3A-ER3J	SMC (Pb-Free)	3000pcs / reel

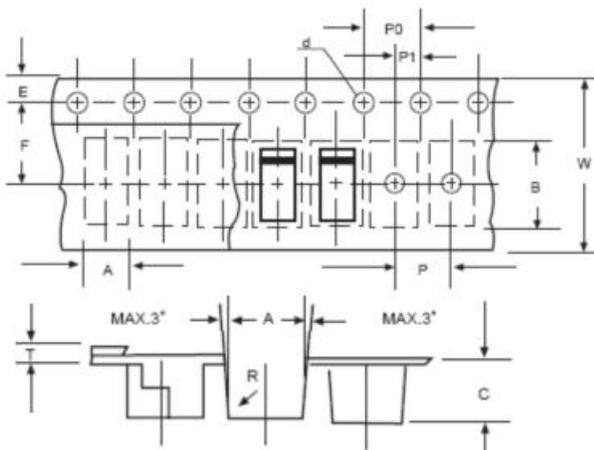
For information on tape and reel specifications, including part orientation and tape sizes, please refer to our tape and reel packaging specification.

Marking Diagram



Cautions: Molding resin
Epoxy resin UL:94V-0

Carrier Tape Specification SMC



SYMBOL	Millimeters	
	Min.	Max.
A	5.90	6.10
B	8.20	8.40
C	2.40	2.60
d	1.40	1.60
E	1.40	1.60
F	7.60	7.70
P	7.90	8.10
P0	3.90	4.10
P1	3.90	4.10
T	-	0.600
W	15.80	16.20

DISCLAIMER:

1- The information given herein, including the specifications and dimensions, is subject to change without prior notice to improve product characteristics. Before ordering, purchasers are advised to contact the SMC - Sangdest Microelectronics (Nanjing) Co., Ltd sales department for the latest version of the datasheet(s).

2- In cases where extremely high reliability is required (such as use in nuclear power control, aerospace and aviation, traffic equipment, medical equipment, and safety equipment), safety should be ensured by using semiconductor devices that feature assured safety or by means of users' fail-safe precautions or other arrangement.

3- In no event shall SMC - Sangdest Microelectronics (Nanjing) Co., Ltd be liable for any damages that may result from an accident or any other cause during operation of the user's units according to the datasheet(s). SMC - Sangdest Microelectronics (Nanjing) Co., Ltd assumes no responsibility for any intellectual property claims or any other problems that may result from applications of information, products or circuits described in the datasheets.

4- In no event shall SMC - Sangdest Microelectronics (Nanjing) Co., Ltd be liable for any failure in a semiconductor device or any secondary damage resulting from use at a value exceeding the absolute maximum rating.

5- No license is granted by the datasheet(s) under any patents or other rights of any third party or SMC - Sangdest Microelectronics (Nanjing) Co., Ltd.

6- The datasheet(s) may not be reproduced or duplicated, in any form, in whole or part, without the expressed written permission of SMC - Sangdest Microelectronics (Nanjing) Co., Ltd.

7- The products (technologies) described in the datasheet(s) are not to be provided to any party whose purpose in their application will hinder maintenance of international peace and safety nor are they to be applied to that purpose by their direct purchasers or any third party. When exporting these products (technologies), the necessary procedures are to be taken in accordance with related laws and regulations..