

MOSFET - N-Channel

Shielded Gate

POWERTRENCH®

80 V, 84 A, 6.7 mΩ

FDMS007N08LC

Description

This N-Channel MV MOSFET is produced using onsemi's advanced POWERTRENCH process that incorporates Shielded Gate technology. This process has been optimized to minimize on-state resistance and yet maintain superior switching performance with best in class soft body diode.

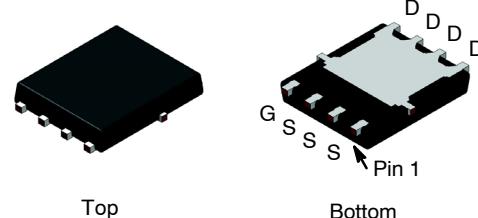
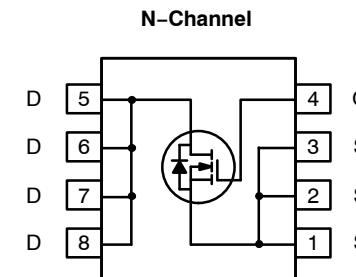
Features

- Shielded Gate MOSFET Technology
- Max $r_{DS(on)} = 6.7 \text{ mΩ}$ at $V_{GS} = 10 \text{ V}$, $I_D = 21 \text{ A}$
- Max $r_{DS(on)} = 9.9 \text{ mΩ}$ at $V_{GS} = 4.5 \text{ V}$, $I_D = 17 \text{ A}$
- 50% Lower Q_{rr} than Other MOSFET Suppliers
- Lowers Switching Noise/EMI
- MSL1 Robust Package Design
- 100% UIL Tested
- These Devices are Pb-Free, Halogen Free/BFR Free and are RoHS Compliant

Typical Applications

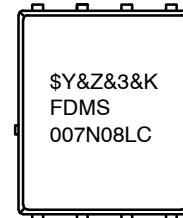
- Primary DC-DC MOSFET
- Synchronous Rectifier in DC-DC and AC-DC
- Motor Drive
- Solar

| V_{DS} | $r_{DS(on)} \text{ MAX}$ | $I_D \text{ MAX}$ |
|----------|--------------------------|-------------------|
| 80 V | 6.7 mΩ @ 10 V | 84 A |



PQFN8 5x6, 1.27P
(Power 56)
CASE 483AE

MARKING DIAGRAM



| | |
|--------------|------------------------|
| \$Y | = onsemi Logo |
| &Z | = Assembly Plant Code |
| &3 | = Numeric Date Code |
| &K | = Lot Code |
| FDMS007N08LC | = Specific Device Code |

ORDERING INFORMATION

| Device | Package | Shipping [†] |
|--------------|------------------|-----------------------|
| FDMS007N08LC | PQFN-8 (Pb-Free) | 000 / Tape & Reel |

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specifications Brochure, [BRD8011/D](#).

FDMS007N08LC

MOSFET MAXIMUM RATINGS (T_A = 25°C, Unless otherwise specified)

| Symbol | Parameter | Ratings | Unit |
|-----------------------------------|---|-------------|------|
| V _{DS} | Drain to Source Voltage | 80 | V |
| V _{GS} | Gate to Source Voltage | ±20 | V |
| I _D | Drain Current – Continuous T _C = 25°C (Note 5) | 84 | A |
| | – Continuous T _C = 100°C (Note 5) | 53 | |
| | – Continuous T _A = 25°C (Note 1a) | 14 | |
| | – Pulsed (Note 4) | 345 | |
| E _{AS} | Single Pulse Avalanche Energy (Note 3) | 181.5 | mJ |
| P _D | Power Dissipation T _C = 25°C | 92.6 | W |
| | Power Dissipation T _A = 25°C (Note 1a) | 2.5 | |
| T _J , T _{STG} | Operating and Storage Junction Temperature Range | –55 to +150 | °C |

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

| Symbol | Parameter | Ratings | Unit |
|------------------|---|---------|------|
| R _{θJC} | Thermal Resistance, Junction to Case | 1.35 | °C/W |
| R _{θJA} | Thermal Resistance, Junction to Ambient (Note 1a) | 50 | |

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise noted)

| Symbol | Parameter | Test Conditions | Min | Typ | Max | Unit |
|--------|-----------|-----------------|-----|-----|-----|------|
|--------|-----------|-----------------|-----|-----|-----|------|

OFF CHARACTERISTICS

| | | | | | | |
|--------------------------------------|---|--|----|----|------|-------|
| BV _{DSS} | Drain to Source Breakdown Voltage | I _D = 250 μA, V _{GS} = 0 V | 80 | – | – | V |
| ΔBV _{DSS} / ΔT _J | Breakdown Voltage Temperature Coefficient | I _D = 250 μA, referenced to 25°C | – | 32 | – | mV/°C |
| I _{DSS} | Zero Gate Voltage Drain Current | V _{DS} = 64 V, V _{GS} = 0 V | – | – | 1 | μA |
| I _{GSS} | Gate to Source Leakage Current | V _{GS} = ±20 V, V _{DS} = 0 V | – | – | ±100 | μA |

ON CHARACTERISTICS

| | | | | | | |
|--|--|---|-----|------|------|-------|
| V _{GS(th)} | Gate to Source Threshold Voltage | V _{GS} = V _{DS} , I _D = 120 μA | 1.0 | 1.4 | 2.5 | V |
| ΔV _{GS(th)} / ΔT _J | Gate to Source Threshold Voltage Temperature Coefficient | I _D = 120 μA, referenced to 25°C | – | –5.6 | – | mV/°C |
| r _{DS(on)} | Static Drain to Source On Resistance | V _{GS} = 10 V, I _D = 21 A | – | 4.9 | 6.7 | mΩ |
| | | V _{GS} = 4.5 V, I _D = 17 A | – | 6.7 | 9.9 | |
| | | V _{GS} = 10 V, I _D = 21 A, T _J = 125°C | – | 8.5 | 11.6 | |
| g _{FS} | Forward Transconductance | V _{DD} = 5 V, I _D = 21 A | – | 84 | – | S |

DYNAMIC CHARACTERISTICS

| | | | | | | |
|------------------|------------------------------|--|-----|------|------|----|
| C _{iss} | Input Capacitance | V _{DS} = 40 V, V _{GS} = 0 V, f = 1 MHz | – | 2227 | 3100 | pF |
| C _{oss} | Output Capacitance | | – | 520 | 760 | pF |
| C _{rss} | Reverse Transfer Capacitance | | – | 27 | 40 | pF |
| R _G | Gate Resistance | | 0.1 | 0.4 | 0.8 | Ω |

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ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$ unless otherwise noted)

SWITCHING CHARACTERISTICS

| | | | | | | |
|--------------|-------------------------------|---|---|----|----|----|
| $t_{d(on)}$ | Turn-on Delay Time | $V_{DD} = 40 \text{ V}$, $I_D = 21 \text{ A}$, $V_{GS} = 10 \text{ V}$, $R_{GEN} = 6 \Omega$ | - | 10 | 21 | ns |
| t_r | Rise Time | | - | 3 | 10 | |
| $t_{d(off)}$ | Turn-off Delay Time | | - | 38 | 61 | |
| t_f | Fall Time | | - | 8 | 16 | |
| Q_g | Total Gate Charge | $V_{GS} = 0 \text{ V}$ to 10 V , $V_{DD} = 40 \text{ V}$, $I_D = 21 \text{ A}$ | - | 33 | 46 | nC |
| Q_g | Total Gate Charge | $V_{GS} = 0 \text{ V}$ to 4.5 V , $V_{DD} = 40 \text{ V}$, $I_D = 21 \text{ A}$ | - | 16 | 22 | nC |
| Q_{gs} | Gate to Source Charge | $V_{DD} = 40 \text{ V}$, $I_D = 21 \text{ A}$ | - | 5 | - | nC |
| Q_{gd} | Gate to Drain "Miller" Charge | $V_{DD} = 40 \text{ V}$, $I_D = 21 \text{ A}$ | - | 4 | - | nC |
| Q_{oss} | Output Charge | $V_{DD} = 40 \text{ V}$, $V_{GS} = 0 \text{ V}$ | - | 30 | - | nC |
| Q_{sync} | Total Gate Charge Sync | $V_{DS} = 0 \text{ V}$, $I_D = 21 \text{ A}$ | - | 35 | - | nC |

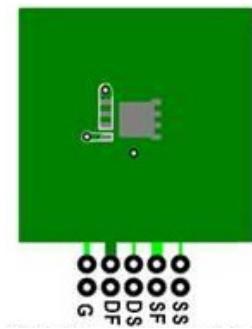
DRAIN-SOURCE DIODE CHARACTERISTICS

| | | | | | | |
|----------|---------------------------------------|---|---|-----|-----|----|
| V_{SD} | Source to Drain Diode Forward Voltage | $V_{GS} = 0 \text{ V}$, $I_S = 2.1 \text{ A}$ (Note 2) | - | 0.7 | 1.2 | V |
| | | $V_{GS} = 0 \text{ V}$, $I_S = 21 \text{ A}$ (Note 2) | - | 0.8 | 1.3 | V |
| t_{rr} | Reverse Recovery Time | $I_F = 10 \text{ A}$, $di/dt = 300 \text{ A}/\mu\text{s}$ | - | 18 | 32 | ns |
| | Reverse Recovery Charge | | - | 24 | 28 | nC |
| t_{rr} | Reverse Recovery Time | $I_F = 10 \text{ A}$, $di/dt = 1000 \text{ A}/\mu\text{s}$ | - | 13 | 23 | ns |
| | Reverse Recovery Charge | | - | 58 | 92 | nC |

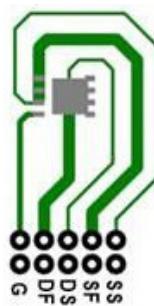
Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

NOTES:

1. $R_{\theta JA}$ is determined with the device mounted on a 1 in² pad 2 oz copper pad on a 1.5 × 1.5 in. board of FR-4 material. $R_{\theta CA}$ is determined by the user's board design.



a) 50°C/W when mounted on a 1 in² pad of 2 oz copper.



b) 125°C/W when mounted on a minimum pad of 2 oz copper.

2. Pulse Test: Pulse Width < 300 μs, Duty cycle < 2.0%.
3. E_{AS} of 181 mJ is based on starting $T_J = 25^\circ\text{C}$; $L = 3 \text{ mH}$, $I_{AS} = 11 \text{ A}$, $V_{DD} = 80 \text{ V}$, $V_{GS} = 10 \text{ V}$. 100% tested at $L = 0.1 \text{ mH}$, $I_{AS} = 35 \text{ A}$.
4. Pulsed I_D please refer to Fig. 11 SOA graph for more details.
5. Computed continuous current limited to Max Junction Temperature only, actual continuous current will be limited by thermal & electro-mechanical application board design.

FDMS007N08LC

TYPICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$ unless otherwise noted)

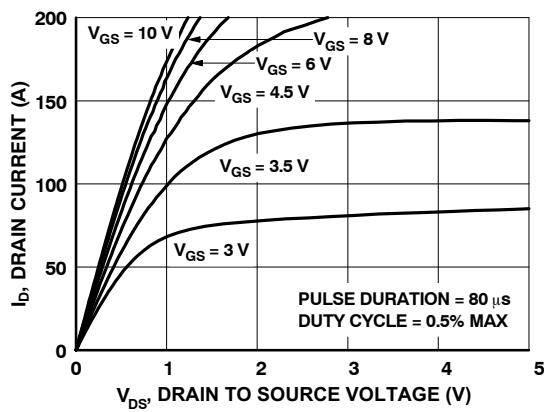


Figure 1. On Region Characteristics

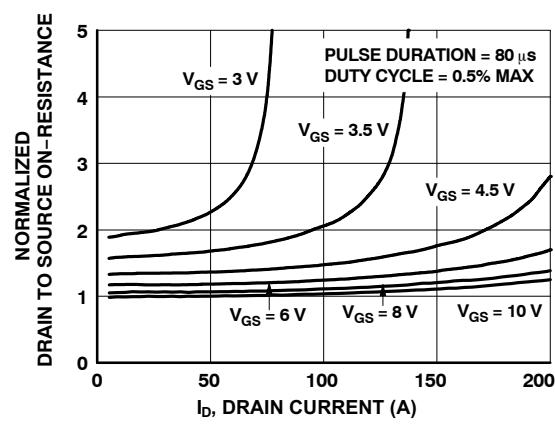


Figure 2. Normalized On-Resistance vs. Drain Current and Gate Voltage

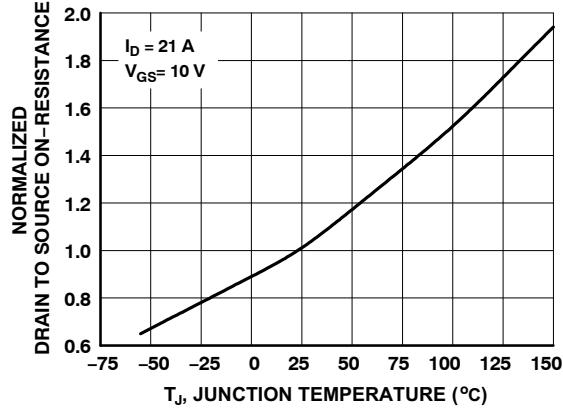


Figure 3. Normalized On Resistance vs. Junction Temperature

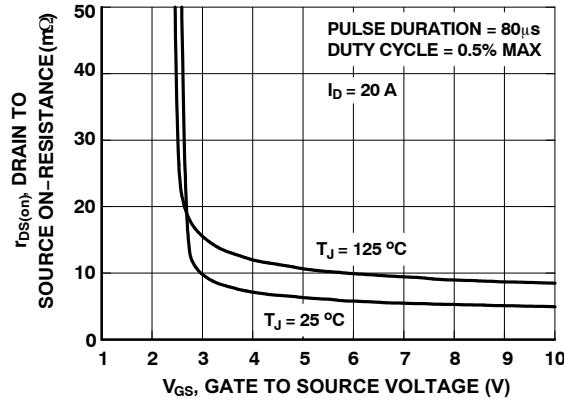


Figure 4. On-Resistance vs. Gate to Source Voltage

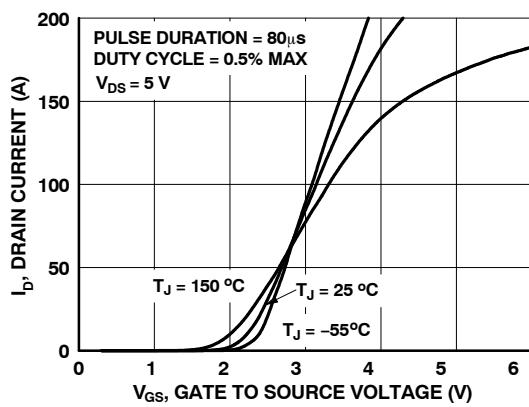


Figure 5. Transfer Characteristics

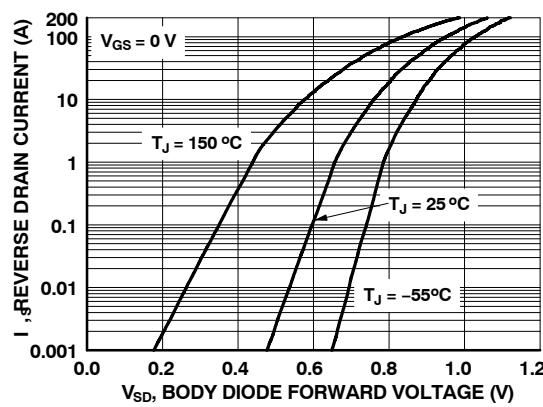


Figure 6. Source to Drain Diode Forward Voltage vs. Source Current

TYPICAL CHARACTERISTICS (continued)

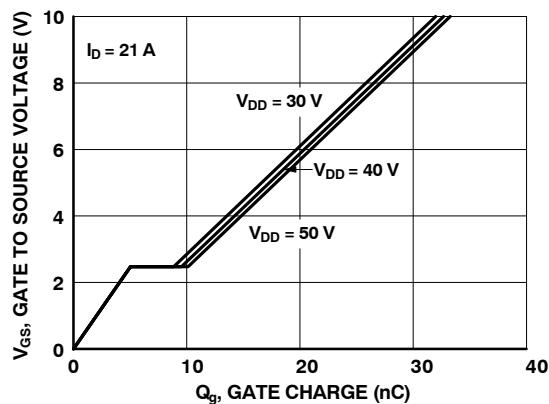


Figure 7. Gate Charge Characteristics

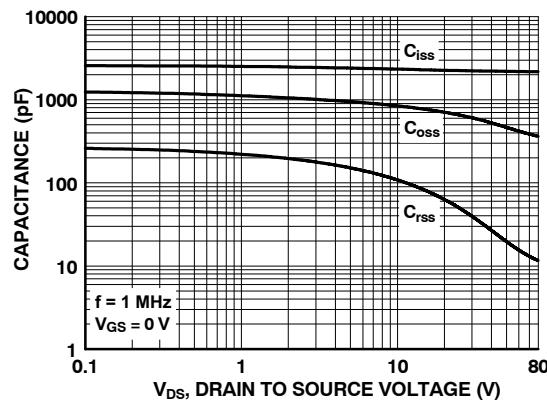


Figure 8. Capacitance vs. Drain to Source Voltage

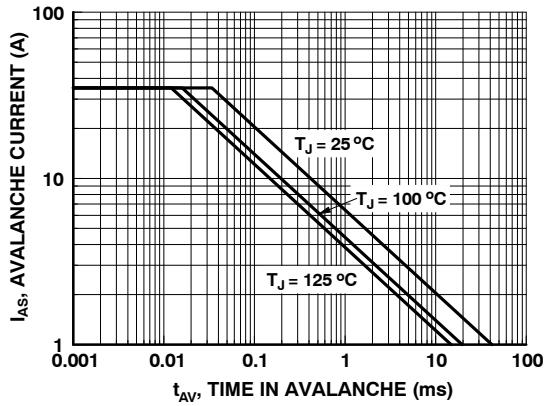


Figure 9. Unclamped Inductive Switching Capability

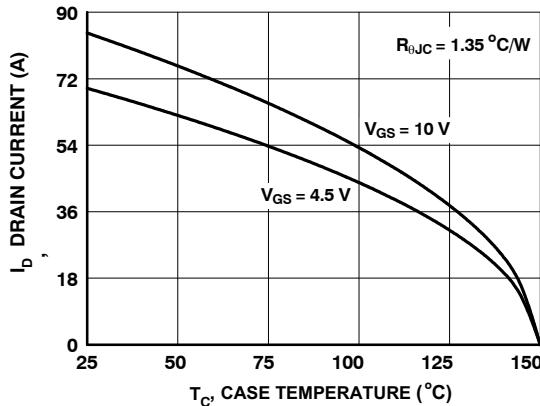


Figure 10. Maximum Continuous Drain Current vs. Case Temperature

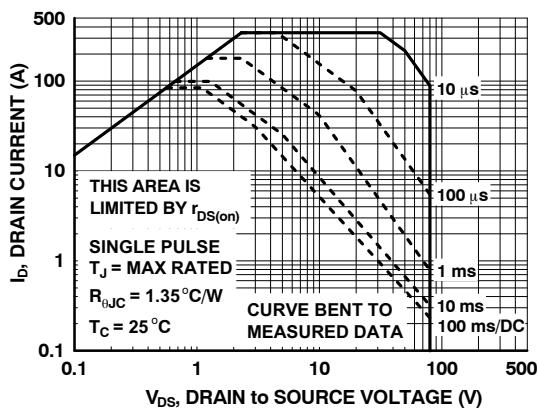


Figure 11. Forward Bias Safe Operating Area

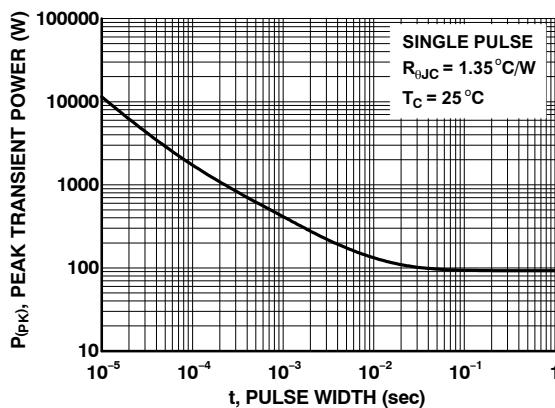


Figure 12. Single Pulse Maximum Power Dissipation

TYPICAL CHARACTERISTICS (continued)

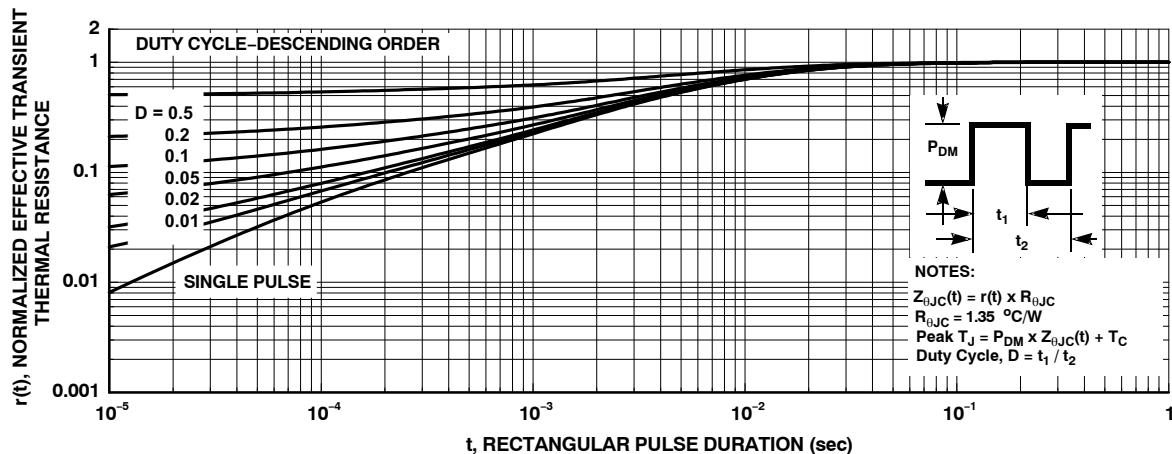
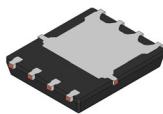
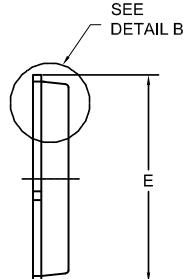
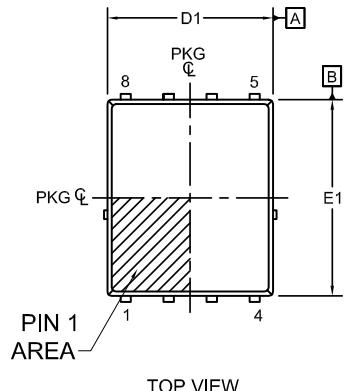


Figure 13. Junction-to-Case Transient Thermal Response Curve

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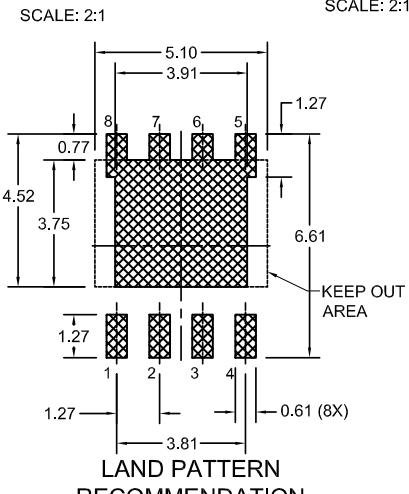
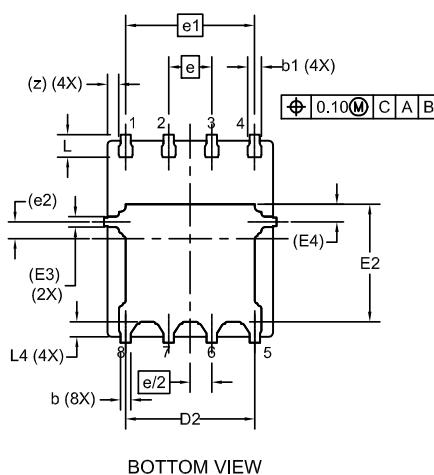
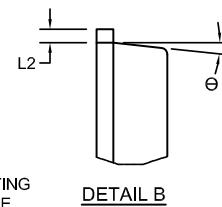
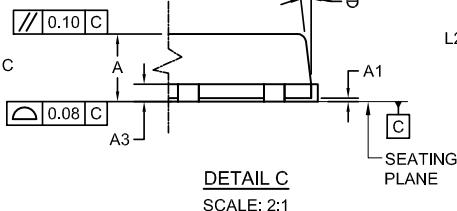
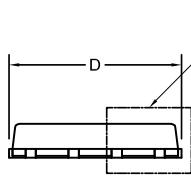

PQFN8 5X6, 1.27P
CASE 483AE
ISSUE C

DATE 21 JAN 2022



NOTES:

1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 2009.
2. CONTROLLING DIMENSION: MILLIMETERS
3. COPLANARITY APPLIES TO THE EXPOSED PADS AS WELL AS THE TERMINALS.
4. DIMENSIONS D1 AND E1 DO NOT INCLUDE MOLD FLASH, PROTRUSIONS, OR GATE BURRS.
5. SEATING PLANE IS DEFINED BY THE TERMINALS. "A1" IS DEFINED AS THE DISTANCE FROM THE SEATING PLANE TO THE LOWEST POINT ON THE PACKAGE BODY.
6. IT IS RECOMMENDED TO HAVE NO TRACES OR VIAS WITHIN THE KEEP OUT AREA.



**LAND PATTERN
RECOMMENDATION**

*FOR ADDITIONAL INFORMATION ON OUR PB-FREE STRATEGY AND SOLDERING DETAILS, PLEASE DOWNLOAD THE ON SEMICONDUCTOR SOLDERING AND MOUNTING TECHNIQUES REFERENCE MANUAL, SOLDERRM/D.

| DIM | MILLIMETERS | | |
|-----|-------------|------|------|
| | MIN. | NOM. | MAX. |
| A | 0.90 | 1.00 | 1.10 |
| A1 | 0.00 | - | 0.05 |
| b | 0.21 | 0.31 | 0.41 |
| b1 | 0.31 | 0.41 | 0.51 |
| A3 | 0.15 | 0.25 | 0.35 |
| D | 4.90 | 5.00 | 5.20 |
| D1 | 4.80 | 4.90 | 5.00 |
| D2 | 3.61 | 3.82 | 3.96 |
| E | 5.90 | 6.15 | 6.25 |
| E1 | 5.70 | 5.80 | 5.90 |
| E2 | 3.38 | 3.48 | 3.78 |
| E3 | 0.30 | REF | |
| E4 | 0.52 | REF | |
| e | 1.27 | BSC | |
| e/2 | 0.635 | BSC | |
| e1 | 3.81 | BSC | |
| e2 | 0.50 | REF | |
| L | 0.51 | 0.66 | 0.76 |
| L2 | 0.05 | 0.18 | 0.30 |
| L4 | 0.34 | 0.44 | 0.54 |
| z | 0.34 | REF | |
| Θ | 0° | - | 12° |

| | | |
|------------------|------------------|---|
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