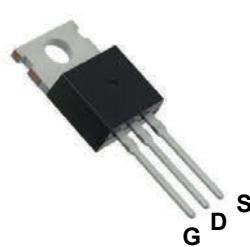
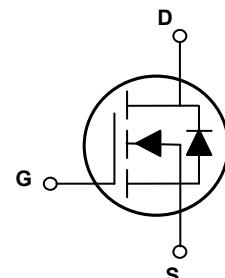


Main Product Characteristics

$V_{(BR)DSS}$	600V
$R_{DS(ON)}$	0.16Ω (max.)
I_D	24A



TO-220



Schematic Diagram

Features and Benefits

- Advanced MOSFET process technology
- Ideal for high efficiency switched mode power supplies
- Low on-resistance with low gate charge
- Fast switching and reverse body recovery



Description

The GSFH60R160 utilizes the latest techniques to achieve high cell density and low on-resistance. These features make this device extremely efficient and reliable for use in high efficiency switch mode power supplies and a wide variety of other applications.

Absolute Maximum Ratings ($T_C=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Parameter.	Unit
Drain-Source Voltage	V_{DS}	600	V
Gate-to-Source Voltage	V_{GS}	± 30	V
Continuous Drain Current, @ Steady-State ($T_C=25^\circ\text{C}$)	I_D	24	A
Continuous Drain Current, @ Steady-State ($T_C=100^\circ\text{C}$)		15	A
Pulsed Drain Current	I_{DM}	96	A
Power Dissipation ($T_C=25^\circ\text{C}$)	P_D	180	W
		1.44	W/ $^\circ\text{C}$
Single Pulse Avalanche Energy ¹	E_{AS}	1060	mJ
Body Diode Reverse Voltage Slope ²	dv/dt	15	V/ns
MOS dv/dt Ruggedness ³	dv/dt	50	V/ns
Junction-to-Ambient (PCB Mounted, Steady-State)	$R_{\theta JA}$	62.5	$^\circ\text{C}/\text{W}$
Junction-to-Case	$R_{\theta JC}$	0.69	$^\circ\text{C}/\text{W}$
Operating Junction and Storage Temperature Range	T_J/T_{STG}	-55 to +150	$^\circ\text{C}$

Electrical Characteristics ($T_C=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
On / Off Characteristics						
Drain-to-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_D=250\mu\text{A}$	600	-	-	V
Drain-to-Source Leakage Current	I_{DSS}	$V_{\text{DS}}=600\text{V}, V_{\text{GS}}=0\text{V}$	-	-	1	μA
Gate-to-Source Forward Leakage	I_{GSS}	$V_{\text{DS}}=0\text{V}, V_{\text{GS}}=30\text{V}$	-	-	100	nA
		$V_{\text{DS}}=0\text{V}, V_{\text{GS}}=-30\text{V}$	-	-	-100	
Static Drain-to-Source On-Resistance	$R_{\text{DS}(\text{ON})}$	$V_{\text{GS}}=10\text{V}, I_D=12\text{A}$	-	0.14	0.16	Ω
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_D=250\mu\text{A}$	2.0	-	4.0	V
Dynamic and Switching Characteristics						
Input Capacitance	C_{iss}	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=100\text{V}, F=1\text{MHz}$	-	1482	-	pF
Output Capacitance	C_{oss}		-	84	-	
Reverse Transfer Capacitance	C_{rss}		-	4.8	-	
Total Gate Charge ^{4,5}	Q_g	$I_D=24\text{A}, V_{\text{DD}}=480\text{V}, V_{\text{GS}}=10\text{V}$	-	50	-	nC
Gate-to-Source Charge ^{4,5}	Q_{gs}		-	13	-	
Gate-to-Drain ("Miller") Charge ^{4,5}	Q_{gd}		-	26	-	
Turn-on Delay Time ^{4,5}	$t_{\text{d}(\text{on})}$	$V_{\text{DD}}=300\text{V}, V_{\text{GS}}=10\text{V}, R_G=25\Omega, I_D=24\text{A}$	-	21	-	nS
Rise Time ^{4,5}	t_r		-	74	-	
Turn-Off Delay Time ^{4,5}	$t_{\text{d}(\text{off})}$		-	211	-	
Fall Time ^{4,5}	t_f		-	64	-	
Gate Resistance	R_g	$F=1\text{MHz}$	-	2.6	-	Ω
Source-Drain Ratings and Characteristics						
Continuous Source Current (Body Diode)	I_s	$T_C=25^\circ\text{C}$, MOSFET symbol showing the integral reverse p-n junction diode.	-	-	24	A
Source Pulse Current	I_{SM}	$I_s=24\text{A}, V_{\text{GS}}=0\text{V}$ $I_F=24\text{A}, V_{\text{DD}}=50\text{V}, dI_F/dt=100\text{A/us}$	-	-	96	A
Diode Forward Voltage	V_{SD}		-	1.1	1.4	V
Reverse Recovery Time ²	T_{rr}		-	442	-	nS
Reverse Recovery Charge ²	Q_{rr}		-	7.0	-	μC

Note:

1. $L=79\text{mH}, I_{AS}=4.8\text{A}, V_{DD}=100\text{V}, R_g=25\Omega$, starting temperature $T_J=25^\circ\text{C}$.
2. $V_{\text{DS}}=0-400\text{V}, I_{SD} \leq I_s, T_J=25^\circ\text{C}$.
3. $V_{\text{DS}}=0-480\text{V}$.
4. Pulse Test : pulse width $\leq 300\mu\text{s}$, duty cycle $\leq 2\%$.
5. Essentially independent of operating temperature.

Typical Electrical and Thermal Characteristic Curves

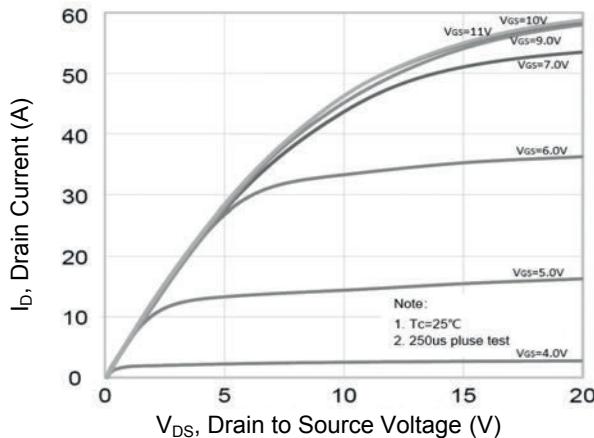


Figure 1. Typical Output Characteristics

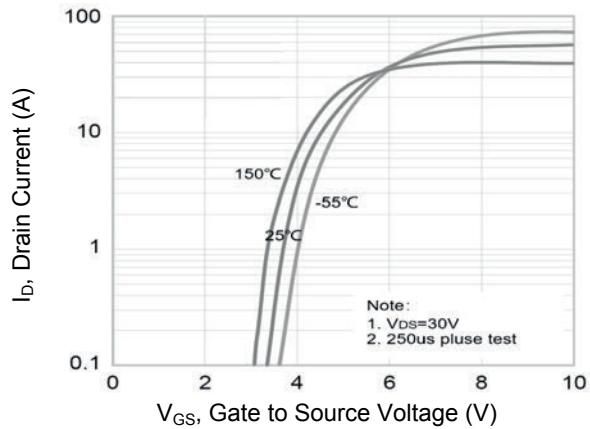


Figure 2. Transfer Characteristics

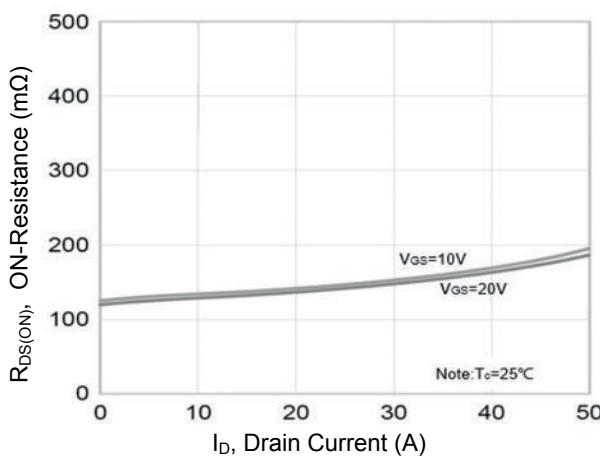


Figure 3. On-Resistance vs. Drain Current

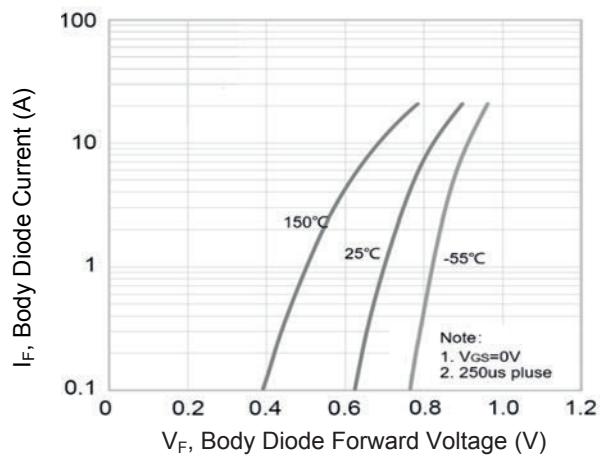


Figure 4. Body Diode Characteristics

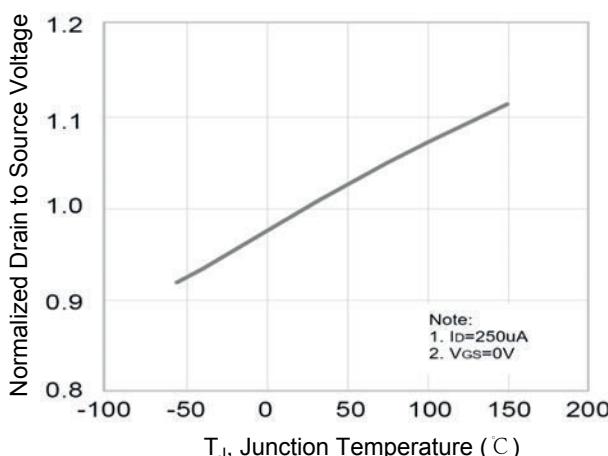


Figure 5. Normalized BV_{DSS} Vs. T_J

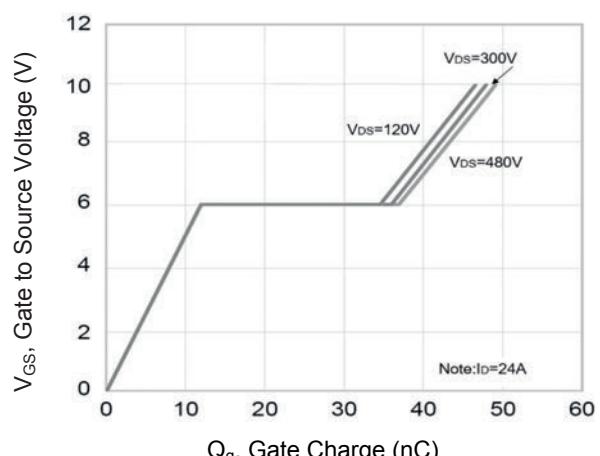


Figure 6. Gate Charge

Typical Electrical and Thermal Characteristic Curves

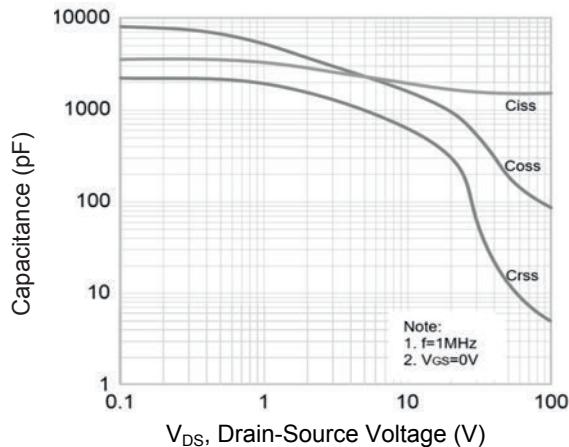


Figure 7. Capacitance Characteristics

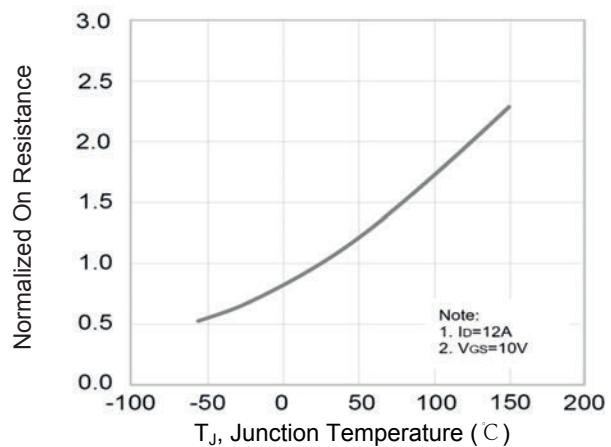


Figure 8. Normalized On-Resistance Vs. T_J

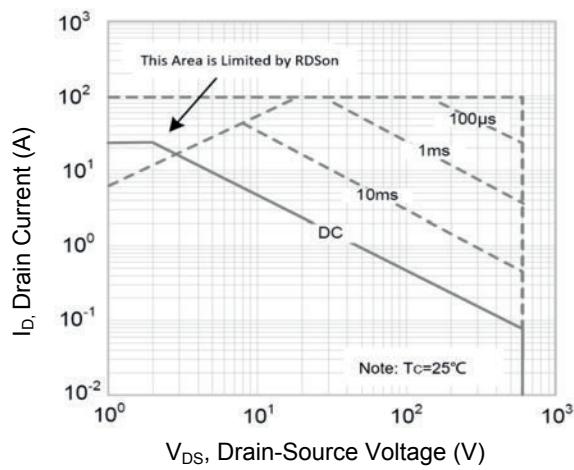
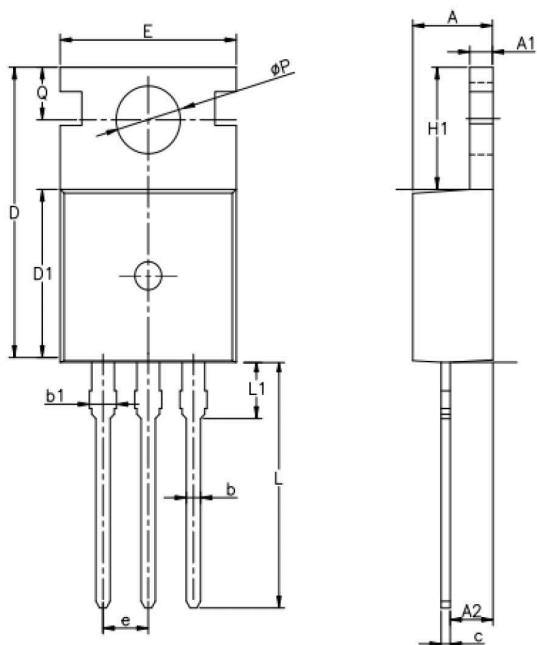


Figure 9. Safe Operation Area

Package Outline Dimensions (TO-220)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	4.300	4.700	0.169	0.185
A1	1.000	1.500	0.039	0.059
A2	1.800	2.800	0.071	0.110
b	0.600	1.000	0.024	0.039
b1	1.000	1.600	0.039	0.063
c	0.300	0.700	0.012	0.028
D	15.100	16.100	0.594	0.634
D1	8.100	10.000	0.319	0.394
E	9.600	10.400	0.378	0.409
e	2.540 BSC		0.100 BSC	
H1	6.100	7.000	0.240	0.276
L	12.600	13.600	0.496	0.535
L1	-	3.950	-	0.156
ΦP	3.400	3.900	0.134	0.154
Q	2.600	3.200	0.102	0.126