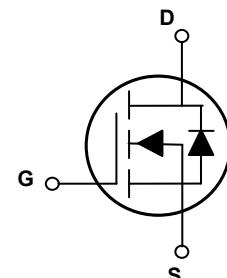
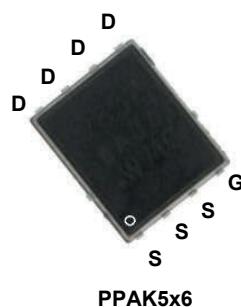


### Main Product Characteristics

$V_{(BR)DSS}$	100V
$R_{DS(ON)}$	13mΩ (Max)
$I_D$	50A



Schematic Diagram

### Features and Benefits

- Advanced MOSFET process technology
- Ideal for high efficiency switched mode power supplies
- Low on-resistance with low gate charge
- Fast switching and reverse body recovery



### Description

The GSFP13010 utilizes the latest techniques to achieve high cell density and low on-resistance. These features make this device extremely efficient and reliable for use in high efficiency switch mode power supplies and a wide variety of other applications.

### Absolute Maximum Ratings ( $T_C=25^\circ\text{C}$ unless otherwise specified)

Parameter	Symbol	Max.	Unit
Drain-Source Voltage	$V_{DS}$	100	V
Gate-to-Source Voltage	$V_{GS}$	$\pm 20$	V
Continuous Drain Current, @ Steady-State ( $T_C=25^\circ\text{C}$ ) <sup>1</sup>	$I_D$	50	A
Continuous Drain Current, @ Steady-State ( $T_C=100^\circ\text{C}$ )		35	A
Pulsed Drain Current <sup>2</sup>	$I_{DM}$	200	A
Power Dissipation ( $T_C=25^\circ\text{C}$ )	$P_D$	96	W
Linear Derating Factor ( $T_C=25^\circ\text{C}$ )		0.77	W/ $^\circ\text{C}$
Single Pulse Avalanche Energy <sup>3</sup>	$E_{AS}$	31	mJ
Junction-to-Case	$R_{\theta JC}$	1.31	$^\circ\text{C}/\text{W}$
Junction-to-Ambient (PCB Mounted, Steady-State) <sup>4</sup>	$R_{\theta JA}$	62.0	$^\circ\text{C}/\text{W}$
Operating Junction and Storage Temperature Range	$T_J/T_{STG}$	-55 to +150	$^\circ\text{C}$

**Electrical Characteristics** ( $T_C=25^\circ\text{C}$  unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
<b>On / Off Characteristics</b>						
Drain-to-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}}=0\text{V}, I_D=250\mu\text{A}$	100	-	-	V
Drain-to-Source Leakage Current	$I_{\text{DSS}}$	$V_{\text{DS}}=100\text{V}, V_{\text{GS}}=0\text{V}$	-	-	1	$\mu\text{A}$
		$T_J=125^\circ\text{C}$	-	-	20	
Gate-to-Source Forward Leakage	$I_{\text{GSS}}$	$V_{\text{GS}}=\pm 20\text{V}$	-	-	$\pm 100$	nA
Static Drain-to-Source On-Resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}}=10\text{V}, I_D=20\text{A}$	-	11	13	$\text{m}\Omega$
		$V_{\text{GS}}=4.5\text{V}, I_D=10\text{A}$	-	15	20	
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}}=V_{\text{GS}}, I_D=250\mu\text{A}$	1.1	-	2.6	V
<b>Dynamic and Switching Characteristics</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{GS}}=0\text{V}, V_{\text{DS}}=50\text{V}$ $F=1\text{MHz}$	-	1716	-	$\text{pF}$
Output Capacitance	$C_{\text{oss}}$		-	171	-	
Reverse Transfer Capacitance	$C_{\text{rss}}$		-	4	-	
Total Gate Charge	$Q_g$	$I_D=20\text{A}, V_{\text{DS}}=50\text{V}, V_{\text{GS}}=10\text{V}$	-	29	-	$\text{nC}$
Gate-to-Source Charge	$Q_{\text{gs}}$		-	7.1	-	
Gate-to-Drain ("Miller") Charge	$Q_{\text{gd}}$		-	7.0	-	
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{GS}}=10\text{V}, V_{\text{DS}}=50\text{V}, I_D=20\text{A}, R_{\text{GEN}}=3\Omega$	-	7.6	-	$\text{nS}$
Rise Time	$t_r$		-	28	-	
Turn-Off Delay Time	$t_{\text{d}(\text{off})}$		-	32	-	
Fall Time	$t_f$		-	11	-	
Gate Resistance	$R_g$	$F=1\text{MHz}$	-	2.4	-	$\Omega$
<b>Source-Drain Ratings and Characteristics</b>						
Continuous Source Current (Body Diode)	$I_s$	MOSFET symbol showing the integral reverse p-n junction diode.	-	-	50	A
Pulsed Source Current (Body Diode)	$I_{\text{SM}}$		-	-	200	A
Diode Forward Voltage	$V_{\text{SD}}$	$I_s=20\text{A}, V_{\text{GS}}=0\text{V}$	-	1.0	1.3	V
Reverse Recovery Time	$t_{\text{rr}}$	$T_J=25^\circ\text{C}, I_F=20\text{A}, \frac{di}{dt}=100\text{A}/\mu\text{s}$	-	42	-	$\text{nS}$
Reverse Recovery Charge	$Q_{\text{rr}}$		-	50	-	

**Notes**

1. Pulse test: Pulse width  $\leq 300\mu\text{s}$ , duty cycle  $\leq 2\%$ .
2. Repetitive rating; pulse width limited by max. junction temperature.
3.  $L=0.1\text{mH}, V_{\text{DD}}=80\text{V}, I_{\text{AS}}=25\text{A}, R_g=25\Omega, T_J=25^\circ\text{C}$ .
4. Device mounted on FR-4 PCB, 1inch x 0.85inch x 0.062inch.

## Typical Electrical and Thermal Characteristic Curves

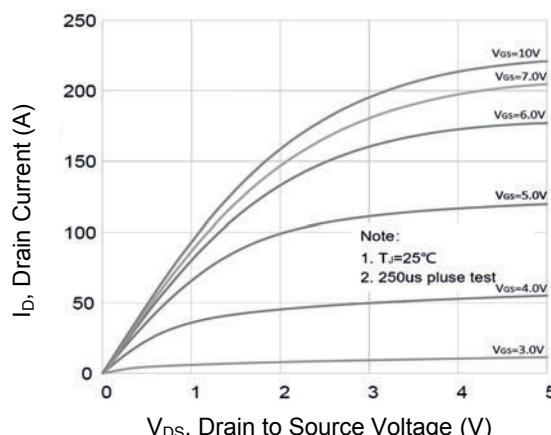


Figure 1. Output Characteristics

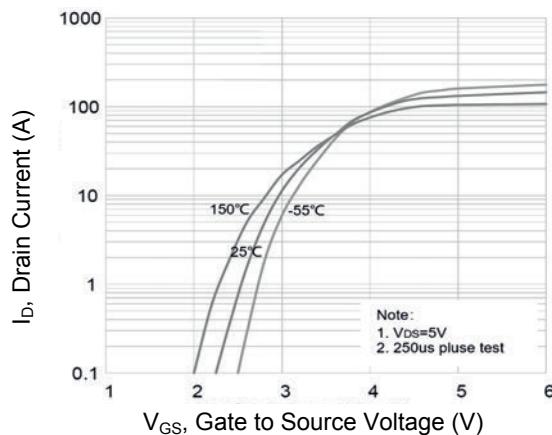


Figure 2. Transfer Characteristics

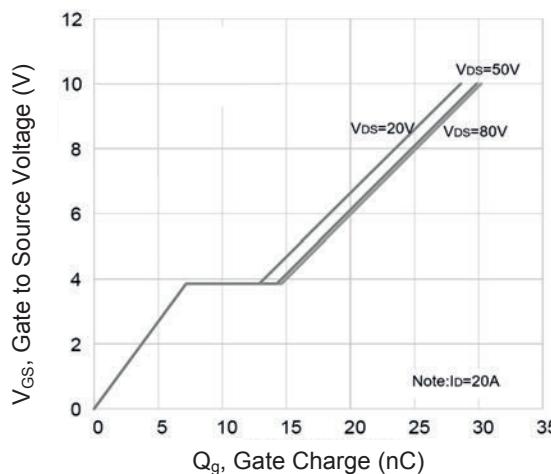


Figure 3. Gate Charge

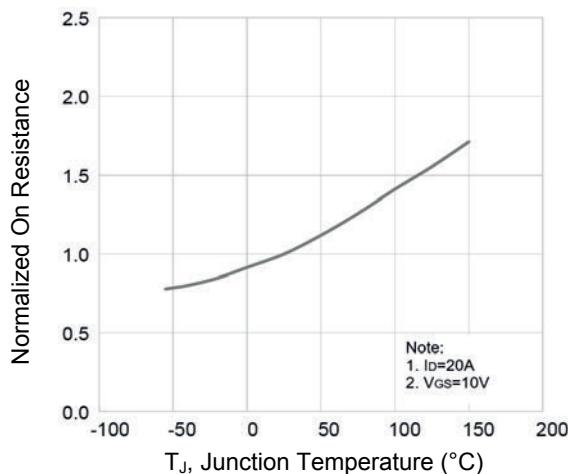


Figure 4. Normalized  $R_{DS(ON)}$  Vs.  $T_J$

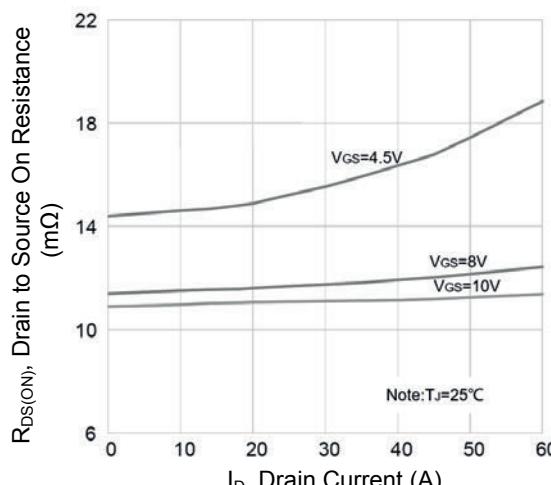


Figure 5. On-Resistance Vs. Drain Current

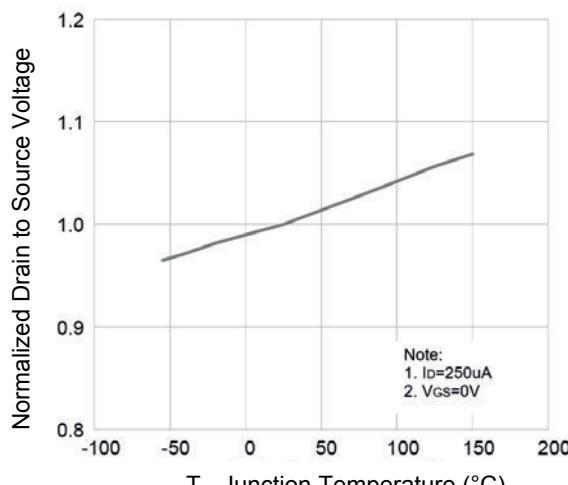


Figure 6. Normalized  $BV_{DSS}$  Vs.  $T_J$

## Typical Electrical and Thermal Characteristic Curves

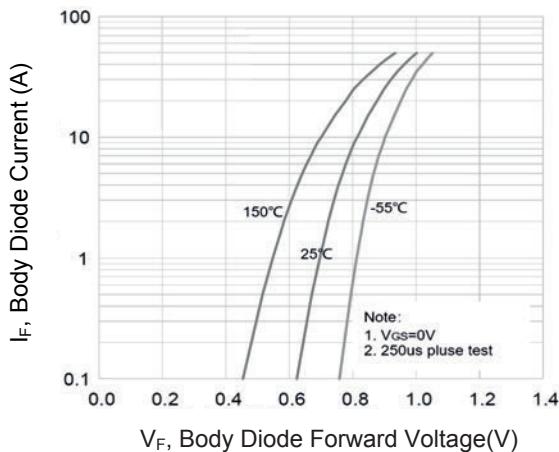


Figure 7. Body Diode Characteristics

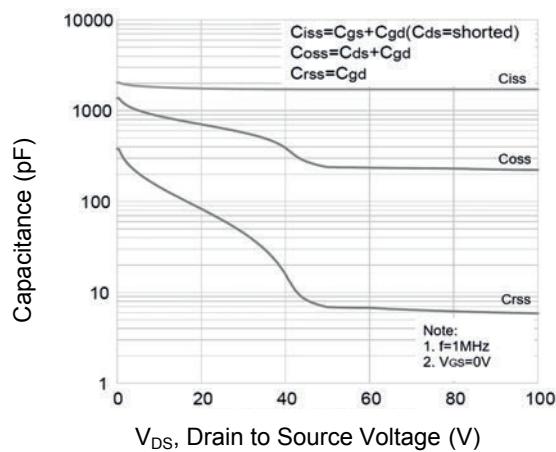


Figure 8. Capacitance Characteristics

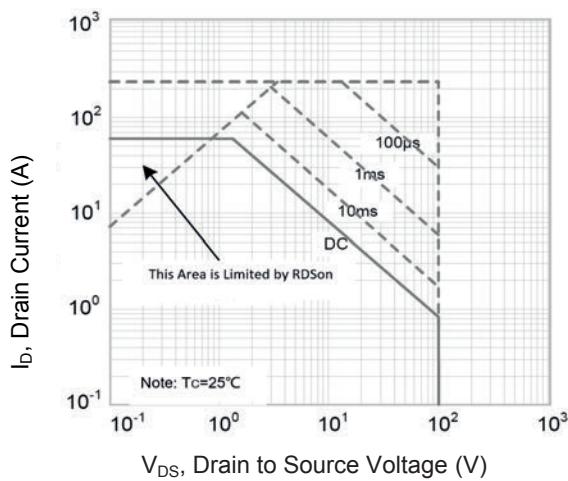
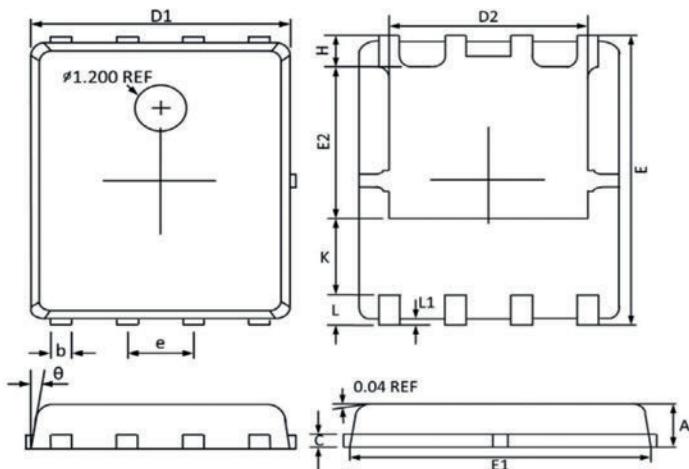


Figure 9. Safe Operation Area

### Package Outline Dimensions (PPAK5x6)



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.800	1.100	0.031	0.043
b	0.330	0.510	0.013	0.020
C	0.200	0.300	0.008	0.012
D1	4.800	5.100	0.189	0.201
D2	3.610	4.100	0.142	0.161
E	5.900	6.200	0.232	0.244
E1	5.700	5.900	0.224	0.232
E2	3.350	3.780	0.132	0.149
e	1.27 BSC		0.050 BSC	
H	0.410	0.700	0.016	0.028
K	1.100	1.500	0.043	0.059
L	0.510	0.710	0.020	0.028
L1	0.060	0.200	0.002	0.008
θ	0°	12°	0°	12°