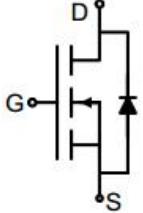


## N-Channel Enhancement Mode Power MOSFET

<p><b>Description</b></p> <p>The GT023N10T uses advanced trench technology to provide excellent <math>R_{DS(ON)}</math>, low gate charge. It can be used in a wide variety of applications.</p> <p><b>General Features</b></p> <ul style="list-style-type: none"> <li>● <math>V_{DS}</math> 100V</li> <li>● <math>I_D</math> (at <math>V_{GS} = 10V</math>) 226A</li> <li>● <math>R_{DS(ON)}</math> (at <math>V_{GS} = 10V</math>) &lt; 2.7mΩ</li> <li>● 100% Avalanche Tested</li> <li>● RoHS Compliant</li> </ul> <p><b>Application</b></p> <ul style="list-style-type: none"> <li>● Power switch</li> <li>● DC/DC converters</li> </ul>	 <p>Schematic diagram</p>  <p>TO-220</p>
--	--

<b>Ordering Information</b>			
<b>Device</b>	<b>Package</b>	<b>Marking</b>	<b>Packaging</b>
GT023N10T	TO-220	GT023N10	50pcs/Tube

<b>Absolute Maximum Ratings</b> $T_C = 25^\circ\text{C}$ , unless otherwise noted			
<b>Parameter</b>	<b>Symbol</b>	<b>Value</b>	<b>Unit</b>
Drain-Source Voltage	$V_{DS}$	100	V
Continuous Drain Current	$I_D$	226	A
Pulsed Drain Current (note1)	$I_{DM}$	904	A
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Power Dissipation	$P_D$	250	W
Single pulse avalanche energy (note2)	$E_{AS}$	600	mJ
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55 To 175	°C

<b>Thermal Resistance</b>			
<b>Parameter</b>	<b>Symbol</b>	<b>Value</b>	<b>Unit</b>
Thermal Resistance, Junction-to-Ambient	$R_{thJA}$	62	°C/W
Maximum Junction-to-Case	$R_{thJC}$	0.5	°C/W

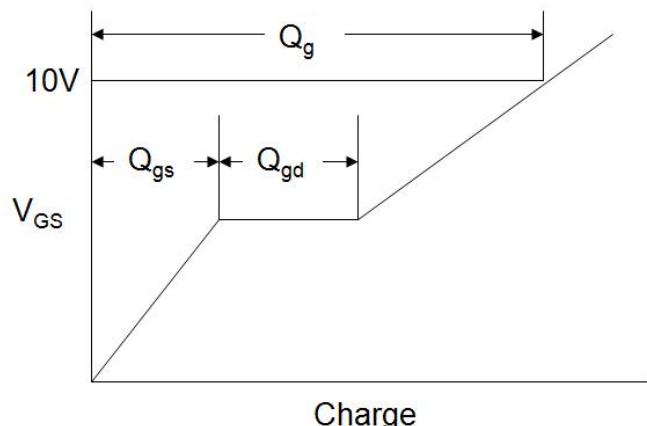
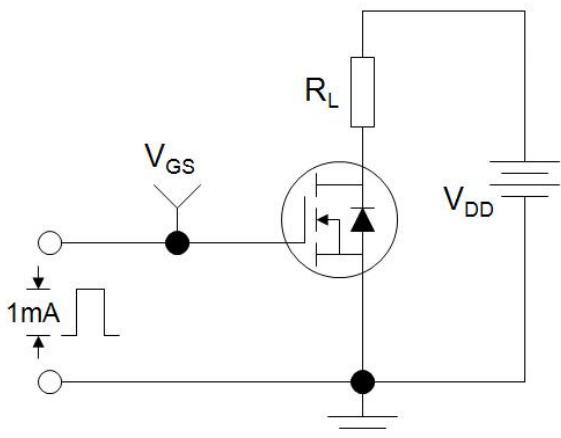
**Specifications**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
<b>Static Parameters</b>						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0\text{V}, I_D = 250\mu\text{A}$	100	--	--	V
Zero Gate Voltage Drain Current	$I_{\text{DSS}}$	$V_{\text{DS}} = 100\text{V}, V_{\text{GS}} = 0\text{V}$	--	--	1	$\mu\text{A}$
Gate-Source Leakage	$I_{\text{GSS}}$	$V_{\text{GS}} = \pm 20\text{V}$	--	--	$\pm 100$	nA
Gate-Source Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250\mu\text{A}$	2.0	3.0	4.0	V
Drain-Source On-Resistance	$R_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 10\text{V}, I_D = 80\text{A}$	--	2.3	2.7	$\text{m}\Omega$
Forward Transconductance	$g_{\text{FS}}$	$V_{\text{GS}} = 5\text{V}, I_D = 80\text{A}$	--	125	--	S
<b>Dynamic Parameters</b>						
Input Capacitance	$C_{\text{iss}}$	$V_{\text{GS}} = 0\text{V}, V_{\text{DS}} = 50\text{V}, f = 0.3\text{MHz}$	--	8139	--	pF
Output Capacitance	$C_{\text{oss}}$		--	2544	--	
Reverse Transfer Capacitance	$C_{\text{rss}}$		--	29	--	
Total Gate Charge	$Q_g$	$V_{\text{DD}} = 50\text{V}, I_D = 80\text{A}, V_{\text{GS}} = 10\text{V}$	--	121	--	nC
Gate-Source Charge	$Q_{\text{gs}}$		--	36	--	
Gate-Drain Charge	$Q_{\text{gd}}$		--	26	--	
Turn-on Delay Time	$t_{\text{d}(\text{on})}$	$V_{\text{DD}} = 50\text{V}, I_D = 80\text{A}, R_G = 5\Omega$	--	24	--	ns
Turn-on Rise Time	$t_r$		--	30	--	
Turn-off Delay Time	$t_{\text{d}(\text{off})}$		--	94	--	
Turn-off Fall Time	$t_f$		--	74	--	
<b>Drain-Source Body Diode Characteristics</b>						
Continuous Body Diode Current	$I_S$	$T_C = 25^\circ\text{C}$	--	--	226	A
Body Diode Voltage	$V_{\text{SD}}$	$T_J = 25^\circ\text{C}, I_{\text{SD}} = 80\text{A}, V_{\text{GS}} = 0\text{V}$	--	--	1.2	V
Reverse Recovery Charge	$Q_{\text{rr}}$	$I_F = 80\text{A}, V_{\text{GS}} = 0\text{V}$ $dI/dt = 100\text{A}/\mu\text{s}$	--	297	--	nC
Reverse Recovery Time	$T_{\text{rr}}$		--	94	--	ns

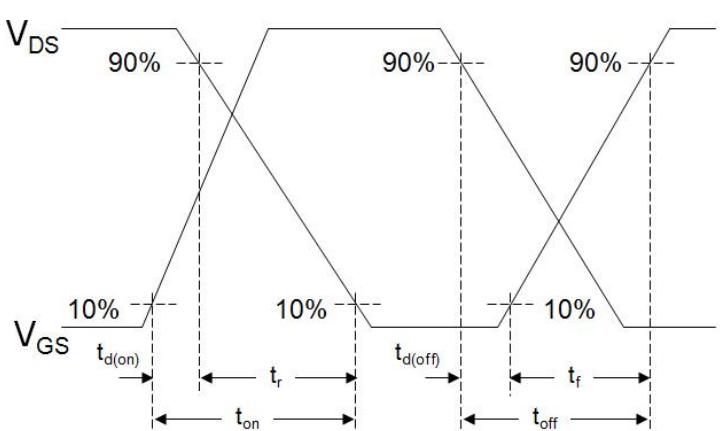
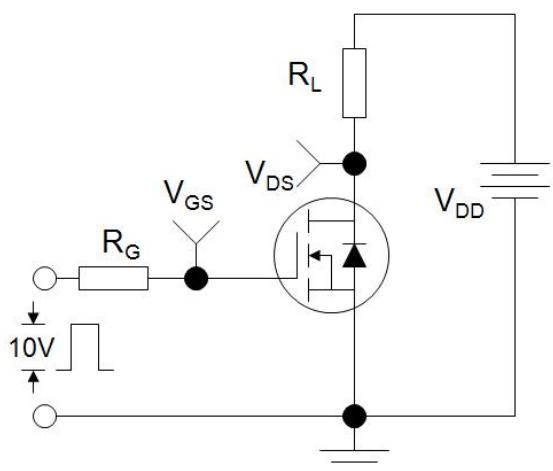
**Notes**

- Repetitive Rating: Pulse width limited by maximum junction temperature
- EAS condition :  $T_J=25^\circ\text{C}$ ,  $V_{\text{DD}}=50\text{V}$ ,  $V_{\text{GS}}=10\text{V}$ ,  $L=0.5\text{mH}$ ,  $R_G=25\Omega$
- Identical low side and high side switch with identical  $R_G$

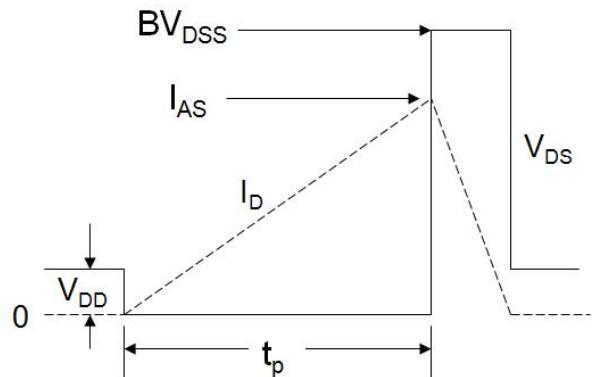
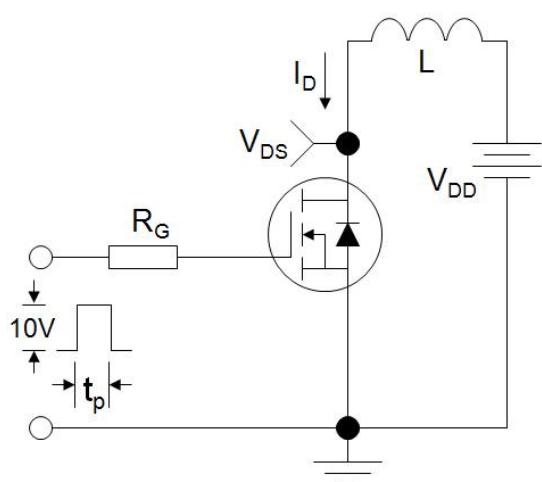
### Gate Charge Test Circuit



### Switch Time Test Circuit

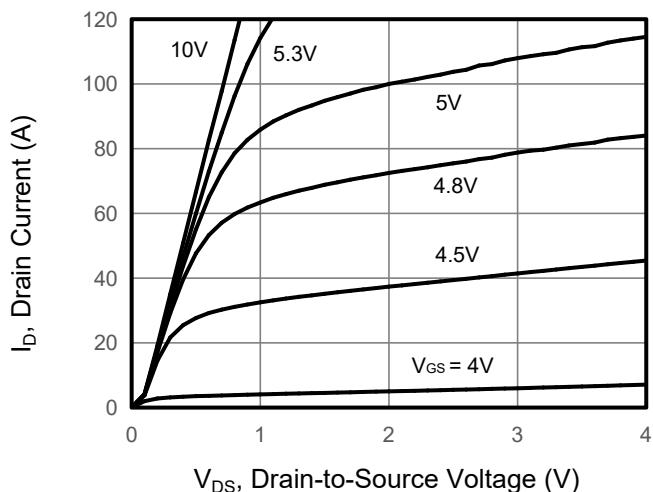


### EAS Test Circuit

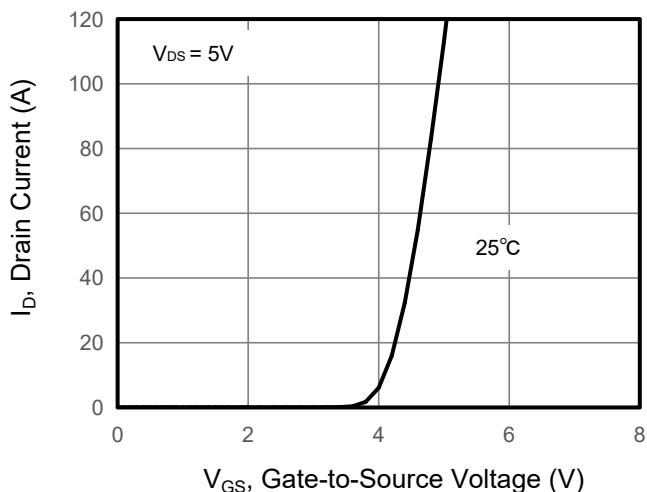


**Typical Characteristics**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

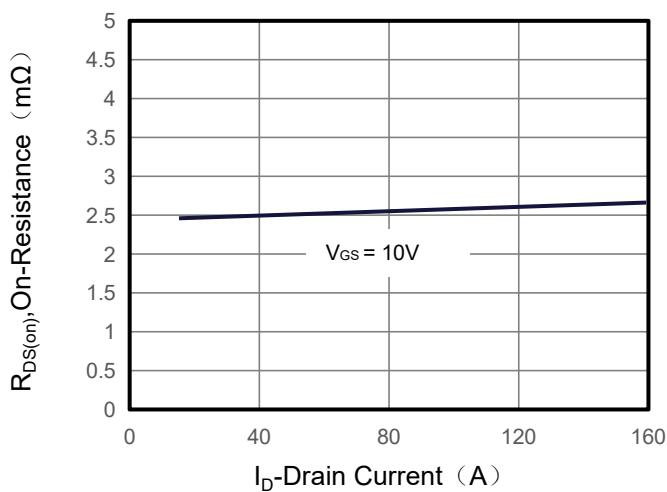
**Figure 1. Output Characteristics**



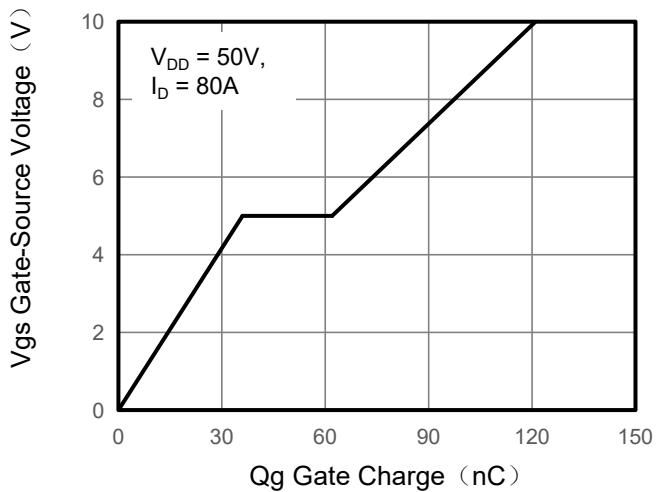
**Figure 2. Transfer Characteristics**



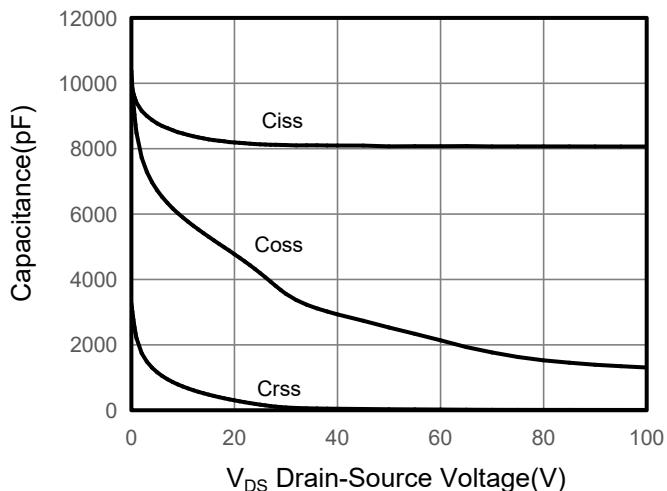
**Figure 3. Drain Source On Resistance**



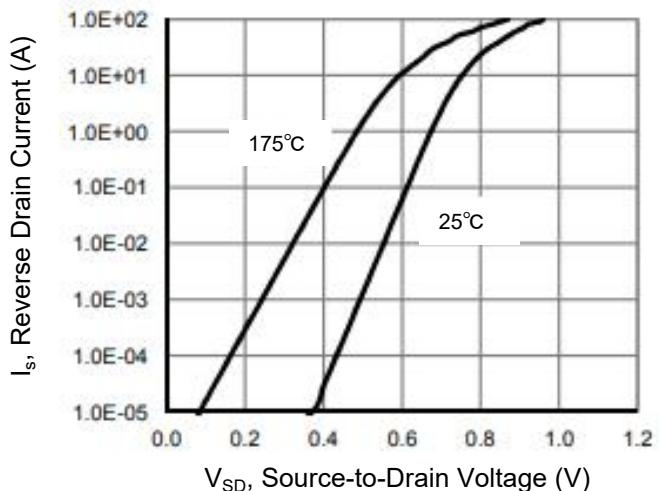
**Figure 4. Gate Charge**



**Figure 5. Capacitance**

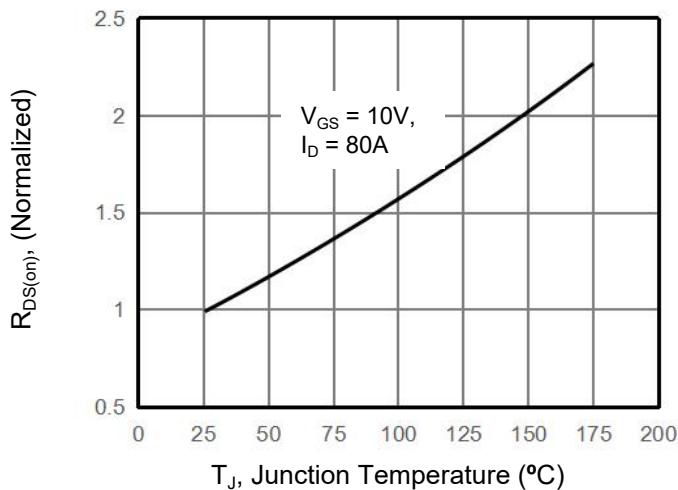


**Figure 6. Source-Drain Diode Forward**

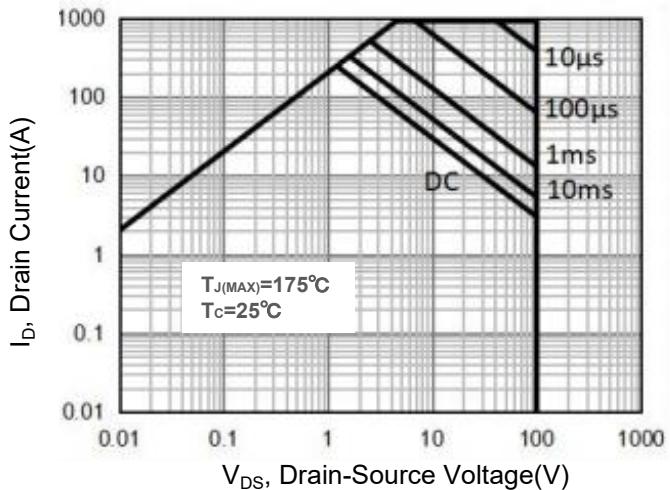


**Typical Characteristics**  $T_J = 25^\circ\text{C}$ , unless otherwise noted

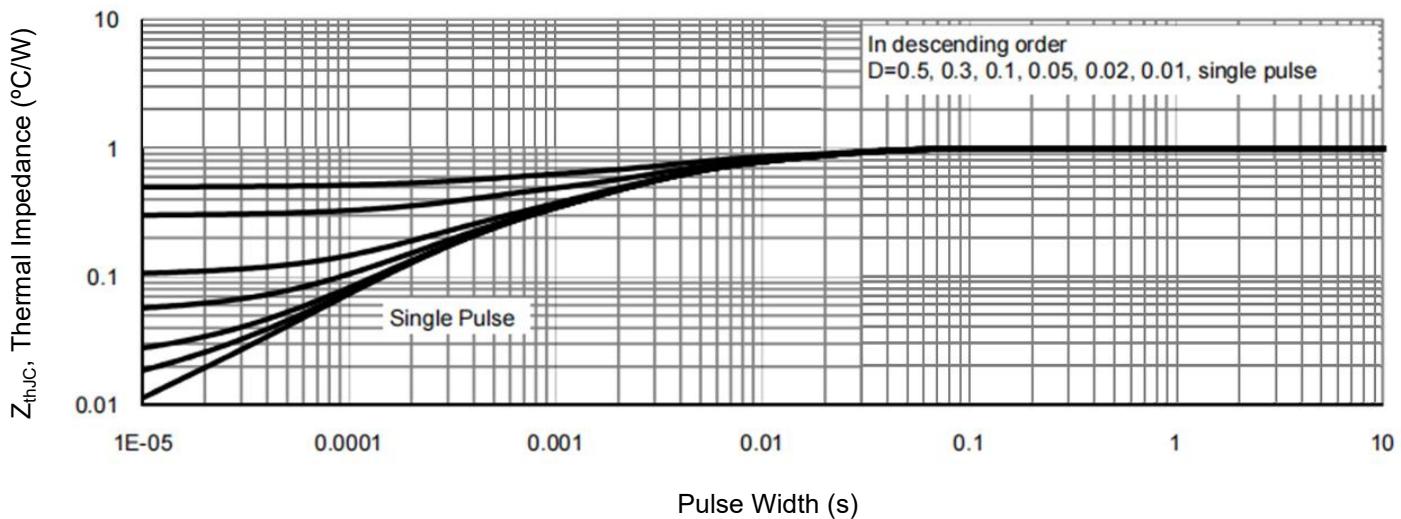
**Figure 7. Drain-Source On-Resistance**



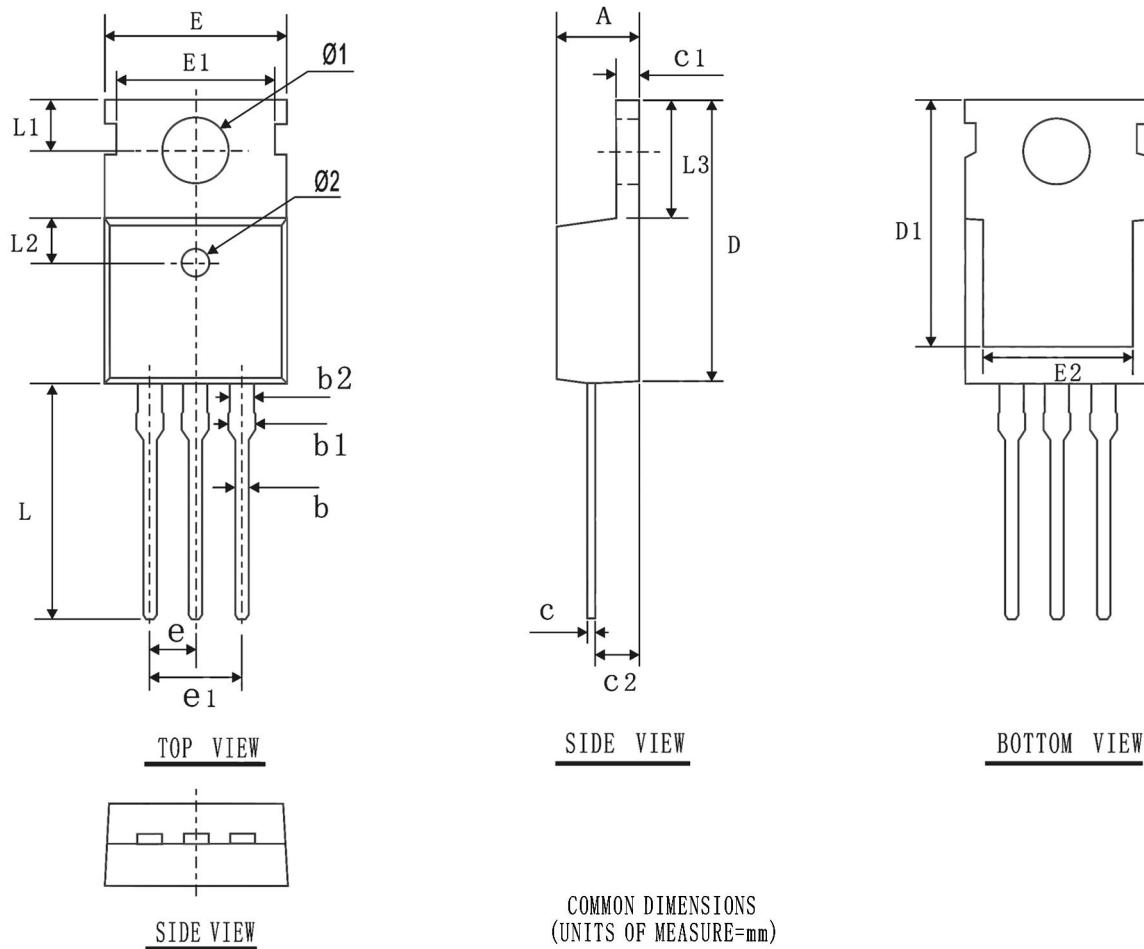
**Figure 8. Safe Operation Area**



**Figure 9. Normalized Maximum Transient Thermal Impedance**



## TO-220 Package Information



SYMBOL	MIN	NOM	MAX
A	4.30	4.50	4.70
b	0.70	0.80	0.90
b1	---	---	1.42
b2	1.17	1.27	1.37
c	0.40	0.50	0.60
C1	1.25	1.30	1.35
C2	2.20	2.40	2.60
D	15.45	15.65	15.85
D1	13.20	13.40	13.60
E	9.80	10.0	10.2
E1	8.60	8.70	8.80
E2	7.80	8.00	8.20
e1	4.88	5.08	5.28
L	12.95	13.15	13.35
L1	2.70	2.80	2.90
L2	2.40	2.50	2.60
L3	6.30	6.50	6.70
Ø1	3.50	3.60	3.70
Ø2	1.35	1.50	1.65
e	2.54BSC		