

## N-Channel Enhancement Mode Power MOSFET

### Description

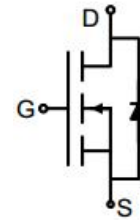
The GT080N10K uses advanced trench technology to provide excellent  $R_{DS(ON)}$ , low gate charge. It can be used in a wide variety of applications.

### General Features

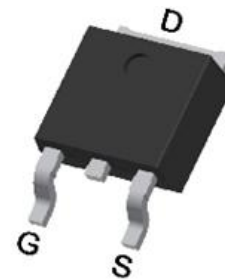
- $V_{DS}$  100V
- $I_D$  (at  $V_{GS} = 10V$ ) 65A
- $R_{DS(ON)}$  (at  $V_{GS} = 10V$ ) < 8m $\Omega$
- $R_{DS(ON)}$  (at  $V_{GS} = 4.5V$ ) < 9.5m $\Omega$
- 100% Avalanche Tested
- RoHS Compliant

### Application

- Power switch
- DC/DC converters



Schematic diagram



TO-252

### Ordering Information

Device	Package	Marking	Packaging
GT080N10K	TO-252	GT080N10	2500pcs/Reel

### Absolute Maximum Ratings $T_C = 25^\circ\text{C}$ , unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-Source Voltage	$V_{DS}$	100	V
Continuous Drain Current	$I_D$	65	A
Pulsed Drain Current (note1)	$I_{DM}$	260	A
Gate-Source Voltage	$V_{GS}$	$\pm 20$	V
Power Dissipation	$P_D$	100	W
Single pulse avalanche energy (note2)	$E_{AS}$	144	mJ
Operating Junction and Storage Temperature Range	$T_J, T_{stg}$	-55 To 150	$^\circ\text{C}$

### Thermal Resistance

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Ambient	$R_{thJA}$	50	$^\circ\text{C/W}$
Maximum Junction-to-Case	$R_{thJC}$	1.25	$^\circ\text{C/W}$

Specifications $T_J = 25^\circ\text{C}$ , unless otherwise noted						
Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
<b>Static Parameters</b>						
Drain-Source Breakdown Voltage	$V_{(BR)DSS}$	$V_{GS} = 0V, I_D = 250\mu A$	100	--	--	V
Zero Gate Voltage Drain Current	$I_{DSS}$	$V_{DS} = 100V, V_{GS} = 0V$	--	--	1	$\mu A$
Gate-Source Leakage	$I_{GSS}$	$V_{GS} = \pm 20V$	--	--	$\pm 100$	nA
Gate-Source Threshold Voltage	$V_{GS(th)}$	$V_{DS} = V_{GS}, I_D = 250\mu A$	1.0	1.7	2.5	V
Drain-Source On-Resistance	$R_{DS(on)}$	$V_{GS} = 10V, I_D = 20A$	--	6.2	8.0	m $\Omega$
		$V_{GS} = 4.5V, I_D = 20A$	--	7.8	9.5	
Forward Transconductance	$g_{FS}$	$V_{GS} = 5V, I_D = 20A$	--	48	--	S
<b>Dynamic Parameters</b>						
Input Capacitance	$C_{iss}$	$V_{GS} = 0V,$ $V_{DS} = 50V,$ $f = 1.0\text{MHz}$	--	2530	--	pF
Output Capacitance	$C_{oss}$		--	395	--	
Reverse Transfer Capacitance	$C_{rss}$		--	13	--	
Total Gate Charge	$Q_g$	$V_{DD} = 50V,$ $I_D = 20A,$ $V_{GS} = 10V$	--	43	--	nC
Gate-Source Charge	$Q_{gs}$		--	7	--	
Gate-Drain Charge	$Q_{gd}$		--	9	--	
Turn-on Delay Time	$t_{d(on)}$	$V_{DD} = 50V,$ $I_D = 20A,$ $R_G = 1.6\Omega$	--	10	--	ns
Turn-on Rise Time	$t_r$		--	8	--	
Turn-off Delay Time	$t_{d(off)}$		--	23	--	
Turn-off Fall Time	$t_f$		--	6	--	
<b>Drain-Source Body Diode Characteristics</b>						
Continuous Body Diode Current	$I_S$	$T_C = 25^\circ\text{C}$	--	--	65	A
Body Diode Voltage	$V_{SD}$	$T_J = 25^\circ\text{C}, I_{SD} = 20A, V_{GS} = 0V$	--	--	1.2	V
Reverse Recovery Charge	$Q_{rr}$	$I_F = 20A, V_{GS} = 0V$ $di/dt = 100A/\mu s$	--	75	--	nC
Reverse Recovery Time	$T_{rr}$		--	42	--	ns

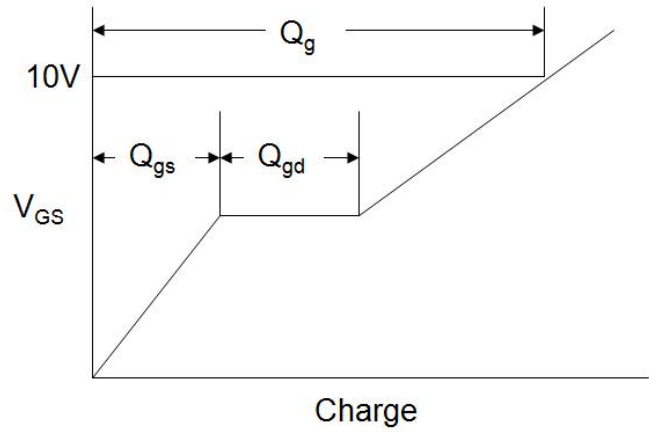
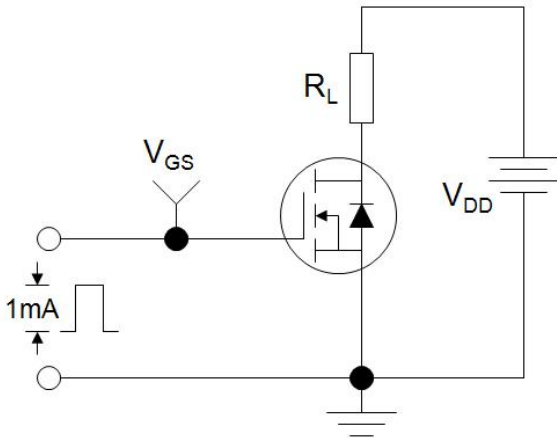
### Notes

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. EAS condition :  $T_J = 25^\circ\text{C}, V_{DD} = 50V, V_{GS} = 10V, L = 0.5\text{mH}, R_G = 25\Omega$

The table shows the minimum avalanche energy, which is 400mJ when the device is tested until failure

3. Identical low side and high side switch with identical  $R_G$

### Gate Charge Test Circuit



### Switch Time Test Circuit

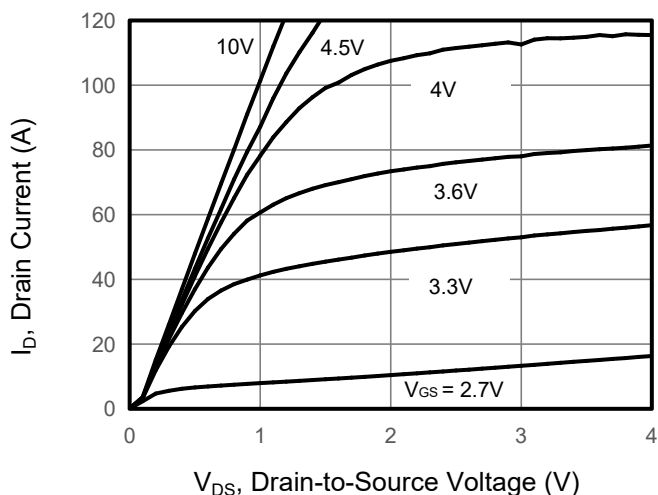


### EAS Test Circuit

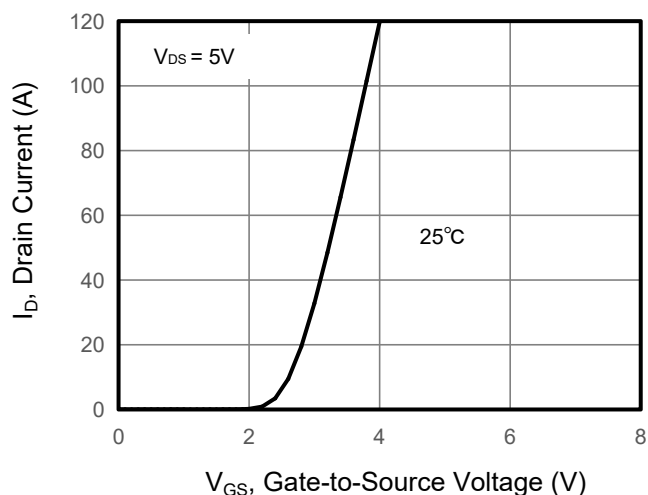


Typical Characteristics  $T_J = 25^\circ\text{C}$ , unless otherwise noted

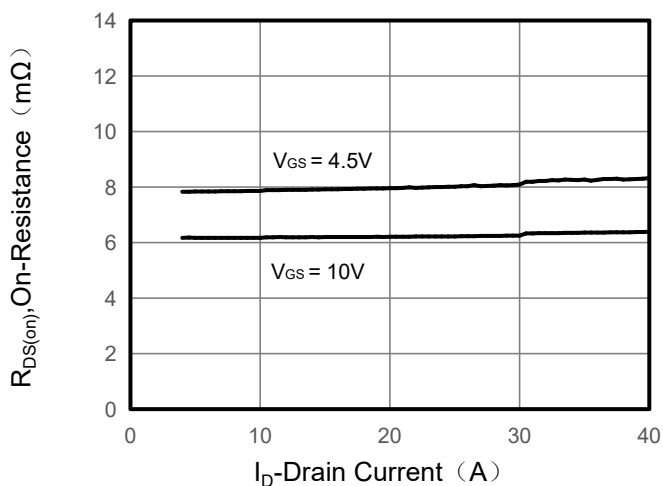
**Figure 1. Output Characteristics**



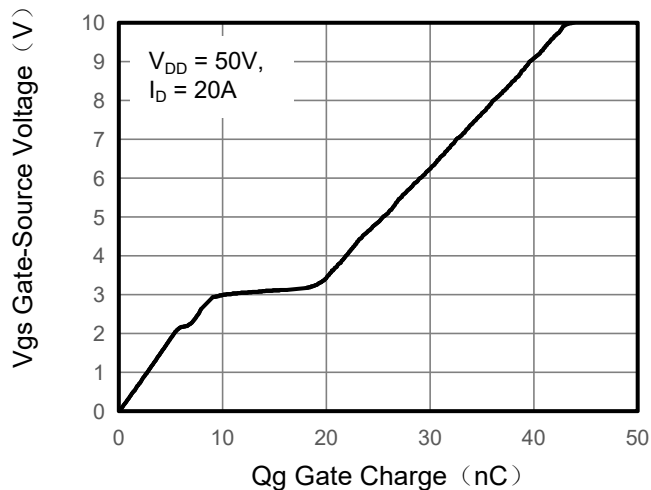
**Figure 2. Transfer Characteristics**



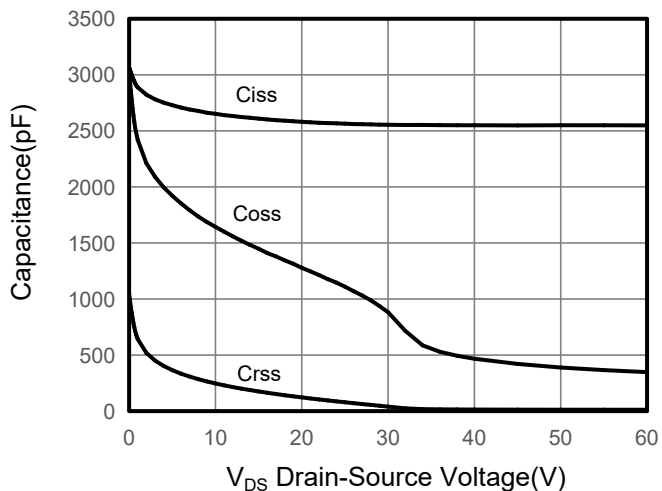
**Figure 3. Drain Source On Resistance**



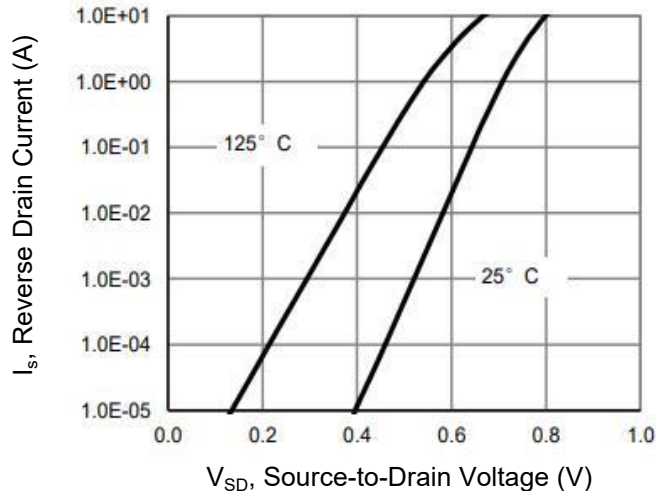
**Figure 4. Gate Charge**



**Figure 5. Capacitance**



**Figure 6. Source-Drain Diode Forward**



Typical Characteristics  $T_J = 25^\circ\text{C}$ , unless otherwise noted

Figure 7. Drain-Source On-Resistance

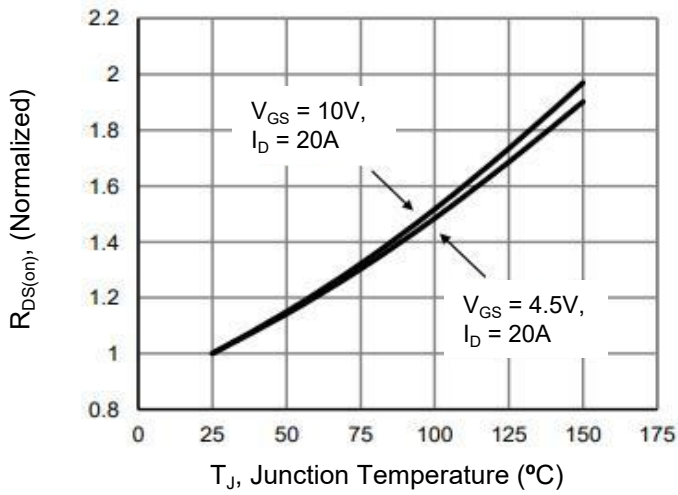


Figure 8. Safe Operation Area

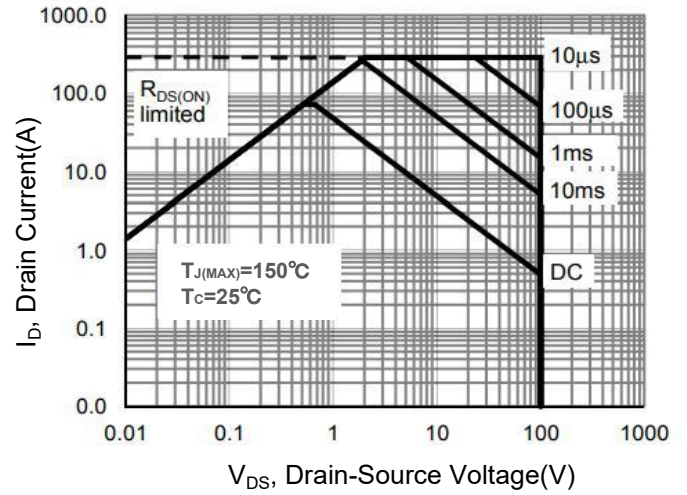
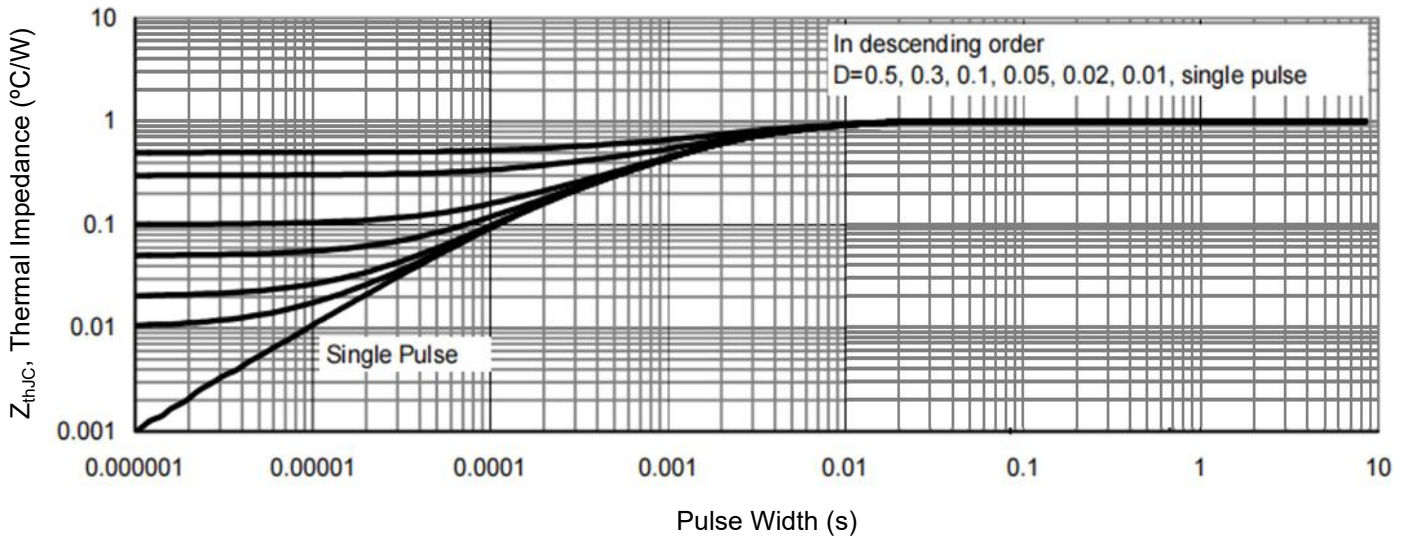
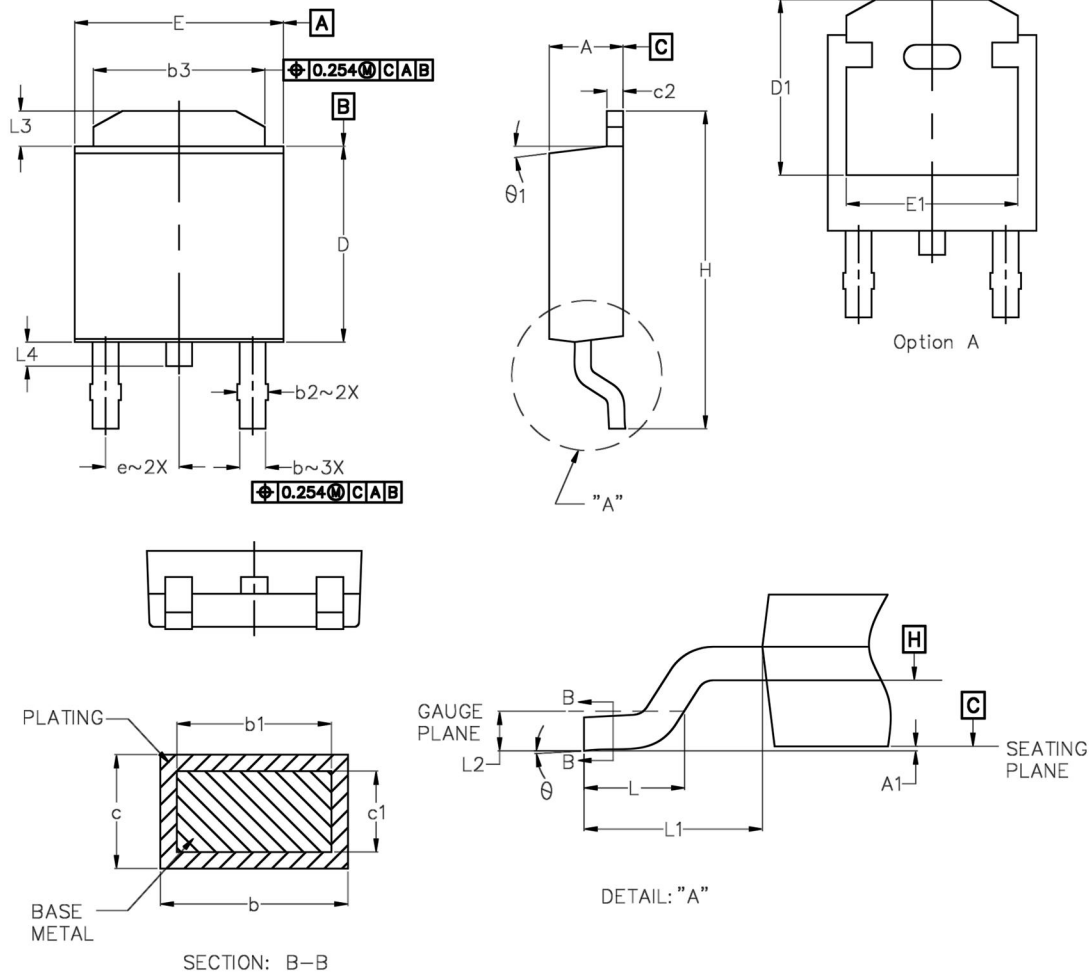


Figure 9. Normalized Maximum Transient Thermal Impedance



## TO-252 Package Information



S TANDARD S	COMMON			
	MM		INCH	
	MIN.	MAX.	MIN.	MAX.
A	2.184	2.387	0.086	0.094
A1	—	0.127	—	0.005
b	0.750	0.890	0.029	0.035
b1	0.750	0.860	0.029	0.034
b2	0.762	1.143	0.030	0.045
b3	4.953	5.461	0.195	0.215
c	0.460	0.610	0.018	0.024
c1	0.410	0.559	0.016	0.022
c2	0.460	0.889	0.018	0.035
D	5.969	6.223	0.235	0.245
D1	5.207	—	0.205	—
E	6.350	6.731	0.250	0.265
E1	4.318	—	0.170	—
e	2.290 BSC		0.090 BSC	
H	9.398	10.414	0.370	0.410
L	1.397	1.778	0.055	0.070
L1	2.743 BSC		0.108 BSC	
L2	0.508 BSC		0.020 BSC	
L3	0.889	1.270	0.035	0.050
L4	—	1.016	—	0.040
$\theta$	0°	10°	0°	10°
$\theta_1$	0°	15°	0°	15°