ESD Array Protection Device

Mechanical Data

Description

The H04X643V3U is ultra low capacitance ESD arrays designed to protect high speed data interfaces. This series has been specifically designed to protect sensitive components which are connected to high-speed data and transmission lines from over voltage caused by ESD (electrostatic discharge).

Features

- Protects four I/O lines (Data line)
- Peak Pulse Power :Ppp = 50W (tp=8/20 us)
- Reverse Working Voltage: 3.3V
- Low Leakage Current
- Low Clamping Voltage
- Ultra Low Junction Capacitance: I/O to I/O, 0.3pF (Max)
- IEC 61000-4-2 (ESD) :±20kV(Contact) / ±25kV(Air)

Applications

- High definition multi-media interface (HDMI)
- Digital visual interface (DVI)
- Display protTM interface
- USB 3.0/3.1
- MDDI ports / SATA

- Case: DFN2510 Package
- Case Material: "Green" Molding Compound UL Flammability

Peak Pulse Power - 50 Watts

Reverse Working Voltage - 3.3V

Classification Rating 94V-0

- Terminal: Matte tin plated.
- Component in accordance to RoHS
- Halogen Free

Note: Products with logo or or are made by HY Electronic (Cayman) Limited.

Ordering Information

- Package :DFN2510
- Reel Size :7 (inches)
- Quantity Per Reel :3,000/Tape & Reel
- Quantity One Box :30,000/Tape & Reel
- Quantity One Carton :120,000/Tape & Reel

Marking Information

3324P

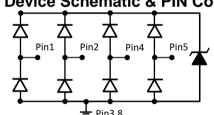
Product Type Marking Code

Package Outline



DFN2510 Top View

Device Schematic & PIN Configuration



Pin Assignment		
1, 2, 4, 5	Input lines	
6, 7, 9, 10	NC	
3, 8	Ground	

Maximum Ratings (@TA = +25°C, unless otherwise specified.)

Absolute Ratings

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Parameter	Symbol	Value	Unit	
Peak Pulse Power Dissipation (8/20 us)	Ppp	50	W	
Peak Pulse Current (8/20 us)	Ipp	4	Α	
ESD Protection- Contact (Standard IEC 61000-4-2)	Vesd	±20	k۷	
ESD Protection- Air (Standard IEC 61000-4-2)	VESD	±25	ΚV	
Operating Temperature Range	TJ	-55 to +125	° C	
Storage Temperature Range	Тѕтс	-55 to +150	° C	
Soldering Temperature, t max =10s	TL	260	° C	

Electrical Characteristics

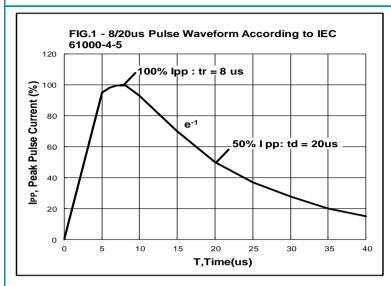
Parameter	Test Conditions	Symbol	Min	Тур	Max	Unit
Reverse Working Voltage	Any I/O pin to ground	Vrwm	-	-	3.3	V
Reverse Breakdown Voltage	I⊤= 1mA	Vв	4.2	-	9	V
Reverse Current	VR = 3.3V	l _R	-	-	1	uA
Reverse Clamping Voltage	I _{PP} = 1A (8/20μs)	Vc	-	-	10	V
	I _{PP} = 4A (8/20μs)		-	-	14	
Junction Capacitance	VR = 0V, F = 1MHz Between I/O pins	Ci	-	0.25	0.3	рF
	$V_R = 0V, F = 1MHz$ Any I/O pin to ground	C)	-	0.5	0.6	

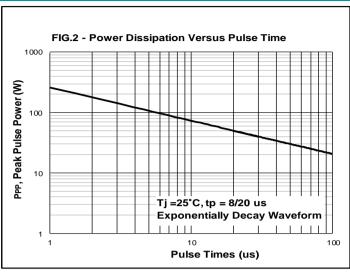
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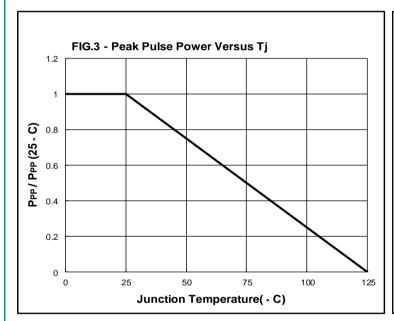
Rev-1, 20-Jul-2021

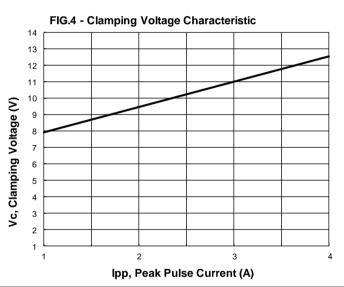


Rating and Characteristic Curves

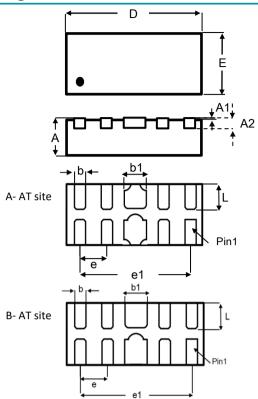








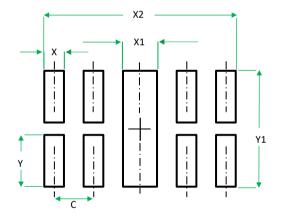
Package Outline Dimensions



DFN2510 Package				
Dim.	Min	Тур Мах		
D	2.45	2.50	2.55	
Е	0.95	1.05		
А	0.45	0.55		
A1	0.00	-	0.05	
A2	0.15REF			
b	0.15 0.20 0.		0.25	
b1	0.35	0.40	0.45	
е	-	0.50	-	
e1	-	2.00	-	
L	0.33	0.38	0.43	
All Dimensions in mm				

Note: HY internal have both AT site

Suggested Soldering Pad Layout



Dim.	Value	
Х	0.25	
X1	0.45	
X2	2.25	
Y	0.63	
Y1	1.40	
С	0.50	
All Dimensions in mm		



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