

FEATURES

- Ultra Low Dropout Voltage
- Compatible with low ESR MLCC as Input / Output Capacitor
- Good Line and Load Regulation
- Guaranteed Output Current of 1.5A
- Available in TO-252-5L Package
- Fixed Output: 1.8V, 2.5V, and 3.3V
- Output Auto Discharge Function
- Over-Temperature/ Over-Current Protection

APPLICATION

- LCD TVs and SETTOP Boxes
- Battery Powered Equipment
- Motherboards and Graphic Cards
- Microprocessor Power Supplies
- Peripheral Cards
- High Efficiency Linear Regulators
- Battery Chargers

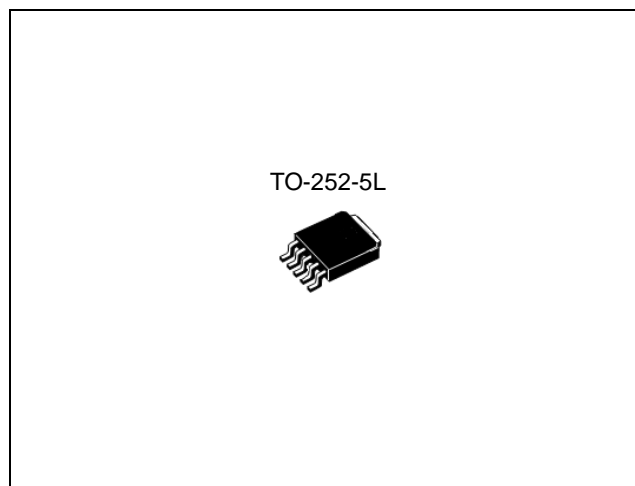
DESCRIPTION

The LD39150 series of high performance ultra-low dropout linear regulators operates from 2.5V to 5.5V input supply and provides ultra-low dropout voltage, high output current with low ground current. Wide range of preset output voltage options are available. These ultra-low dropout linear regulators respond fast to step changes in load which makes them suitable for low voltage micro-processor applications. The LD39150 is developed on a CMOS process technology which allows low quiescent current operation independent of output load current. This CMOS process also allows the LD39150 to operate under extremely low dropout conditions.

ABSOLUTE MAXIMUM RATINGS (Note 1)

CHARACTERISTIC	SYMBOL	MIN.	MAX.	UNIT
Input Supply Voltage (Survival)	V_{IN}	-	6.5	V
Maximum Output Current	I_{MAX}	-	2	A
Lead Temperature (Soldering, 5 sec)	T_{SOL}		260	°C
Storage Temperature Range	T_{STG}	-65	150	°C
Operating Junction Temperature Range	T_{JOPR}	-40	125	°C
Package Thermal Resistance *	$\Theta_{JA-TO252-5L}$	105		°C/W

* No heat sink / No air flow / No adjacent heat source / 0.066 inch² copper area. ($T_A=25^{\circ}\text{C}$)

**ORDERING INFORMATION**

Device	Package
LD39150GRS-ADJ	TO-252-5L
LD39150GRS-X.X	

X.X = Output Voltage = 1.8, 2.5, and 3.3

1.5A Ultra Low Dropout Linear Regulator

LD39150

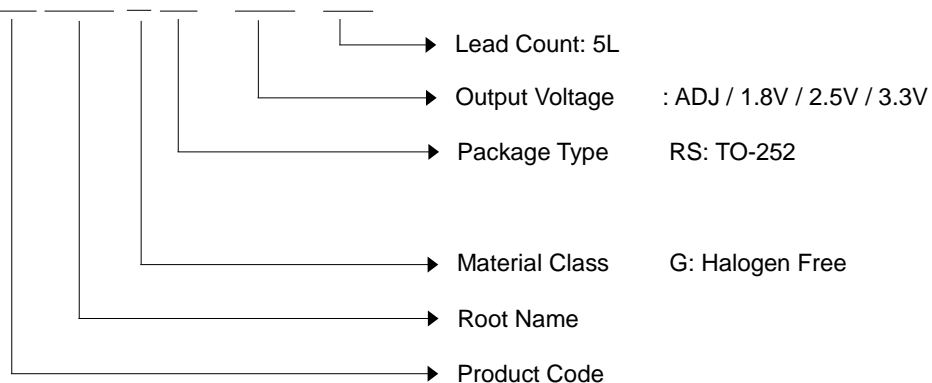
RECOMMENDED OPERATING RATINGS (Note 2)

CHARACTERISTIC	SYMBOL	MIN.	MAX.	UNIT
Operating Input Voltage Range	V_{IN}	2.5	5.5	V

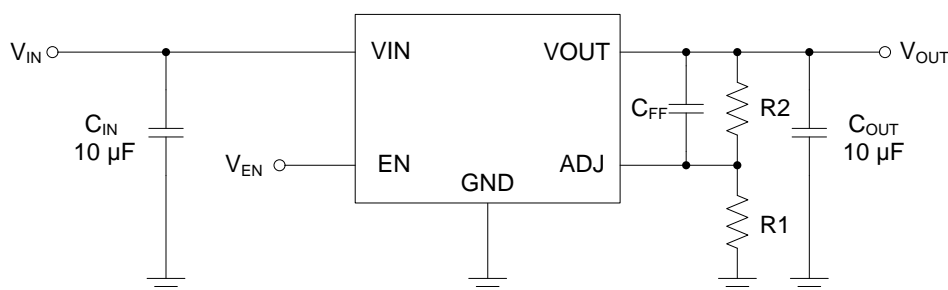
ORDERING INFORMATION

V_{OUT}	Package	Order No.	Description	Package Marking	Status
ADJ	TO-252-5L	LD39150GRS-ADJ-5L	1.5A, Enable, Adjustable	LD39150	Active
1.8V	TO-252-5L	LD39150GRS-1.8-5L	1.5A, Enable	LD39150 1.8	Contact Us
2.5V	TO-252-5L	LD39150GRS-2.5-5L	1.5A, Enable	LD39150 2.5	Contact Us
3.3V	TO-252-5L	LD39150GRS-3.3-5L	1.5A, Enable	LD39150 3.3	Contact Us

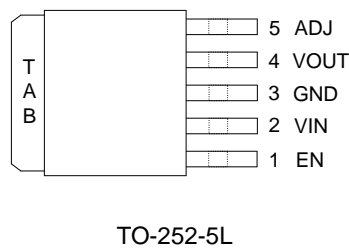
LD 39150 GRS – ADJ – 5 L



TYPICAL APPLICATION



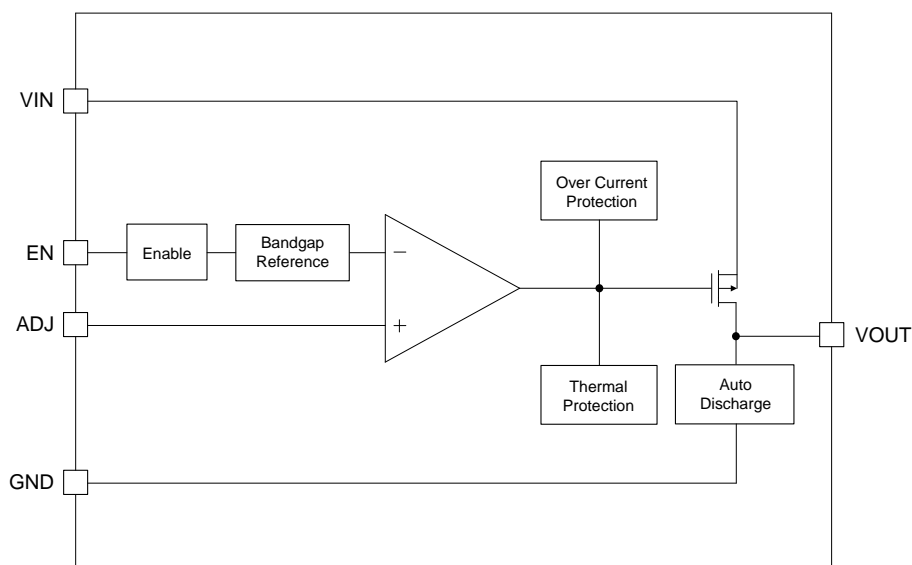
PIN CONFIGURATION



PIN DESCRIPTION

Pin No.	Pin Name	Pin Function
1	EN	Chip Enable. Pulling this pin below 0.4V turns the regulator off. Do not float.
2	VIN	Input Supply.
3	GND	Ground.
4	VOUT	Output Voltage.
5	ADJ	Output Adjust.
TAB	TAB	Connect to ground.

BLOCK DIAGRAM



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LD39150

ELECTRICAL CHARACTERISTICS (Note 3)

Limits in standard typeface are for $T_J=25^{\circ}\text{C}$, and limits in **boldface type** apply over the **full operating temperature range**.
Unless otherwise specified: $V_{IN}^{(Note\ 4)} = V_{O(NOM)} + 1\text{ V}$, $I_L = 10\text{ mA}$, $C_{IN} = 10\text{ }\mu\text{F}$, $C_{OUT} = 10\text{ }\mu\text{F}$, $V_{EN} = V_{IN} - 0.3\text{ V}$

PARAMETER		SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Output Voltage Tolerance		V _O	V _{OUT} +1 V < V _{IN} < 5.5 V	-2 -3	0	2 3	%
Adjustable Pin Voltage (ADJ version)		V _{ADJ}	2.5 V < V _{IN} < 5.5 V	0.588 0.582	0.6	0.612 0.618	V
Line Regulation ^(Note 5)		ΔV _{LINE}	V _{OUT} +1 V < V _{IN} < 5.5 V	-	0.25	-	%/V
Load Regulation ^(Note 5, 6)		ΔV _{LOAD}	10 mA < I _L < 1.5 A	-	0.20	-	%
Dropout Voltage ^(Note 7)		V _{DROP}	I _L = 200 mA	-	45	55 65	mV
			I _L = 1.5 A	-	280	380 450	
Ground Pin Current ^(Note 8)		I _{GND}	I _L = 200 mA	-	0.20	0.30 0.40	mA
			I _L = 1.5 A	-	0.30	0.40 0.60	
Ground Pin Current ^(Note 9)		I _{GND_OFF}	V _{EN} < 0.2 V, POK=Open	-	0.1	- 1	μA
Power Supply Rejection Ratio		PSRR	f = 1kHz	-	45	-	dB
			f = 1kHz, C _{FF} = 1μF	-	60	-	
Thermal Shutdown Temperature		T _{SD}	-	-	165	-	°C
Thermal Shutdown Hysteresis		ΔT _{SD}	-	-	20	-	°C
OCP Threshold Level		I _{OCP}	-	-	3.6	-	A
Auto Discharge Resistance		R _{DS}	V _{IN} = 5V, V _{EN} = 0V	-	330	-	Ω
Enable threshold	Logic Low	V _{IL}	Output = Low	-	-	0.4	V
	Logic High	V _{IH}	Output = High	2.0	-	-	V
Enable Input Current		I _{EN}	V _{EN} = V _{IN}	-	0.1	- 1	μA

Note 1. Exceeding the absolute maximum ratings may damage the device.

Note 2. The device is not guaranteed to function outside its operating ratings.

Note 3. Stresses listed as the absolute maximum ratings may cause permanent damage to the device. These are for stress ratings. Functional operating of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may remain possibly to affect device reliability.

Note 4. The minimum operating value for input voltage is equal to either $(V_{OUT,NOM} + V_{DROP})$ or 2.5V, whichever is greater.

- Note 5. Output voltage line regulation is defined as the change in output voltage from the nominal value due to change in the input line voltage.
Output voltage load regulation is defined as the change in output voltage from the nominal value due to change in load current.
- Note 6. Regulation is measured at constant junction temperature by using a 10ms current pulse. Devices are tested for load regulation in the load range from 10mA to 1.5A.
- Note 7. Dropout voltage is defined as the minimum input to output differential voltage at which the output drops 2% below the nominal value.
Dropout voltage specification applies only to output voltages of 2.5V and above. For output voltages below 2.5V, the dropout voltage is nothing but the input to output differential, since the minimum input voltage is 2.5V
- Note 8. Ground current, or quiescent current, is the difference between input and output currents. It's defined by $I_{GND1} = I_{IN} - I_{OUT}$ under the given loading condition. The total current drawn from the supply is the sum of the load current plus the ground pin current.
- Note 9. Ground current, or standby current, is the input current drawn by a regulator when the output voltage is disabled by an enable signal.

APPLICATION INFORMATION

Introduction

LD39150 is intended for applications where high current capability and very low dropout voltage are required. It provides a simple, low cost solution that occupies very little PCB estate. Additional features include an enable pin to allow for a very low power consumption standby mode, an adjustable pin to provide a fully adjustable output voltage.

Input Capacitor

A large bulk capacitance over than $10\mu\text{F}$ should be closely placed to the input supply pin of the LD39150 to ensure that the input supply voltage does not sag. Also a minimum of $10\mu\text{F}$ ceramic capacitor is recommended to be placed directly next to the VIN Pin. It allows for the device being some distance from any bulk capacitor on the rail. Additionally, input droop due to load transients is reduced, improving load transient response. Additional capacitance may be added if required by the application. (See Fig.1)

Output Capacitor

A minimum ceramic capacitor over than $10\mu\text{F}$ should be very closely placed to the output voltage pin of the LD39150. Increasing capacitance will improve the overall transient response and stability.

Decoupling (Bypass) Capacitor

In very electrically noisy environments, it is recommended that additional ceramic capacitors be placed from VIN to GND. The use of multiple lower value ceramic capacitors in parallel with output capacitor also allows to achieve better transient performance and stability if required by the application. (See Fig.1)

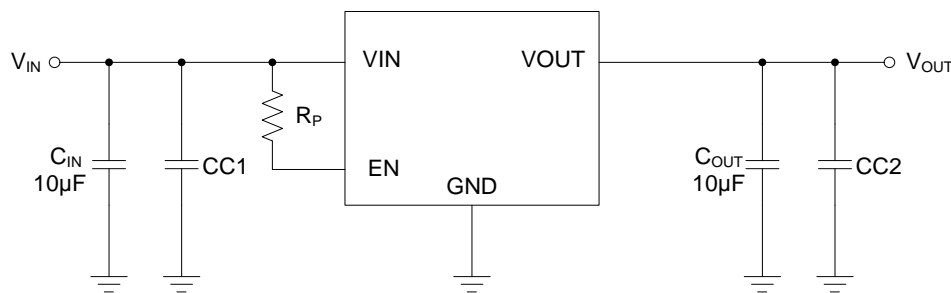


Fig. 1. Application with Decoupling Capacitor, CC1 & CC2

Feed-Forward Capacitor

To get the higher PSRR than the inherent performance of LD39150, it is recommended that additional ceramic feed-forward capacitor be placed from VOUT pin to ADJ pin. The capacitance of feed-forward capacitor with range of 2.2nF to $1\mu\text{F}$ allows to achieve better PSRR performance when required by the application. (See Fig.2)

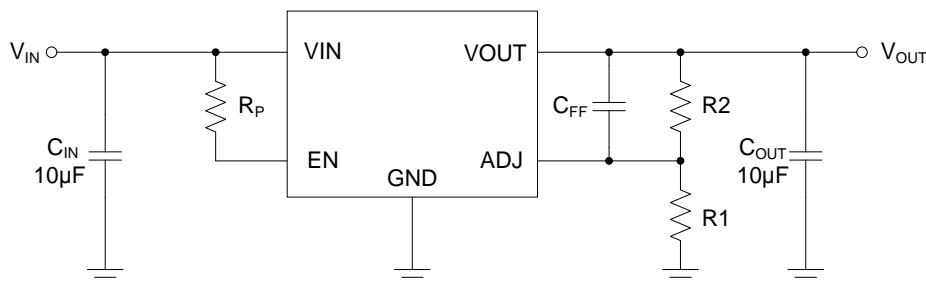


Fig. 2. Application with Feed-Forward Capacitor, CFF

Delayed Start-Up

When power sequence control is required or rising time of input supply voltage is over than 100μsec, it is recommended to apply delayed start-up by using Cdelay as shown in Fig. 3. It can adjust proper delay by Rp-Cdelay time constant. And also it can prevent any unexpected transient characteristics at output voltage when the rising time of input supply voltage is as long as 100μsec or longer.

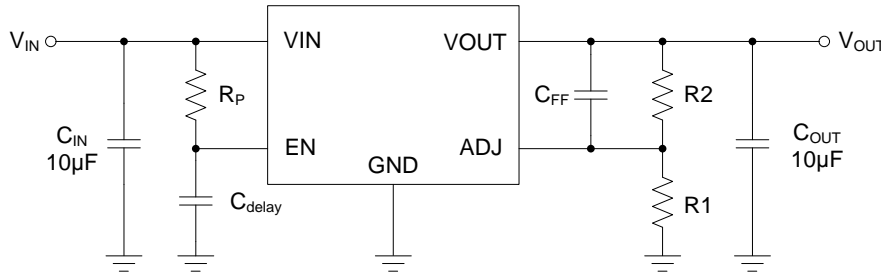
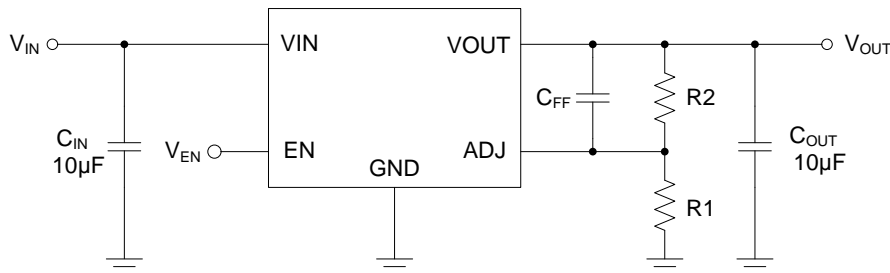


Fig. 3. Application with Delayed Start-Up

Output Adjustment (Adjustable Version)

An adjustable output device has output voltage range of 1.0V to 5.0V. The operating condition of VIN and the operating characteristics of VOUT depend on the dropout voltage performance in accordance with output load current. To obtain a desired output voltage, the following equation can be used with R1 resistor range of 1kΩ to 100kΩ.



$$R_2 = R_1 \left(\frac{V_{OUT}}{0.6} - 1 \right)$$

Fig. 4. Application for Adjustable Output Voltage

To enhance output stability, a feed-forward capacitor of 2.2nF to 1μF can be placed in series with VOUT and ADJ. (Refer to "Component Selection" Section)

Auto Discharge Function

The LD39150 provides an auto discharge function that is used for faster discharging of the output capacitor. This function is automatically activated when the EN input goes into an active low state.

Maximum Output Current Capability

The LD39150 can deliver a continuous current of 1.5A over the full operating junction temperature range. However, the output current is limited by the restriction of power dissipation which differs from packages. A heat sink may be required depending on the maximum power dissipation and maximum ambient temperature of application. With respect to the applied package, the maximum output current of 1.5A may be still undeliverable due to the restriction of the power dissipation of LD39150. Under all possible conditions, the junction

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LD39150

temperature must be within the range specified under operating conditions.
The temperatures over the device are given by:

$$\begin{aligned}T_C &= T_A + P_D \times \theta_{CA} \\T_J &= T_C + P_D \times \theta_{JC} \\T_J &= T_A + P_D \times \theta_{JA}\end{aligned}$$

where T_J is the junction temperature, T_C is the case temperature, T_A is the ambient temperature, P_D is the total power dissipation of the device, θ_{CA} is the thermal resistance of case-to-ambient, θ_{JC} is the thermal resistance of junction-to-case, and θ_{JA} is the thermal resistance of junction to ambient.

The total power dissipation of the device is given by:

$$\begin{aligned}P_D &= P_{IN} - P_{OUT} = (V_{IN} \times I_{IN}) - (V_{OUT} \times I_{OUT}) \\&= (V_{IN} \times (I_{OUT} + I_{GND})) - (V_{OUT} \times I_{OUT}) = (V_{IN} - V_{OUT}) \times I_{OUT} + V_{IN} \times I_{GND}\end{aligned}$$

where I_{GND} is the operating ground current of the device which is specified at the Electrical Characteristics. The maximum allowable temperature rise (T_{Rmax}) depends on the maximum ambient temperature (T_{Amax}) of the application, and the maximum allowable junction temperature (T_{Jmax}):

$$T_{Rmax} = T_{Jmax} - T_{Amax}$$

The maximum allowable value for junction-to-ambient thermal resistance, θ_{JA} , can be calculated using the formula:

$$\theta_{JA} = T_{Rmax} / P_D$$

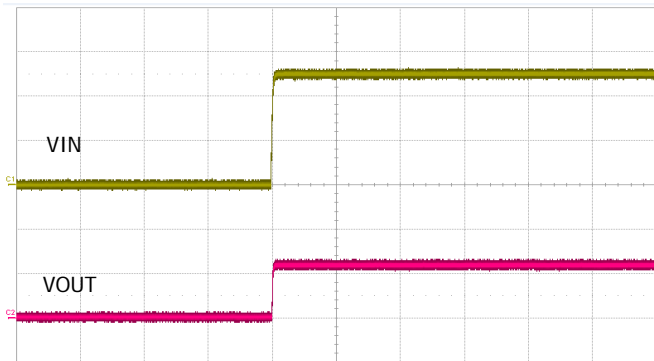
LD39150 is available in TO-252-5L packages. The thermal resistance depends on amount of copper area or heat sink, and on air flow.

If proper cooling solution such as heat sink, copper plane area, or air flow is applied, the maximum allowable power dissipation could be increased. However, if the ambient temperature is increased, the allowable power dissipation would be decreased.

TYPICAL OPERATING CHARACTERISTICS

VOUT = 1.2V (VIN = 2.5V, R1 = 10K Ω , R2 = 10K Ω)

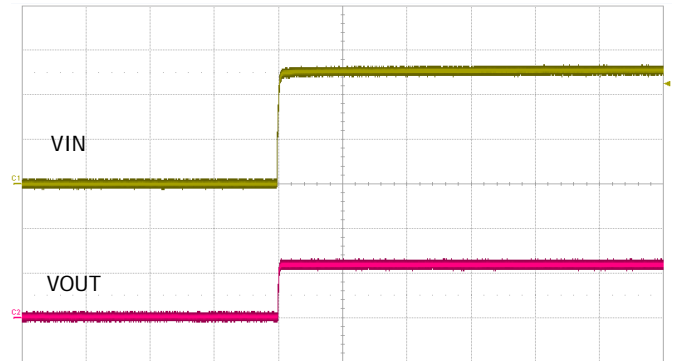
VOUT = 1.2V (Cff = 10nF)



VIN : 1.0V/div, VOUT : 1.0V/div, Time : 10ms/div

Start Up @ Iout=0A

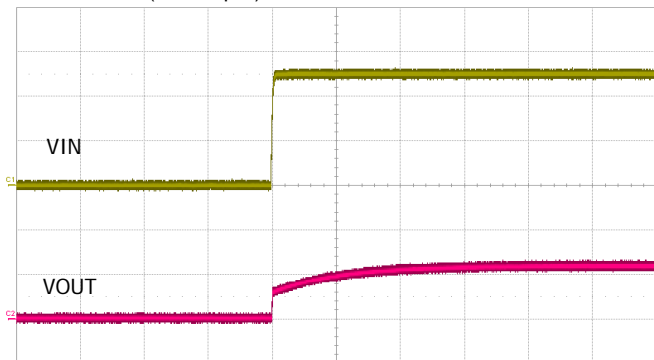
VOUT = 1.2V (Cff = 10nF)



VIN : 1.0V/div, VOUT : 1.0V/div, Time : 10ms/div

Start Up @ Iout=1.5A

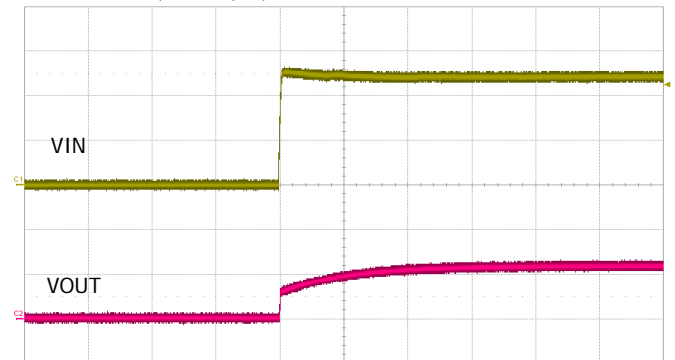
VOUT = 1.2V (Cff = 1 μ F)



VIN : 1.0V/div, VOUT : 1.0V/div, Time : 10ms/div

Start Up @ Iout=0A

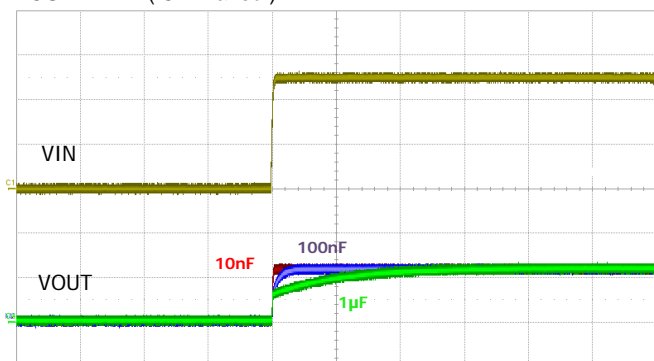
VOUT = 1.2V (Cff = 1 μ F)



VIN : 1.0V/div, VOUT : 1.0V/div, Time : 10ms/div

Start Up @ Iout=1.5A

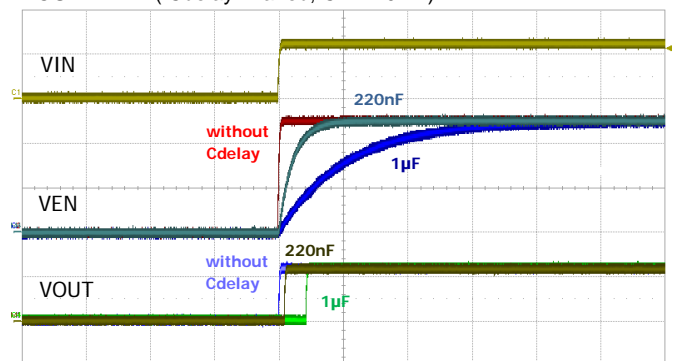
VOUT = 1.2V (Cff : varied)



VIN : 1.0V/div, VOUT : 1.0V/div, Time : 10ms/div

Start Up @ Iout=10mA

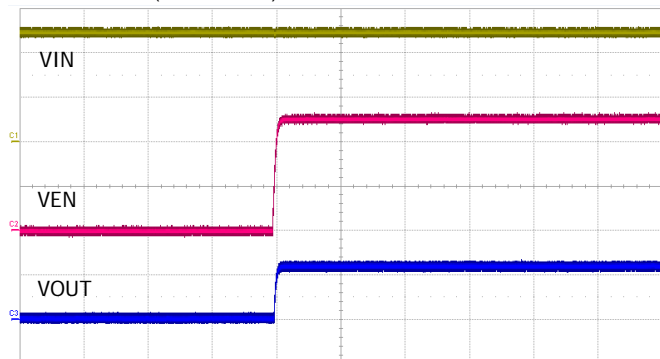
VOUT = 1.2V (Cdelay : varied, Cff = 10nF)



VIN : 2.0V/div, VEN : 1.0V/div, VOUT : 1.0V/div, Time : 10ms/div

Start Up with Cdelay @ Iout=10mA

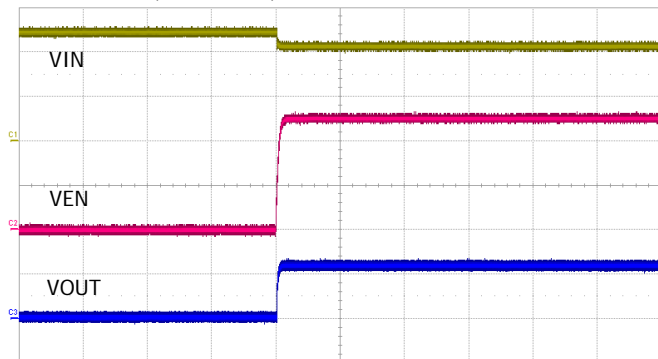
VOUT = 1.2V (Cff = 10nF)



VIN : 1.0V/div, VEN : 1.0V/div, VOUT : 1.0V/div, Time : 5ms/div

Start Up by External VEN @ Iout=0A

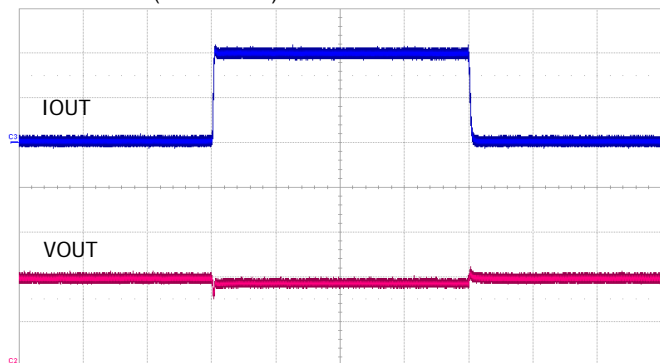
VOUT = 1.2V (Cff = 10nF)



VIN : 1.0V/div, VEN : 1.0V/div, VOUT : 1.0V/div, Time : 5ms/div

Start Up by External VEN @ Iout=1.5A

VOUT = 1.2V (Cff = 10nF)

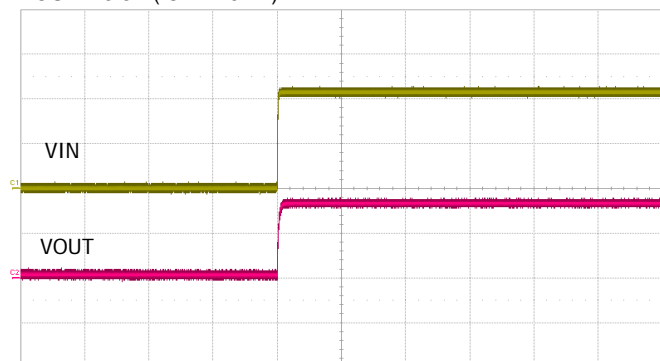


IOUT : 1.0A/div, VOUT : 100mV/div, Time : 500μs/div

Load Transient Response

VOUT = 3.3V (VIN = 4.3V, R1 = 10KΩ, R2 = 45KΩ)

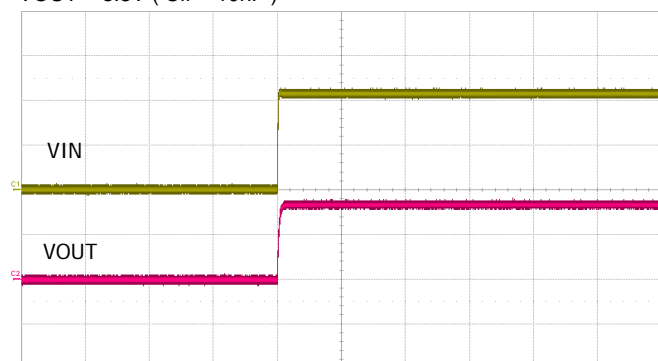
VOUT = 3.3V (Cff = 10nF)



VIN : 2.0V/div, VOUT : 2.0V/div, Time : 20ms/div

Start Up @ Iout=0A

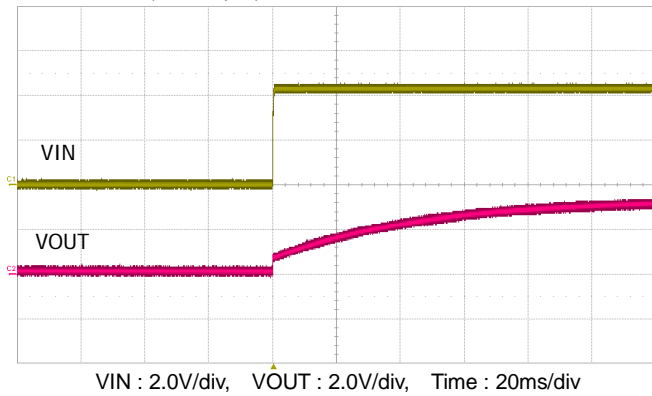
VOUT = 3.3V (Cff = 10nF)



VIN : 2.0V/div, VOUT : 2.0V/div, Time : 20ms/div

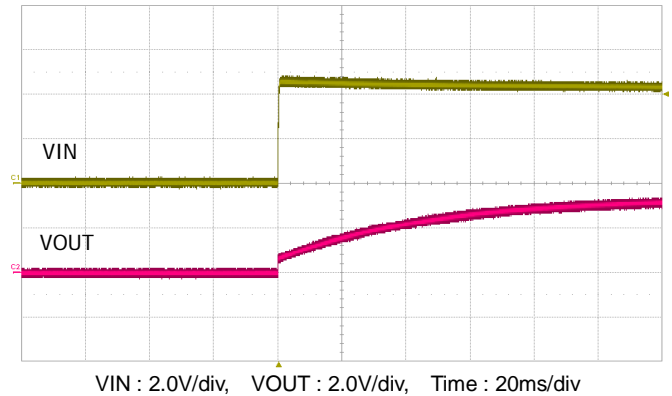
Start Up @ Iout=1.5A

VOUT = 3.3V (Cff = 1 μ F)



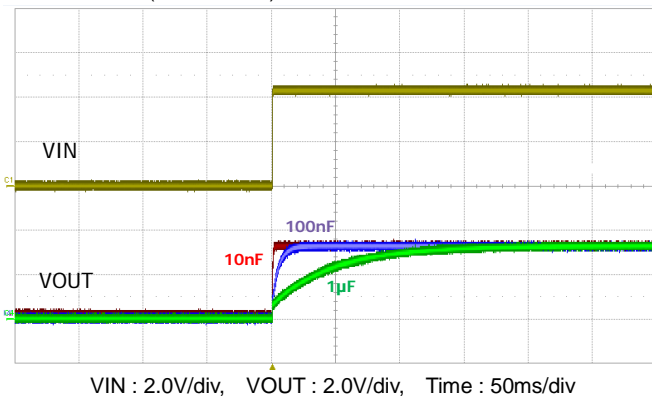
Start Up @ Iout=0A

VOUT = 3.3V (Cff = 1 μ F)



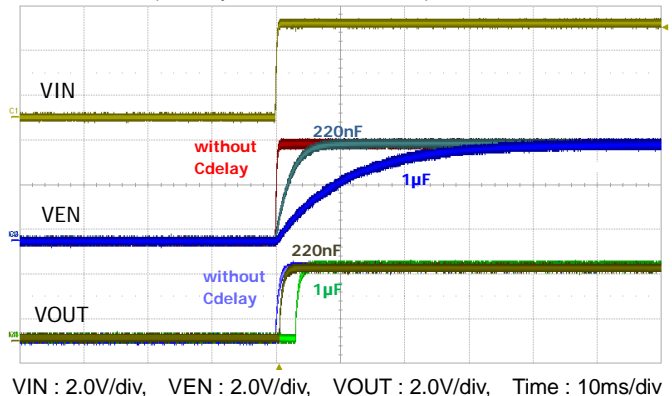
Start Up @ Iout=2A

VOUT = 3.3V (Cff : varied)



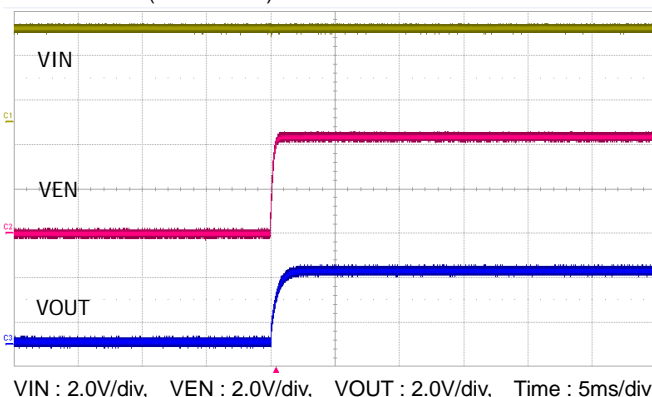
Start Up @ Iout=10mA

VOUT = 3.3V (Cdelay : varied, Cff = 10nF)



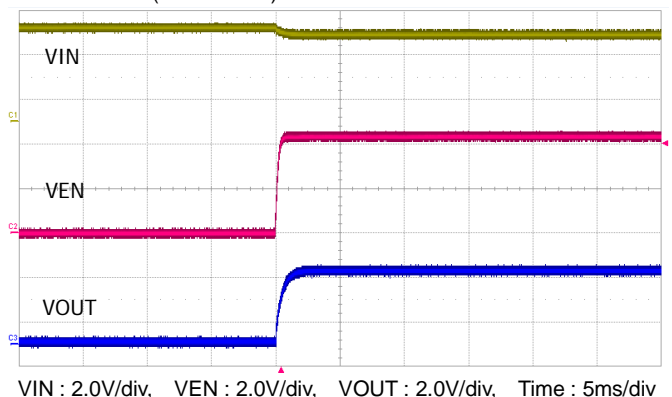
Start Up with Cdelay @ Iout=10mA

VOUT = 3.3V (Cff = 10nF)

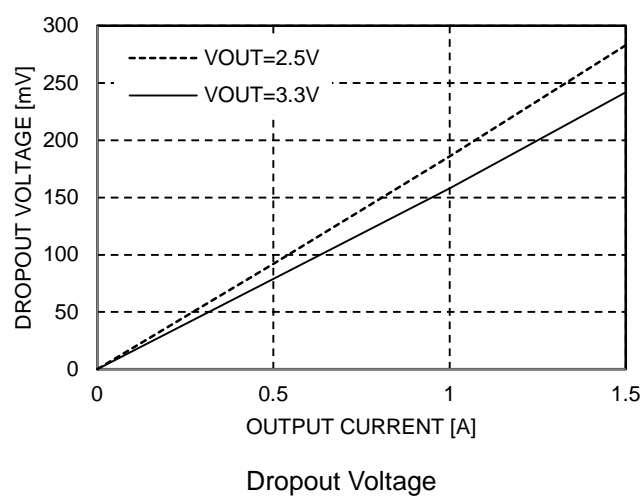
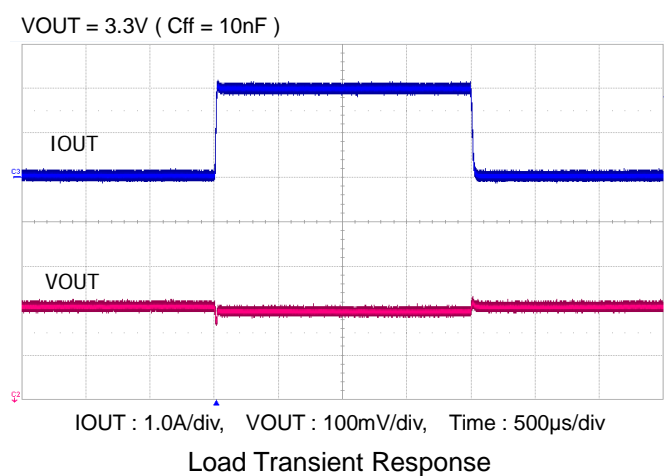


Start Up by External VEN @ Iout=0A

VOUT = 3.3V (Cff = 10nF)



Start Up by External VEN @ Iout=1.5A



REVISION NOTICE

The description in this datasheet is subject to change without notice to describe its electrical characteristics properly.