

Switch, SP3T 100 Watt Reflective 0.03 - 3.0 GHz



MASW-011030

Rev. V4

Features

- Suitable for High Power Military and Civilian Radio Applications
- Power Handling: 100 W @ 85°C
- Insertion Loss: 0.35 dB @ 2 GHz
- Isolation: 40 dB @ 2 GHz
- Surface Mount 7 mm 16-lead HQFN Package
- RoHS* Compliant
- Class 1B HBM ESD Rating

Applications

- High Power Military & Civilian Radio

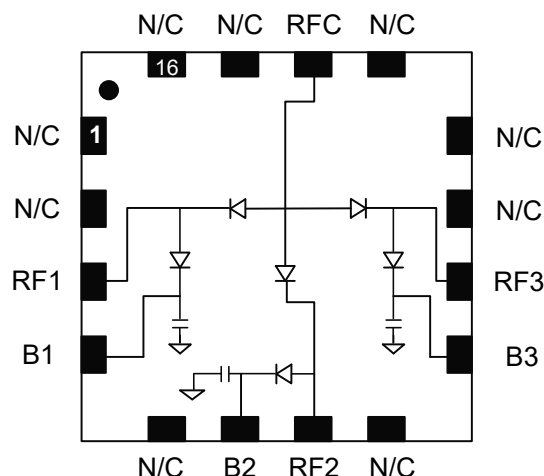
Description

The MASW-011030 is a high power PIN diode SP3T switch in a common anode configuration, operating from 30 MHz to 3 GHz. It features low insertion loss and excellent linearity with low DC consumption. This device is capable of handling 100 Watts CW incident power at a base plate temperature of 85°C.

This high power switch is ideal for use on land mobile radio and MIL-COM applications that require higher CW and pulsed power operation.

The MASW-011030 is manufactured using MACOM's hybrid manufacturing process featuring high voltage PIN diodes and passive devices integrated in a 7 mm HQFN 16-lead plastic package.

Functional Schematic



Pin Configuration

Pin #	Function
1,2,5,8,11,12,13,15,16	No Connection
3	RF1 / V1 Bias
4	B1 Bias
6	B2 Bias
7	RF2 / V2 Bias
9	B3 Bias
10	RF3 / V3 Bias
14	RFC / V4 Bias
Paddle ²	Ground

2. The exposed pad centered on the package bottom must be connected to RF, DC and thermal ground.

Ordering Information¹

Part Number	Package
MASW-011030-14040T	500 piece reel
MASW-011030-001SMB	Sample Board

1. Reference Application Note M513 for reel size information.

* Restrictions on Hazardous Substances, compliant to current RoHS EU directive.

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DC-0006238

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Electrical Specifications: $T_A = 25^\circ\text{C}$, Bias³ = +5 / -5 V, 50 mA / 100 mA

Parameter	Test Conditions	Units	Min.	Typ.	Max.
Insertion Loss $P_{IN} = 0 \text{ dBm}$	0.5 GHz 1.0 GHz 2.0 GHz	dB	—	0.15 0.20 0.35	— 0.35 0.50
Isolation $P_{IN} = 0 \text{ dBm}$	0.5 GHz 1.0 GHz 2.0 GHz	dB	45	50 50 40	—
Input Return Loss	$P_{IN} = 0 \text{ dBm}$	dB	—	>15	—
CW Input Power	25°C base plate, 2.0 GHz 85°C base plate, 2.0 GHz	dBm / W	—	52 / 158 50 / 100	—
P0.1dB	25°C base plate, 2.0 GHz	dBm	—	>52	—
Input IP3	F1 = 2.00 GHz, F2 = 2.01 GHz $P_{IN} = 40 \text{ dBm/Tone}$, 28 V	dBm	—	62	—
RF Switching Speed	(10 - 90% RF Voltage) 1 MHz Rep Rate in Modulating Mode	ns	—	800	—

3. See Bias table.

Bias (+5 V / -5 V)⁴

RF State	V1 Bias (V)	V2 Bias (V)	V3 Bias (V)	B1 Bias (V)	B2 Bias (V)	B3 Bias (V)	V4 Bias (V)
RFC – RF1 Low Loss RFC – RF2 Isolation RFC – RF3 Isolation	-5 V @ 100 mA	+5 V @ 50 mA	+5 V @ 50 mA	0 V	0 V	0 V	0 V
RFC – RF2 Low Loss RFC – RF1 Isolation RFC – RF3 Isolation	+5 V @ 50 mA	-5 V @ 100 mA	+5 V @ 50 mA	0 V	0 V	0 V	0 V
RFC – RF3 Low Loss RFC – RF1 Isolation RFC – RF2 Isolation	+5 V @ 50 mA	+5 V @ 50 mA	-5 V @ 100 mA	0 V	0 V	0 V	0 V

4. DC reverse bias of a PIN Diode operating at a high power is dependent on RF frequency, incident power, and VSWR. See Minimum Reverse DC Voltage table for high power operation.

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Minimum Reverse DC Voltage⁵

Frequency (MHz)	Minimum Reverse DC Voltage
30	-120 V
100	-119 V
200	-114 V
300	-106 V
500	-90 V
1000	-59 V
1500	-43 V
2000	-33 V

5. Required to maintain low loss under 100 W of incident power with 1.5:1 VSWR

Nominal Operating Conditions

Parameter	Value
Forward Current	100 mA
Reverse DC Voltage	-120 V
Operating Temperature	-40°C to +85°C
Storage Temperature	-55°C to +150°C
Junction Temperature	+175°C

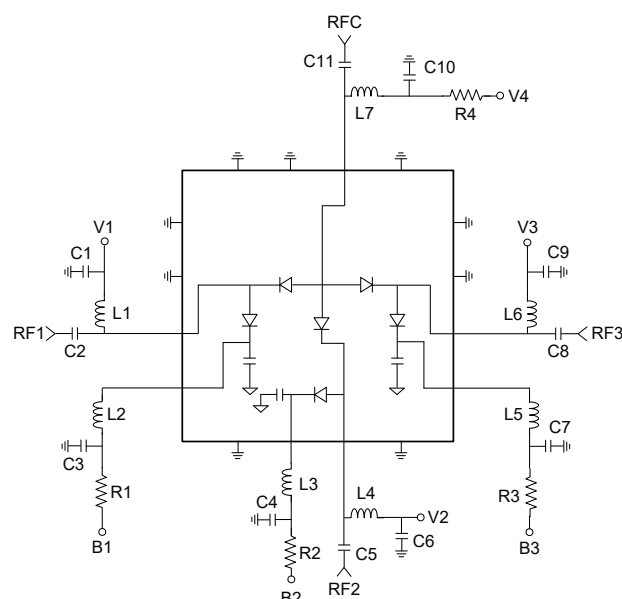
Absolute Maximum Ratings^{6,7}

Parameter	Absolute Maximum
Forward Current	200 mA
Reverse DC Voltage	-150 V
Operating Temperature	-40°C to +85°C
Storage Temperature	-55°C to +150°C
Junction Temperature	+175°C

6. Exceeding any one or combination of these limits may cause permanent damage to this device.

7. MACOM does not recommend sustained operation near these survivability limits.

Application Schematic



Off-Chip Component Values

Component	Operating Frequency		Size
	0.03 - 1.0 GHz	0.5 - 3.0 GHz	
C1, C3, C4, C6, C7, C9, C10	0.1 μ F	270 pF	0603
C2, C5, C8, C11	0.1 μ F	27 pF	0603
L1 - L7	3.3 μ H	82 nH	0603
R1 - R3 ⁸	82 Ω	82 Ω	1210
R4 ⁸	39 Ω	39 Ω	1210

8. Resistance values are used for small signal testing under +5 V / -5 V bias conditions.

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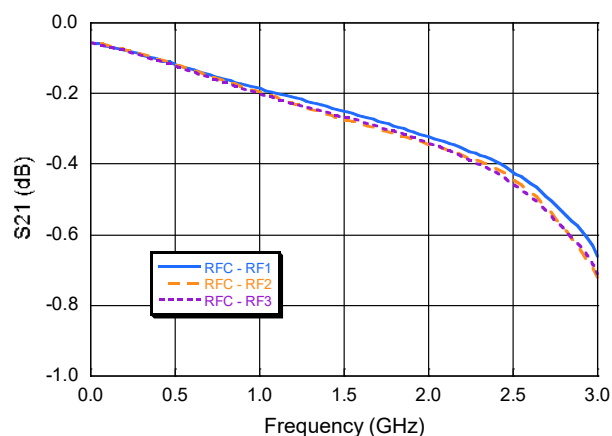


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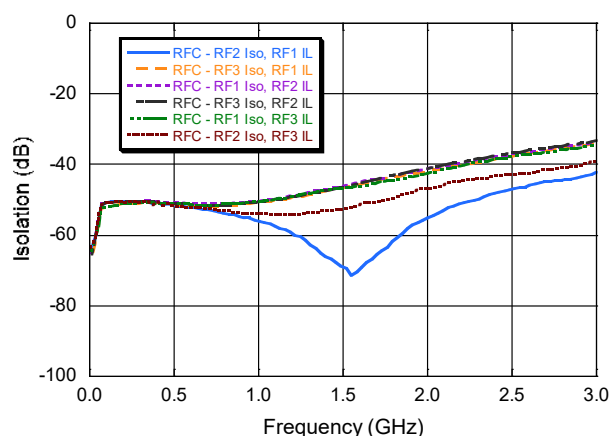
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Typical Performance Curves (using external bias tees):

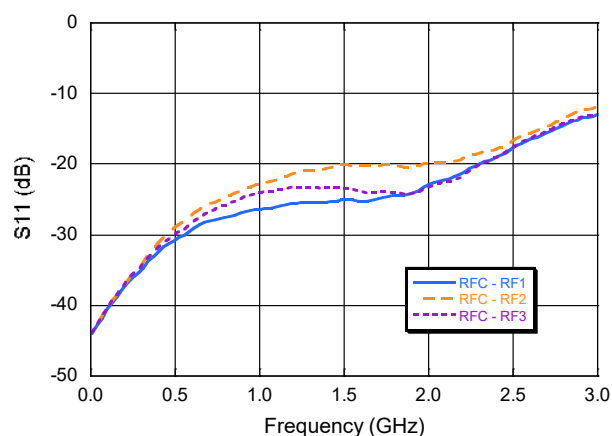
Insertion Loss



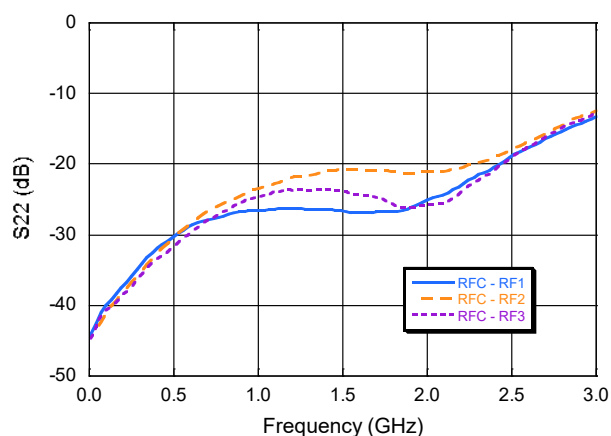
Isolation



Input Return Loss



Output Return Loss



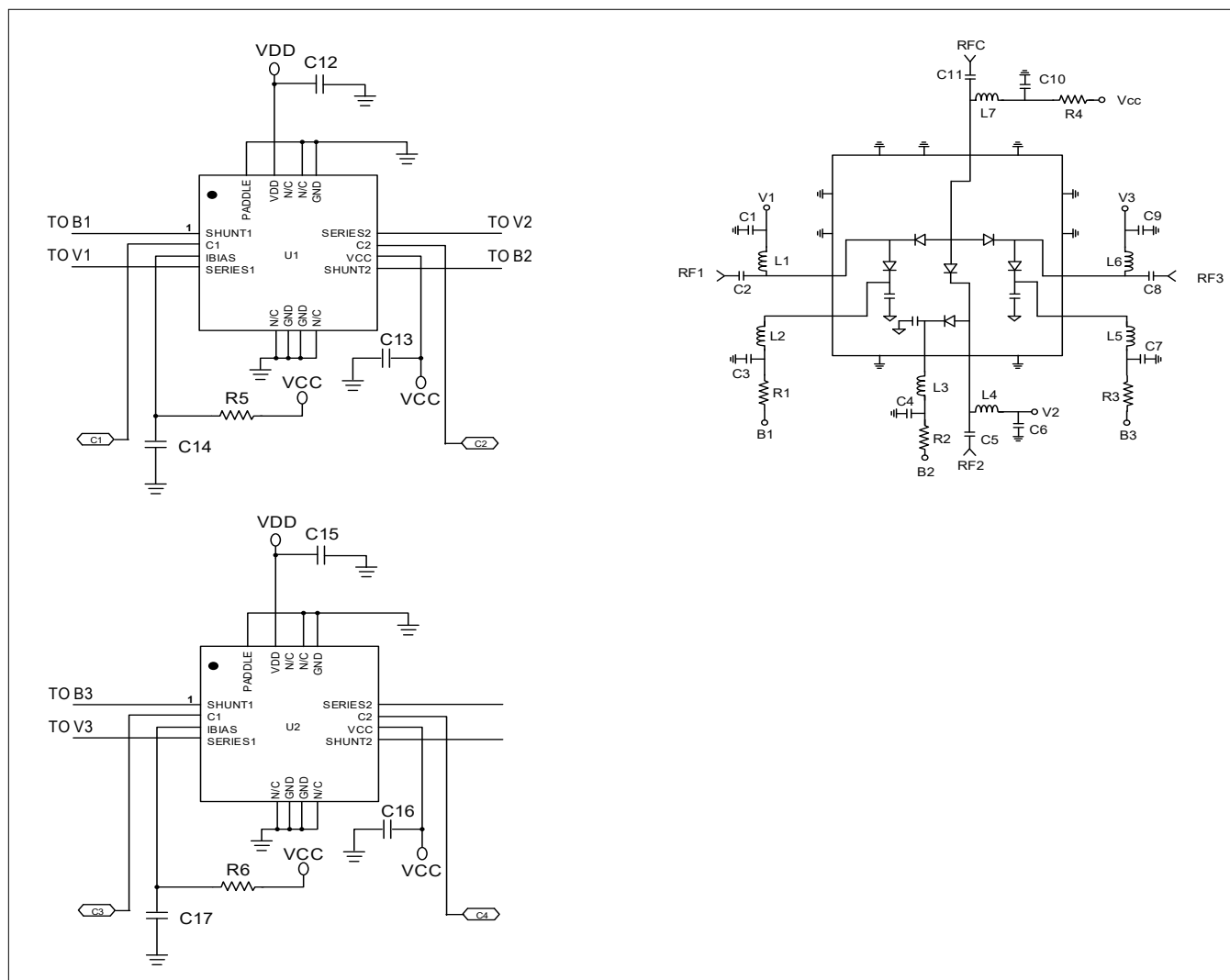
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Application Schematic with MADR-010574 Driver⁹



9. See page 3 for L1 - L7 and C1 - C11 values.

Parts List

Part	Value
C12,C15	0.01 μ F
C13,C14,C16,C17	0.1 μ F
R1 - R3 ¹⁰	5 k Ω
R4	39 Ω
R5,R6	499 k Ω

10. Resistor values calculated to provide ~10 mA of shunt diode bias current given $V_{CC} = 5$ V and $V_{DD} = 50$ V.

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Truth Table and Bias using MADR-010574 Driver ($V_{DD} = 50\text{ V}$, $V_{CC} = 5\text{ V}$)

RF State	C1	C2	C3	C4	V1 (V)	V2 (V)	V3 (V)	B1 (V)	B2 (V)	B3 (V)	V _{CC} (V)
RFC – RF1 Isolation RFC – RF2 Isolation RFC – RF3 Isolation	1	1	1	1	+50 V 10 mA	+50 V 10 mA	+50 V 10 mA	0 V -10 mA	0 V -10 mA	0 V -10 mA	+5 V 0 mA
RFC – RF1 Low Loss RFC – RF2 Isolation RFC – RF3 Isolation	0	1	1	1	0 V -100 mA	+50 V 10 mA	+50 V 10 mA	+50 V 0 mA	0 V -10 mA	0 V -10 mA	+5 V 100 mA
RFC – RF2 Low Loss RFC – RF1 Isolation RFC – RF3 Isolation	1	0	1	1	+50 V 10 mA	0 V -100 mA	+50 V 10 mA	0 V -10 mA	+50 V 0 mA	0 V -10 mA	+5 V 100 mA
RFC – RF3 Low Loss RFC – RF1 Isolation RFC – RF2 Isolation	1	1	0	1	+50 V 10 mA	+50 V 10 mA	0 V -100 mA	0 V -10 mA	0 V -10 mA	+50 V 0 mA	+5 V 100 mA

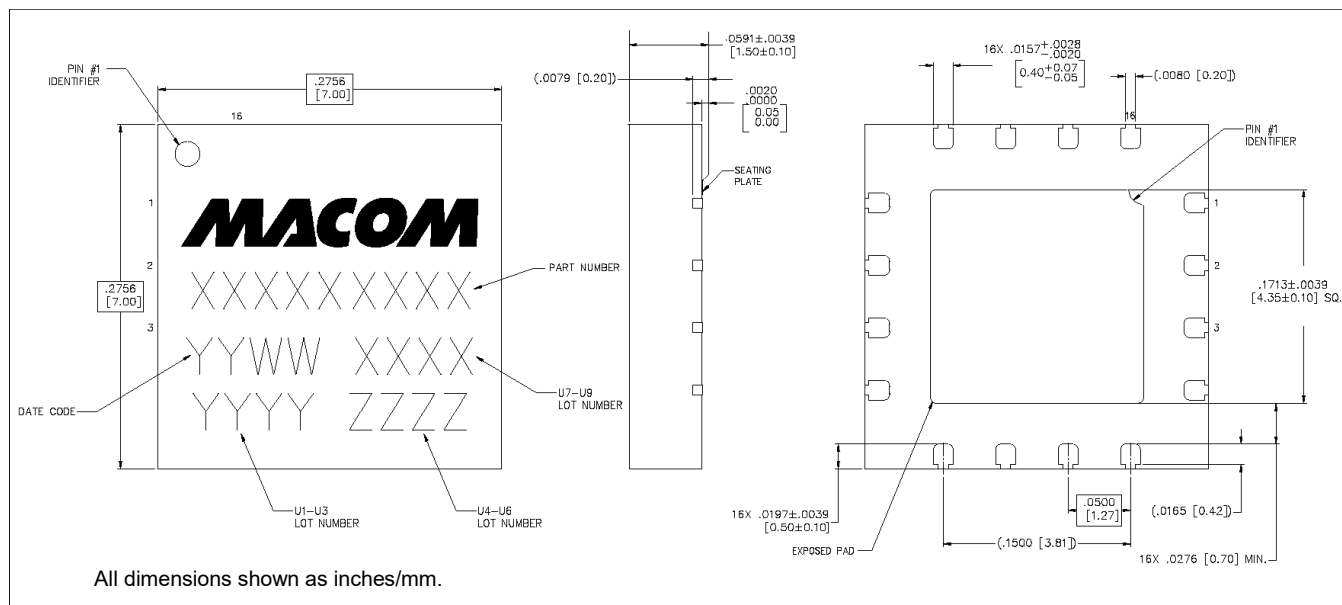
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Lead Free 7 mm 16-Lead HQFN [†]



[†] Reference Application Note S2083 for lead-free solder reflow recommendations.
Meets JEDEC moisture sensitivity level 3 requirements.
Plating is NiPdAuAg.

Handling Procedures

Please observe the following precautions to avoid damage:

Static Sensitivity

These electronic devices are sensitive to electrostatic discharge (ESD) and can be damaged by static electricity. Proper ESD control techniques should be used when handling these HBM Class 1B devices.

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