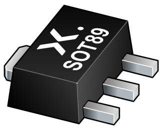

PBSS301PX

12 V, 5.3 A PNP low VCEsat transistor

6 February 2024 Product data sheet

1. General description

PNP low V_{CEsat} transistor in a SOT89 (SC-62/TO-243) small and flat lead Surface-Mounted Device (SMD) plastic package.

NPN complement: PBSS301NX

2. Features and benefits

- Low collector-emitter saturation voltage VCEsat
- High collector current capability IC and ICM
- High collector current gain (hFE) at high IC
- High efficiency due to less heat generation
- Smaller required Printed-Circuit Board (PCB) area than for conventional transistors
- AEC-Q101 qualified

3. Applications

- DC-to-DC conversion
- MOSFET gate driving
- Motor control
- Charging circuits
- Power switches (e.g. motors, fans)

4. Quick reference data

Table 1. Quick reference data

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
V_{CEO}	collector-emitter voltage	open base		-	-	-12	V
I_C	collector current			-	-	-5.3	A
I_{CM}	peak collector current	single pulse; $t_p \leq$ 1 ms		-	-	-10.6	A
R_{CEsat}	collector-emitter saturation resistance	I_C = -4 A; I_B = -200 mA; pulsed; $t_p \leq$ 300 µs; $\delta \leq$ 0.02; T_{amb} = 25 °C		-	28	40	mΩ

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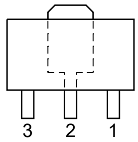
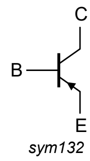
5. Pinning information

Table 2. Pinning information

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	E	emitter	 **SOT89**	
2	C	collector		
3	B	base		

6. Ordering information

Table 3. Ordering information

Type number	Package		
	Name	Description	Version
PBSS301PX	SOT89	plastic, surface-mounted package; 3 leads; 1.5 mm pitch; 4.5 mm x 2.5 mm x 1.5 mm body	SOT89

7. Marking

Table 4. Marking codes

Type number	Marking code[1]
PBSS301PX	%5H

[1] % = placeholder for manufacturing site code

8. Limiting values

Table 5. Limiting values

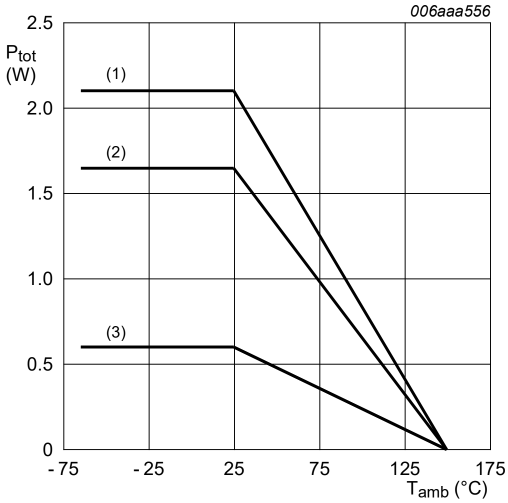
In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions		Min	Max	Unit
V_{CBO}	collector-base voltage	open emitter		-	-12	V
V_{CEO}	collector-emitter voltage	open base		-	-12	V
V_{EBO}	emitter-base voltage	open collector		-	-5	V
I_C	collector current			-	-5.3	A
I_{CM}	peak collector current	single pulse; $t_p \leq$ 1 ms		-	-10.6	A
P_{tot}	total power dissipation	$T_{amb} \leq$ 25 °C	[1]	-	0.6	W
			[2]	-	1.65	W
			[3]	-	2.1	W
T_j	junction temperature			-	150	°C
T_{amb}	ambient temperature			-65	150	°C
T_{stg}	storage temperature			-65	150	°C

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm^2.

[3] Device mounted on a ceramic PCB, Al_2O_3, standard footprint.



(1) Ceramic PCB, Al_2O_3, standard footprint
(2) FR4 PCB, mounting pad for collector 6 cm^2
(3) FR4 PCB, standard footprint

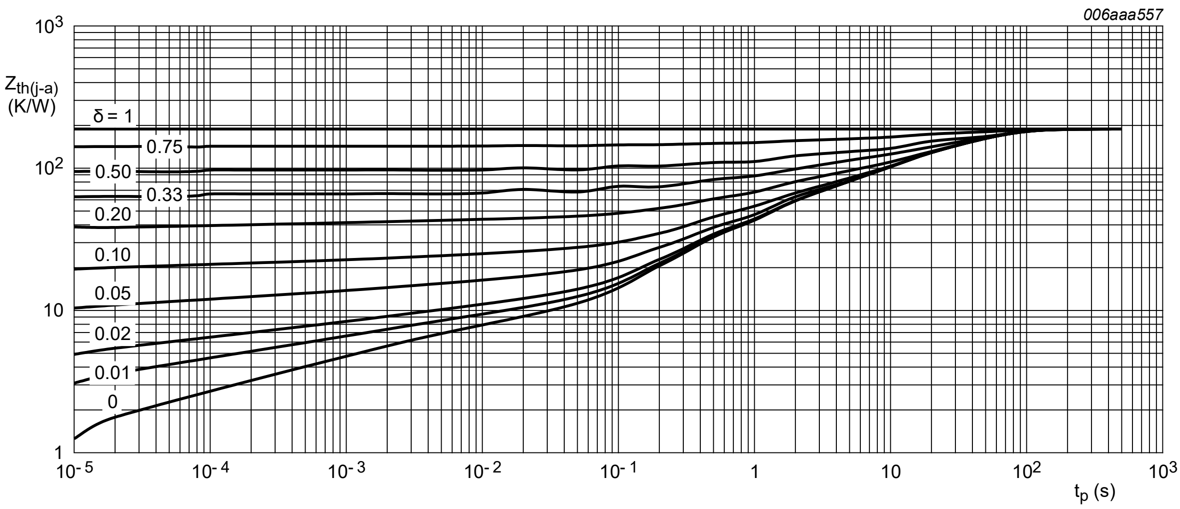
Fig. 1. Power derating curves

9. Thermal characteristics

Table 6. Thermal characteristics

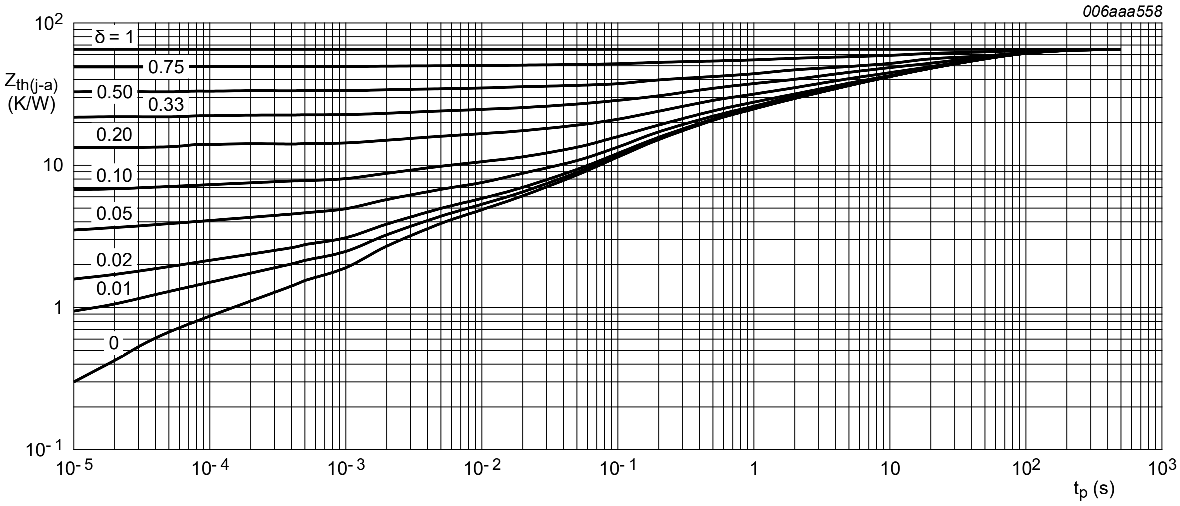
Symbol	Parameter	Conditions		Min	Typ	Max	Unit
$R_{th(j-a)}$	thermal resistance from junction to ambient	in free air	[1]	-	-	208	K/W
			[2]	-	-	76	K/W
			[3]	-	-	60	K/W
$R_{th(j-sp)}$	thermal resistance from junction to solder point			-	-	20	K/W

[1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 6 cm^2.
[3] Device mounted on a ceramic PCB, Al_2O_3, standard footprint.



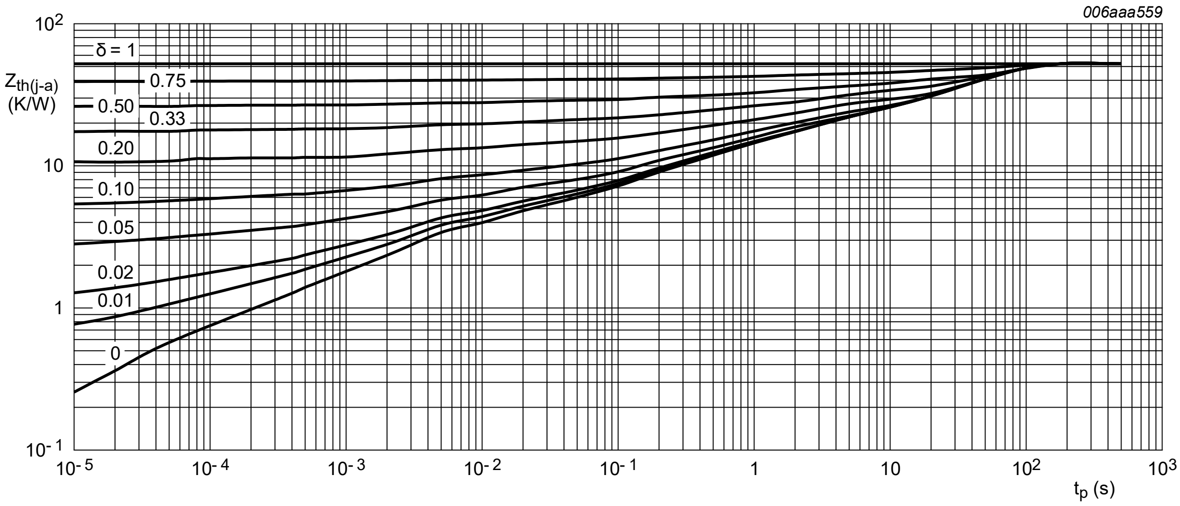
FR4 PCB, standard footprint

Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, mounting pad for collector 6 cm^2

Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



Ceramic PCB, Al_2O_3, standard footprint

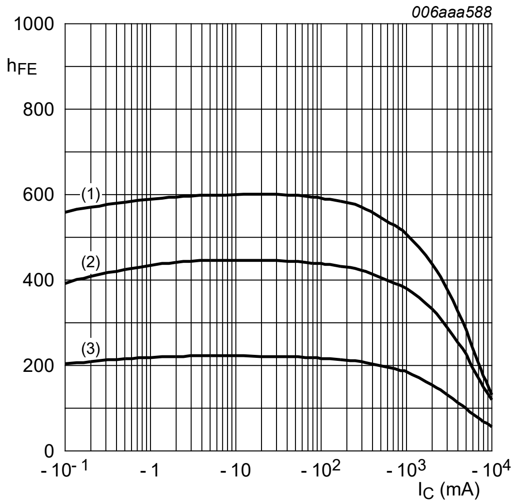
Fig. 4. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

10. Characteristics

Table 7. Characteristics

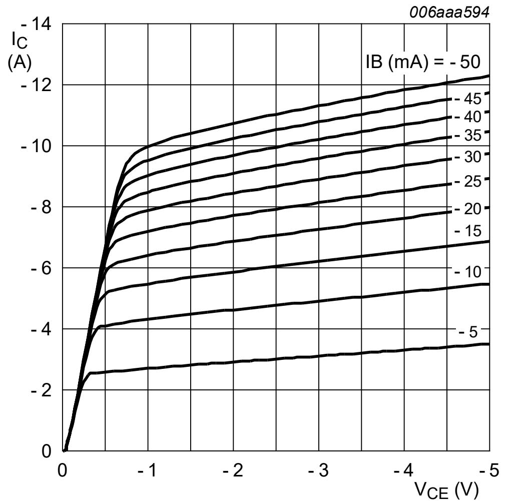
Symbol	Parameter	Conditions		Min	Typ	Max	Unit
I_{CBO}	collector-base cut-off current	V_{CB} = -12 V; I_E = 0 A; T_{amb} = 25 °C		-	-	-100	nA
		V_{CB} = -12 V; I_E = 0 A; T_j = 150 °C		-	-	-50	µA
I_{EBO}	emitter-base cut-off current	V_{EB} = -5 V; I_C = 0 A; T_{amb} = 25 °C		-	-	-100	nA
h_{FE}	DC current gain	V_{CE} = -2 V; I_C = -0.5 A; pulsed; t_p ≤ 300 µs; δ ≤ 0.02; T_{amb} = 25 °C		250	400	-	
		V_{CE} = -2 V; I_C = -1 A; pulsed; t_p ≤ 300 µs; δ ≤ 0.02; T_{amb} = 25 °C		250	380	-	
		V_{CE} = -2 V; I_C = -2 A; pulsed; t_p ≤ 300 µs; δ ≤ 0.02; T_{amb} = 25 °C		200	335	-	
		V_{CE} = -2 V; I_C = -4 A; pulsed; t_p ≤ 300 µs; δ ≤ 0.02; T_{amb} = 25 °C		150	250	-	
		V_{CE} = -2 V; I_C = -6 A; pulsed; t_p ≤ 300 µs; δ ≤ 0.02; T_{amb} = 25 °C		130	200	-	
V_{CEsat}	collector-emitter saturation voltage	I_C = -0.5 A; I_B = -50 mA; pulsed; t_p ≤ 300 µs; δ ≤ 0.02; T_{amb} = 25 °C		-	-20	-30	mV
		I_C = -1 A; I_B = -50 mA; pulsed; t_p ≤ 300 µs; δ ≤ 0.02; T_{amb} = 25 °C		-	-40	-60	mV
		I_C = -1 A; I_B = -10 mA; pulsed; t_p ≤ 300 µs; δ ≤ 0.02; T_{amb} = 25 °C		-	-60	-90	mV
		I_C = -2 A; I_B = -40 mA; pulsed; t_p ≤ 300 µs; δ ≤ 0.02; T_{amb} = 25 °C		-	-70	-100	mV
		I_C = -4 A; I_B = -200 mA; pulsed; t_p ≤ 300 µs; δ ≤ 0.02; T_{amb} = 25 °C		-	-115	-160	mV
		I_C = -4 A; I_B = -400 mA; pulsed; t_p ≤ 300 µs; δ ≤ 0.02; T_{amb} = 25 °C		-	-110	-155	mV
		I_C = -4 A; I_B = -40 mA; pulsed; t_p ≤ 300 µs; δ ≤ 0.02; T_{amb} = 25 °C		-	-150	-240	mV
		I_C = -5.3 A; I_B = -265 mA; pulsed; t_p ≤ 300 µs; δ ≤ 0.02; T_{amb} = 25 °C		-	-145	-210	mV
R_{CEsat}	collector-emitter saturation resistance	I_C = -4 A; I_B = -200 mA; pulsed; t_p ≤ 300 µs; δ ≤ 0.02; T_{amb} = 25 °C		-	28	40	mΩ
		I_C = -4 A; I_B = -40 mA; pulsed; t_p ≤ 300 µs; δ ≤ 0.02; T_{amb} = 25 °C		-	38	60	mΩ
V_{BEsat}	base-emitter saturation voltage	I_C = -1 A; I_B = -100 mA; pulsed; t_p ≤ 300 µs; δ ≤ 0.02; T_{amb} = 25 °C		-	-0.82	-0.9	V
		I_C = -4 A; I_B = -400 mA; pulsed; t_p ≤ 300 µs; δ ≤ 0.02; T_{amb} = 25 °C		-	-0.93	-1.05	V
V_{BEon}	base-emitter turn-on voltage	V_{CE} = -2 V; I_C = -2 A; pulsed; t_p ≤ 300 µs; δ ≤ 0.02; T_{amb} = 25 °C		-	-0.76	-0.85	V
t_d	delay time	V_{CC} = -12.5 V; I_C = -3 A; I_{Bon} = -0.15 A; I_{Boff} = 0.15 A; T_{amb} = 25 °C		-	10	-	ns
t_r	rise time			-	55	-	ns
t_{on}	turn-on time			-	65	-	ns
t_s	storage time			-	165	-	ns
t_f	fall time			-	160	-	ns
t_{off}	turn-off time			-	325	-	ns

Symbol	Parameter	Conditions		Min	Typ	Max	Unit
f_T	transition frequency	V_{CE} = -10 V; I_C = -0.1 A; f = 100 MHz; T_{amb} = 25 °C		-	140	-	MHz
C_c	collector capacitance	V_{CB} = -10 V; I_E = 0 A; i_e = 0 A; f = 1 MHz; T_{amb} = 25 °C		-	190	250	pF



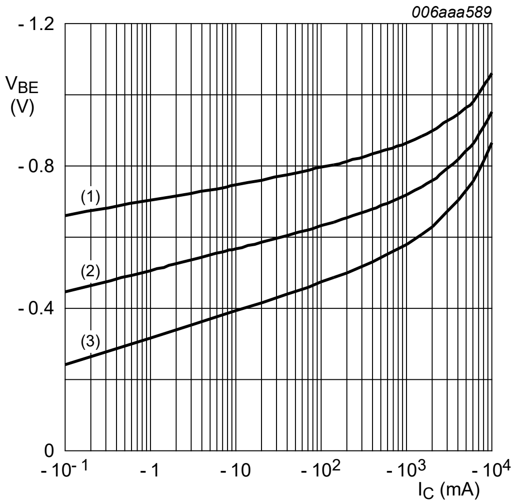
V_{CE} = −2 V
(1) T_{amb} = 100 °C
(2) T_{amb} = 25 °C
(3) T_{amb} = −55 °C

Fig. 5. DC current gain as a function of collector current; typical values



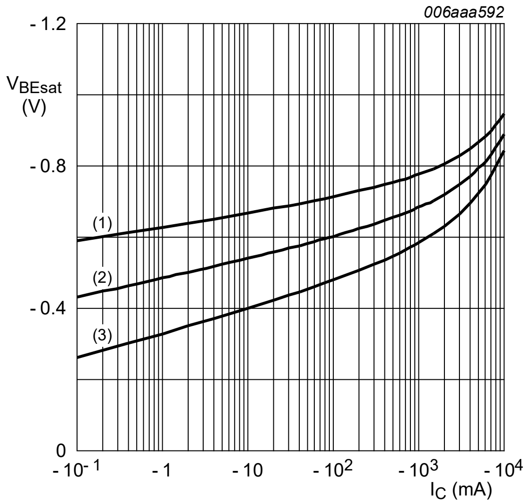
T_{amb} = 25 °C

Fig. 6. Collector current as a function of collector-emitter voltage; typical values



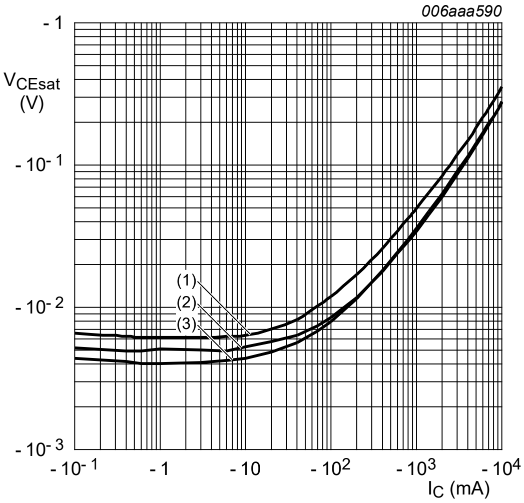
V_{CE} = −2 V
(1) T_{amb} = −55 °C
(2) T_{amb} = 25 °C
(3) T_{amb} = 100 °C

Fig. 7. Base-emitter voltage as a function of collector current; typical values



I_C/I_B = 20
(1) T_{amb} = −55 °C
(2) T_{amb} = 25 °C
(3) T_{amb} = 100 °C

Fig. 8. Base-emitter saturation voltage as a function of collector current; typical values



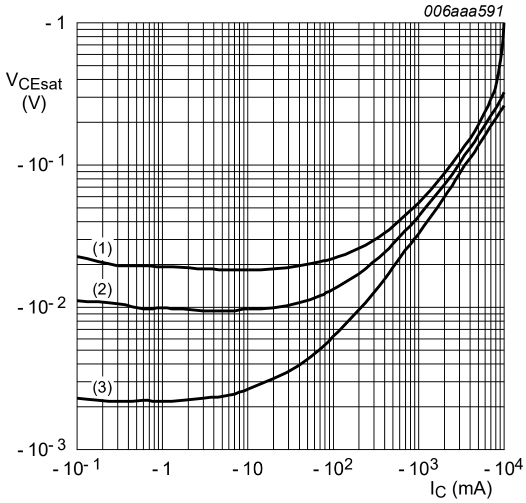
$I_C/I_B = 20$

(1) $T_{amb} = 100$ °C

(2) $T_{amb} = 25$ °C

(3) $T_{amb} = -55$ °C

Fig. 9. Collector-emitter saturation voltage as a function of collector current; typical values



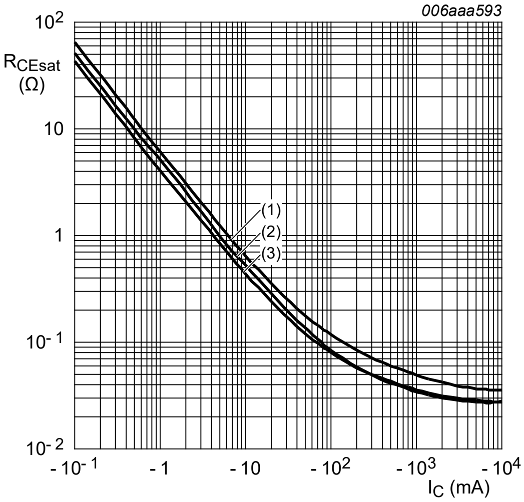
$T_{amb} = 25$ °C

(1) $I_C/I_B = 100$

(2) $I_C/I_B = 50$

(3) $I_C/I_B = 10$

Fig. 10. Collector-emitter saturation voltage as a function of collector current; typical values



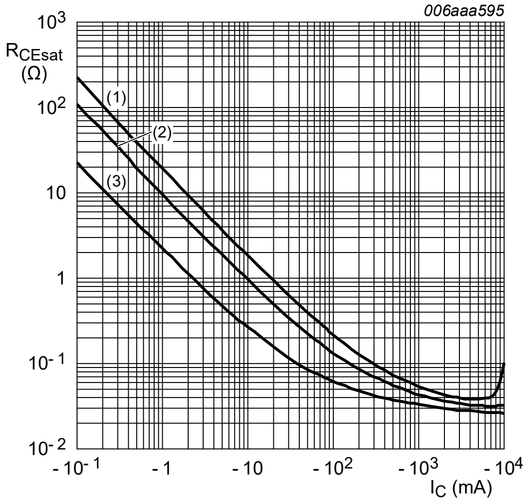
$I_C/I_B = 20$

(1) $T_{amb} = 100$ °C

(2) $T_{amb} = 25$ °C

(3) $T_{amb} = -55$ °C

Fig. 11. Collector-emitter saturation resistance as a function of collector current; typical values



$T_{amb} = 25$ °C

(1) $I_C/I_B = 100$

(2) $I_C/I_B = 50$

(3) $I_C/I_B = 10$

Fig. 12. Collector-emitter saturation resistance as a function of collector current; typical values

11. Test information

Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101 - Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline

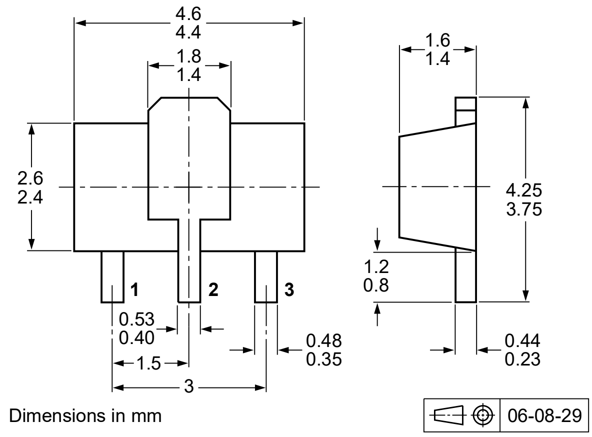


Fig. 13. Package outline SOT89

13. Soldering

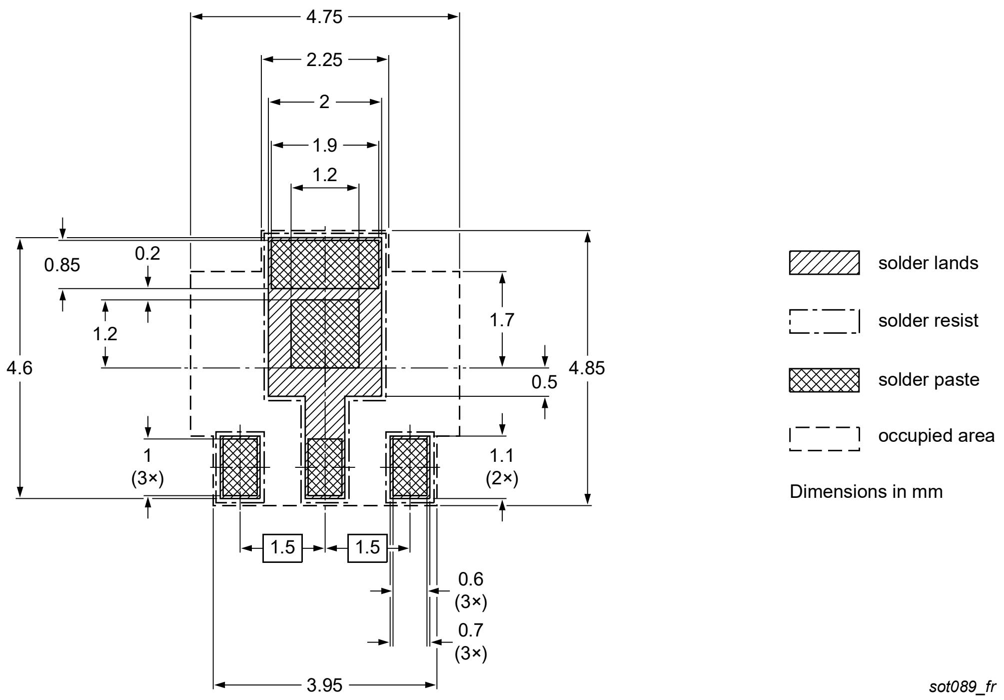


Fig. 14. Reflow soldering footprint for SOT89

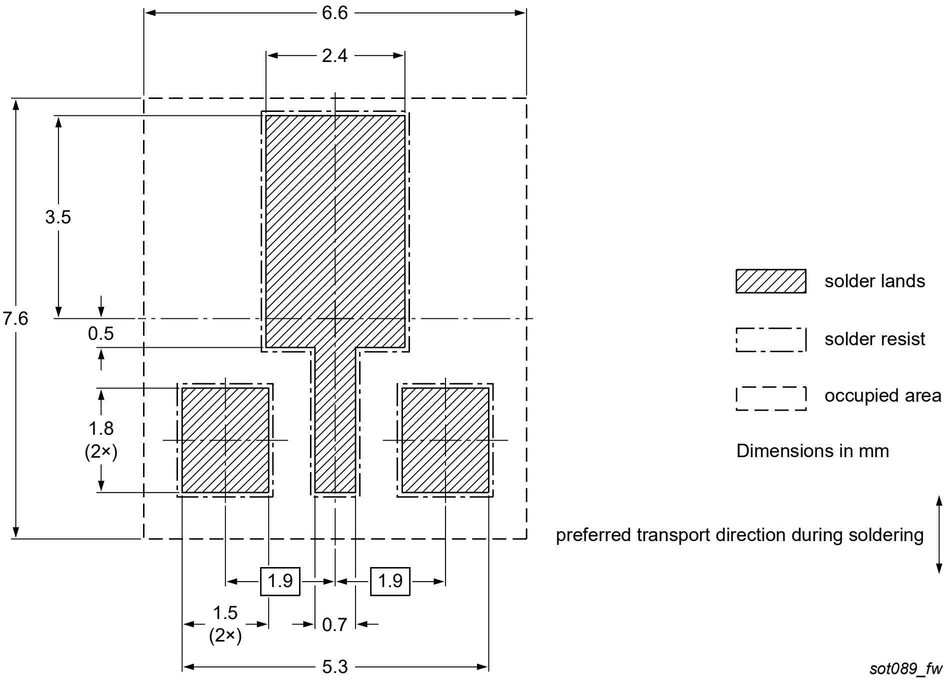


Fig. 15. Wave soldering footprint for SOT89

14. Revision history

Table 8. Revision history

Data sheet ID	Release date	Data sheet status	Change notice	Supersedes
PBSS301PX v.3	20240206	Product data sheet	-	PBSS301PX v.2
Modifications:	• The format of this data sheet has been redesigned to comply with the identity guidelines of Nexperia. • Legal texts have been adapted to the new company name where appropriate. • Section "Packing information" removed.			
PBSS301PX v.2	20091117	Product data sheet	-	PBSS301PX v.1
PBSS301PX v.1	20060821	Product data sheet	-	-

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.
[2] The term 'short data sheet' is explained in section "Definitions".
[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at https://www.nexperia.com.

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Contents

Date of release: 6 February 2024